



Product Change Notification / SYST-13ZPMP757

Date:

14-Sep-2021

Product Category:

Microprocessors

PCN Type:

Document Change

Notification Subject:

Data Sheet - SAM9X60 Datasheet Revision

Affected CPNs:

[SYST-13ZPMP757_Affected_CPN_09142021.pdf](#)

[SYST-13ZPMP757_Affected_CPN_09142021.csv](#)

Notification Text:

SYST-13ZPMP757

Microchip has released a new Product Documents for the SAM9X60 Datasheet of devices. If you are using one of these devices please read the document located at [SAM9X60 Datasheet](#).

Notification Status: Final

Description of Change: Revision includes:

1. Debug Unit (DBGU) - DBGU_CIDR: added new reset value (0x819835A2)
2. Power Management Controller (PMC):
 - Divider and Phase Lock loop Programming and PMC_PII_ACR: replaced PMC_PLL_ACR recommended values by references to the Electrical Characteristics section
 - PMC_PII_CTRL0: updated ENPLICK bit description
3. Electrical Characteristics - EMAC Timings: added section Ethernet MAC MDIO Interface Timings; removed "Ethernet MAC Signals Relative to MDC" table and "Minimum and Maximum Access Times of Ethernet MAC Output Signals" figure

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 14 Sept 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAM9X60 Datasheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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