

<b>Title of Change:</b>	Trench 6 Technology Capacity Expansion by Qualification of Aizu Fujitsu Semiconductor Manufacturing, Japan.
<b>Proposed Changed Material First Ship Date:</b>	27 April 2018 or earlier upon customer approval.
<b>Current Material Last Order Date:</b>	N/A
<b>Current Material Last Delivery Date:</b>	N/A
<b>Product Category:</b>	Active components – Discrete components
<b>Contact information</b>	Contact your local ON Semiconductor Sales.
<b>Samples</b>	Contact your local ON Semiconductor Sales Office or < <a href="mailto:Cheryl.Nudo@onsemi.com">Cheryl.Nudo@onsemi.com</a> > Sample requests are to be submitted no later than 45 days after publication of this change notification.
<b>Sample Availability Date:</b>	1 June 2017
<b>PPAP Availability Date:</b>	27 May 2017
<b>Additional Reliability Data</b>	Contact your local ON Semiconductor Sales Office or < <a href="mailto:Don.Knudsen@onsemi.com">Don.Knudsen@onsemi.com</a> >
<b>Type of Notification</b>	This is a Final Product/Process Change Notification (FPCN) sent to customers. FPCNs are issued 12 months prior to implementation of the change or earlier upon customer approval. ON Semiconductor will consider this proposed change and its conditions acceptable, unless an inquiry is made in writing within 45 days of delivery of this notice. To do so, contact < <a href="mailto:PCN.Support@onsemi.com">PCN.Support@onsemi.com</a> >.
<b>Change Category:</b>	<b>Type of Change</b>
Process – Wafer Production	Move of all or part of wafer fab to a different location/site/subcontractor
Process – Wafer Production	Process integrity: tuning within specification
Equipment	Production from a new equipment/tool which uses the same basic technology (replacement equipment or extension of existing equipment pool) without change of process.
<b>Description and Purpose:</b>	
This is a final change notification to customers on the qualification of additional wafer fabrication capacity of 40V and 60V Trench (T6) MOSFET technology in Aizu Fujitsu Semiconductor Manufacturing (AFSM) located in Aizu, Japan. At the expiration of this notification, all products listed here will be dual sourced from its current ON Semiconductor wafer fab in Gresham and AFSM.	
<b>Reason / Motivation for Change:</b>	Change benefits for customer: FAB capacity expansion to meet customer demand Quality improvement: No change Risk for late release for customer: Failure to approve will expose the possible risk of not getting all the required products or cause an extended lead time to receive the products
<b>Anticipated impact on fit, form, function, reliability, product safety or manufacturability</b>	The device has been qualified and validated based on the same Product Specification. The device has successfully passed the qualification tests. Potential impacts can be identified, but due to testing performed by ON Semiconductor in relation to the PCN, associated risks are verified and excluded.  No anticipated impacts.
<b>Sites Affected:</b>	
<input type="checkbox"/> All site(s) <input type="checkbox"/> not applicable <input type="checkbox"/> ON Semiconductor site(s) : <input checked="" type="checkbox"/> External Foundry/Subcon site(s) <b>FUJITSU SEMICONDUCTOR LTD.</b>	

<b>Marking of Parts/ Traceability of Change:</b>	Affected products will be identified with date code.																																																				
<b>Reliability Data Summary:</b>																																																					
<b>NVMFS5C604NLT1G (60V LL)</b>																																																					
<b>Package: S08FL HEFET</b>																																																					
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**Note: AEC-1pager is attached.**

To access file attachments on pdf copy of PCN, please be guided by the steps below:

1. Download pdf copy of the PCN to your computer
2. Open the downloaded pdf copy of the PCN
3. Click on the paper clip icon available on the menu provided in the left/bottom portion of the screen to reveal the Attachment field
4. Then click on the attached file/

**Electrical Characteristic Summary:**

Electrical characteristics are not impacted.

**List of Affected Standard Parts**

Current Part Number	Qualification Vehicle
NVMFS5C604NLT1G	
NVMFS5C604NLT3G	
NVMFS5C604NLWFT1G	
NVMFS5C604NLWFT3G	
NVMFS5C612NLT1G	
NVMFS5C612NLT3G	
NVMFS5C612NLWFT1G	
NVMFS5C612NLWFT3G	
NVMFS5C628NLT1G	
NVMFS5C628NLT3G	
NVMFS5C628NLWFT1G	
NVMFS5C628NLWFT3G	
NVMFS5C645NLT1G	
NVMFS5C645NLT3G	
NVMFS5C645NLWFT1G	NVMFS5C604NLT1G
NVMFS5C645NLWFT3G	
NVMFS5C646NLT1G	
NVMFS5C646NLT3G	
NVMFS5C646NLWFT1G	
NVMFS5C646NLWFT3G	
NVMFS5C670NLT1G	
NVMFS5C670NLT3G	
NVMFS5C670NLWFT1G	
NVMFS5C670NLWFT3G	
NVMFS5C673NLT1G	
NVMFS5C673NLT3G	
NVMFS5C673NLWFT1G	
NVMFS5C673NLWFT3G	
NVMFS5C682NLT1G	



NVMFS5C682NLT3G	
NVMFS5C682NLWFT1G	
NVMFS5C682NLWFT3G	
NVMFS5C404NLT1G	
NVMFS5C404NLT3G	
NVMFS5C404NLWFT1G	
NVMFS5C404NLWFT3G	
NVMFS5C410NLT1G	
NVMFS5C410NLT3G	
NVMFS5C410NLWFT1G	
NVMFS5C410NLWFT3G	
NVMFS5C423NLT1G	
NVMFS5C423NLT3G	
NVMFS5C423NLWFT1G	
NVMFS5C423NLWFT3G	
NVMFS5C430NLT1G	
NVMFS5C430NLT3G	
NVMFS5C430NLWFT1G	
NVMFS5C430NLWFT3G	
NVMFS5C442NLT1G	
NVMFS5C442NLT3G	
NVMFS5C442NLWFT1G	
NVMFS5C442NLWFT3G	
NVMFS5C450NLT1G	
NVMFS5C450NLT3G	
NVMFS5C450NLWFT1G	
NVMFS5C450NLWFT3G	
NVMFS5C456NLT1G	
NVMFS5C456NLT3G	
NVMFS5C456NLWFT1G	
NVMFS5C456NLWFT3G	
NVMFS5C460NLT1G	
NVMFS5C460NLT3G	
NVMFS5C460NLWFT1G	
NVMFS5C460NLWFT3G	
NVMFS5C468NLT1G	
NVMFS5C468NLT3G	
NVMFS5C468NLWFT1G	
NVMFS5C468NLWFT3G	



## Final Product/Process Change Notification

Document # : FPCN20937Z

Issue Date: 27 April 2017

NVMFS5C404NT1G	
NVMFS5C404NT3G	
NVMFS5C404NWFT1G	
NVMFS5C404NWFT3G	
NVMFS5C410NT1G	
NVMFS5C410NT3G	
NVMFS5C410NWFT1G	
NVMFS5C410NWFT3G	
NVMFS5C426NT1G	
NVMFS5C426NT3G	
NVMFS5C426NWFT1G	
NVMFS5C426NWFT3G	
NVMFS5C430NT1G	
NVMFS5C430NT3G	
NVMFS5C430NWFT1G	
NVMFS5C430NWFT3G	
NVMFS5C442NT1G	
NVMFS5C442NT3G	
NVMFS5C442NWFT1G	
NVMFS5C442NWFT3G	
NVMFS5C450NT1G	
NVMFS5C450NT3G	
NVMFS5C450NWFT1G	
NVMFS5C450NWFT3G	

NVMFS5C604NLT1G  
NVMFS5C404NT1G

## AEC Q101 Discrete Device Semiconductor Component Qualification Summary

Supplier: ON Semiconductor	Customer PN:
Supplier PN: NVMFS5C410NLT1G	General Specification: AEC - Q101
Product Description: 40V N-Ch LL Power MOSFET	Q006
Supplier Fab Info: AFSM, Japan	Reliability Report Date: Jan-17
Supplier Assy Info: Assy Seremban, Malaysia	Family Type: Power MOSFET, S08FL
Reason for Qual: Qual Vehicle S08FL	

#	Test	Stress Name	Reference	Test Conditions	Comments	# Lots	S.S.	Results Fail/Total	Comments/ Test Results
1	TEST	Pre and post stress electrical test	Device specification			All	All	0 Fails	
2	PC	Preconditioning	JEDS22 A113	Performed on surface mount devices prior to TC, AC, H3TRB or HAST, and IOL.	MSL1 @ 260°C temp	3	336	0 Fails	
3	EV	External Visual	JEDS22 B101	Device construction, marking, and workmanship		3	77	0 Fails	
4	PV	Parametric Verification	Device specification	Tested to device specification requirements		3	25	0 Fails	
5	HTRB	High Temperature Reverse Bias	MILSTD750-1 method M1038A	Tj= max, V=100% rated V, 1008 Hrs		3	84	0 Fails	JESD22 A108
5a	ACBV	AC Blocking Voltage	MILSTD750-1 M1040 A	Tj= max, AC blocking V = max, 1000 Hrs		3	84		Thyristors only
5b	HTFB	High Temperature forward Bias	JESD22 A108	Ta= , V=100% rated forward V, 1000 Hrs		3	84		LEDs Only
5c	SSOP	Steady State Operational Life	MILSTD750-1 method M1038B	Tj= max, V=100% rated IZ max, 1000 Hrs		3	84		Zeners only
6	HTGB	High Temperature Gate Bias	JESD22 A108	Ta= max, Vgs=100%, 1008 Hrs		3	84	0 Fails	
	HTSL	High Temperature Storage Life	JESD22 A103	Ta= max, 1000 Hrs, 1008 Hrs	Q006 - NVMFS5C604NLT1G	3	84	0 Fails	Cross section 1X and 2X Complete/Pass.
7	TC	Temperature Cycling	JESD22 A104; Q101 appendix 6	-55°C to +150°C, t(dwell>15 min, 1000 Cycles	Board mounted Q006 - NVMFS5C604NLT1G	3	84	0 Fails	SAT, Wire pull, cross section, ball shear post stress - Complete/PASS
7a	TCHT	Temperature Cycling Hot Test	JESD22 A104; Q101 appendix 6	125°C Hot Test after TC, followed by wire pull on all wires from 5 part		3	84		Hermetic Packages only
7a alt	TCDT	Temperature Cycling Delamination Test	JESD22 A104; Q101 appendix 6 J-STD 035	100% C-SAM inspection after TC, followed by decap, inspection or wire pull on all wires from 5 parts for 5 highest delaminated parts.		3	84	0 Fails	
7b	WBI	Wire Bond Integrity	MILSTD750 Method 2037	post 500 cyc		3	5	0 Fails	
8	UHAST	Unbiased HAST	JESD22 A118	Ta=130°C, 85% RH, no bias, 96 hrs		3	84	0 Fails	
8 alt	AC	Autoclave	JESD22 A102	Ta = 121°C, P= 15 PSIG, RH = 100%, 96 Hours		3	84		uHAST performed
9	HAST	Highly Accelerated Stress Test	JESD22 A110	130C/85%RH, 80% rated V or 42V max, 96 hours	Q006 - NVMFS5C604NLT1G	3	84	0 Fails	SAT, Wire pull, cross section, ball shear post stress - Complete/PASS
9 alt	H3TRB	High Humidity, High Temperature Reverse Bias	JESD22 A101	85°C, 85% RH, V=80% rated V or 100V max. 1000 Hours		3	84		HAST performed
9a	HTHBB	High Temperature High Humidity Bias	JESD22 A101	85°C, 85% RH, FORWARD BIAS V=80% rated V or 100V max. 1000 Hours		3	84		LEDs Only
10	IOL	Intermittent Operational Life	MIL-STD-750 Method 1037	Ta=25°C Delta Tj=100°C, t(on)=t(off)= 2 min, 15K Cycles	Q006 - NVMFS5C604NLT1G	3	84	0 Fails	
(alt)	PTC	Power and Temperature Cycle	JESD22 A105	Performed if IOL cannot be achieved		3	84		IOL performed
11	ESD	ESD - CDM/HBM	AEC Q101-001/005	Prefer CDM and HBM.		3	30	0 Fails	
12	DPA	DPA	AEC Q101-004 Section 4	Post H3TRB or HAST and TC		3	2	0 Fails	
13	PD	Physical Dimension	JESD22 B100	Verify physical dimensions to specifications		1	30	0 Fails	
14	TS	Terminal Strength	MIL-STD-750 Method 2036	Evaluate lead integrity on leaded parts only		1	30		through hole parts only
15	RTS	Resistance to Solvents	JESD22 B107	Verify marking permanency. Not required for laser marking		1	30		Not for laser marked parts
16	CA	Constant Acceleration	MIL-STD-750 Method 2006	Y1 plane, 15Kg force		1	30		For Cavity package only
17	VVF	Vibration Variable Frequency	JESD22 B103	displacement 0.06" 20Hz to 100Hz and 50g peak acceleration 100Hz to 2KHz		1	30		For Cavity package only
18	MS	Mechanical Shock	JESD22 B104	1500g's for 0.5ms, 5 blows, 3 orientations		1	30		For Cavity package only
19	HER	Hermeticity	JESD22 A109	Fine and gross leak		1	30		For Hermetic pkgs only
20	RSH	Resistance to Solder Heat	JESD22 B106	per AEC - Q101		3	30	0 Fails	
21	SD	Solderability	J-STD-002 , B102	50X		3	10	0 Fails	
22	TR	Thermal Resistance	appropriate	per device specification, pre & post process change					Per datasheet
23	WBS	Wire Bond Strength	MIL-STD-750 Method 2037	Pre & post process change		3	10	0 Fails	
24	BS	Bond Shear	AEC Q101-003			3	10	0 Fails	
25	DS	Die Shear	MIL-STD-750 Method 2017			3	5	0 Fails	
26	UIS	Unclamped Inductive Switching	AEC Q101-004 Section 2			3	5	0 Fails	
27	DI	Dielectric Integrity	AEC Q101-004 Section 3			3	5	0 Fails	
28	SCR	Short Circuit Reliability Characterization	AEC Q101 - 006			3	10		For Smart Power parts only
29	LF	Lead Free	AEC Q005	For all related solderability, solder heat resistance and whisker requirements		3	3	0 Fails	JESD201 Class 2

Notes:

NOT Recommended for Military, Medical or Aerospace

Summarized from NVMFS5C410NL S32572, NVMFS5C604NL S32577 (Q006)

ON Semiconductor®



## AEC Q101 Discrete Device Semiconductor Component Qualification Summary

Supplier: ON Semiconductor  
 Supplier PN: NVMFS5C404NT1G  
 Product Description: 40V N-Ch SG Power MOSFET  
 Supplier Fab Info: AFSM, Japan  
 Supplier Assy Info: Assy Seremban, Malaysia  
 Reason for Qual: Qual Vehicle S08FL HE

Customer PN:  
 General Specification: AEC - Q101  
 Q006  
 Reliability Report Date: Jan-17  
 Family Type: Power MOSFET, S08FL HE

ON Semiconductor®



#	Test	Stress Name	Reference	Test Conditions	Comments	# Lots	S.S.	Results Fail/Total	Comments/ Test Results
1	TEST	Pre and post stress electrical test	Device specification			All	All	0 Fails	
2	PC	Preconditioning	JEDS22 A113	Performed on surface mount devices prior to TC, AC, H3TRB or HAST, and IOL.	MSL1 @ 260°C temp	3	336	0 Fails	
3	EV	External Visual	JEDS22 B101	Device construction, marking, and workmanship		3	77	0 Fails	
4	PV	Parametric Verification	Device specification	Tested to device specification requirements		3	25	0 Fails	
5	HTRB	High Temperature Reverse Bias	MILSTD750-1 method M1038A	T <sub>j</sub> = max, V=100% rated V, 1008 Hrs		3	84	0 Fails	JESD22 A108
5a	ACBV	AC Blocking Voltage	MILSTD750-1 M1040 A	T <sub>j</sub> =max, AC blocking V = max, 1000 Hrs		3	84		Thyristors only
5b	HTFB	High Temperature forward Bias	JESD22 A108	T <sub>a</sub> =, V=100% rated forward V, 1000 Hrs		3	84		LEDs Only
5c	SSOP	Steady State Operational Life	MILSTD750-1 method M1038B	T <sub>j</sub> = max, V=100% rated I <sub>D</sub> max, 1000 Hrs		3	84		Zeners only
6	HTGB	High Temperature Gate Bias	JESD22 A108	T <sub>a</sub> = max, V <sub>GS</sub> =100%, 1008 Hrs		3	84	0 Fails	
	HTSL	High Temperature Storage Life	JESD22 A103	T <sub>a</sub> = max, 1000 Hrs, 2016 Hrs	Q006	3	84	0 Fails	Cross section 1X and 2X Complete/Pass.
7	TC	Temperature Cycling	JESD22 A104; Q101 appendix 6	-55°C to +150°C, t(dwell)>15 min, 1000 Cycles	Board mounted Q006	3	84	0 Fails	SAT, Wire pull, cross section, ball shear post stress - Complete/PASS
7a	TCHT	Temperature Cycling Hot Test	JESD22 A104; Q101 appendix 6	125°C Hot Test after TC, followed by wire pull on all wires from 5 part		3	84		Hermetic Packages only
7a alt	TCDT	Temperature Cycling Delamination Test	JESD22 A104; Q101 appendix 6 J STD 035	100% C-SAM inspection after TC, followed by decap, inspection or wire pull on all wires from 5 parts for 5 highest delaminated parts.		3	84	0 Fails	
7b	WBI	Wire Bond Integrity	MILSTD750 Method 2037	post 500 cyc		3	5	0 Fails	
8	UHAST	Unbiased HAST	JESD22 A118	T <sub>a</sub> =130°C, 85% RH, no bias, 192 hrs		3	84	0 Fails	
8 alt	AC	Autoclave	JESD22 A102	T <sub>a</sub> = 121°C, P= 15 PSIG, RH = 100%, 96 Hours		3	84		uHAST performed
9	HAST	Highly Accelerated Stress Test	JESD22 A110	130C/85%RH, 80% rated V or 42V max, 192 hours	Q006	3	84	0 Fails	SAT, Wire pull, cross section, ball shear post stress - Complete/PASS
9 alt	H3TRB	High Humidity, High Temperature Reverse Bias	JESD22 A101	85°C, 85% RH, V=80% rated V or 100V max. 1000 Hours		3	84		HAST performed
9a	HTHBB	High Temperature High Humidity Bias	JESD22 A101	85°C, 85% RH, FORWARD BIAS V=80% rated V or 100V max. 1000 Hours		3	84		LEDs Only
10	IOL	Intermittent Operational Life	MIL-STD-750 Method 1037	T <sub>a</sub> =25°C Delta T <sub>j</sub> =100°C, t(on)=t(off)= 2 min, 30K Cycles	Q006	3	84	0 Fails	
(alt)	PTC	Power and Temperature Cycle	JESD22 A105	Performed if IOL cannot be achieved		3	84		IOL performed
11	ESD	ESD - CDM/HBM	AEC Q101-001/005	Prefer CDM and HBM.		3	30	0 Fails	
12	DPA	DPA	AEC Q101-004 Section 4	Post H3TRB or HAST and TC		3	2	0 Fails	
13	PD	Physical Dimension	JESD22 B100	Verify physical dimensions to specifications		1	30	0 Fails	
14	TS	Terminal Strength	MIL-STD-750 Method 2036	Evaluate lead integrity on leaded parts only		1	30		through hole parts only
15	RTS	Resistance to Solvents	JESD22 B107	Verify marking permanency. Not required for laser marking		1	30		Not for laser marked parts
16	CA	Constant Acceleration	MIL-STD-750 Method 2006	Y1 plane, 15Kg force		1	30		For Cavity package only
17	VVF	Vibration Variable Frequency	JESD22 B103	displacement 0.06" 20Hz to 100Hz and 50g peak acceleration 100Hz to 2KHz		1	30		For Cavity package only
18	MS	Mechanical Shock	JESD22 B104	1500g's for 0.5ms, 5 blows, 3 orientations		1	30		For Cavity package only
19	HER	Hermeticity	JESD22 A109	Fine and gross leak		1	30		For Hermetic pkgs only
20	RSH	Resistance to Solder Heat	JESD22 B106	per AEC - Q101		3	30	0 Fails	
21	SD	Solderability	J-STD-002 , B102	50X		3	10	0 Fails	
22	TR	Thermal Resistance	appropriate	per device specification, pre & post process change					Per datasheet
23	WBS	Wire Bond Strength	MIL-STD-750 Method 2037	Pre & post process change		3	10	0 Fails	
24	BS	Bond Shear	AEC Q101-003			3	10	0 Fails	
25	DS	Die Shear	MIL-STD-750 Method 2017			3	5	0 Fails	
26	UIS	Unclamped Inductive Switching	AEC Q101-004 Section 2			3	5	0 Fails	
27	DI	Dielectric Integrity	AEC Q101-004 Section 3			3	5	0 Fails	
28	SCR	Short Circuit Reliability Characterization	AEC Q101 - 006			3	10		For Smart Power parts only
29	LF	Lead Free	AEC Q005	For all related solderability, solder heat resistance and whisker requirements		3	3	0 Fails	JESD201 Class 2

Notes:

NOT Recommended for Military, Medical or Aerospace

Summarized from NVMFS5C404N S32575, NVMFS5C604NL S32577

## AEC Q101 Discrete Device Semiconductor Component Qualification Summary

**Supplier:** ON Semiconductor  
**Supplier PN:** NVMFS5C604NLT1G  
**Product Description:** 40V & 60V N-Ch LL Power MOSFET  
**Supplier Fab info:** AFSM, Japan  
**Supplier Assy info:** Assy Seremban, Malaysia  
**Reason for Qual:** Qual Vehicle S08FL HE

**Customer PN:**  
**General Specification:** AEC - Q101  
**Q006**  
**Reliability Report Date:** Jan-17  
**Family Type:** Power MOSFET, S08FL HE

ON Semiconductor®



#	Test	Stress Name	Reference	Test Conditions	Comments	# Lots	S.S.	Results Fail/Total	Comments/ Test Results
1	TEST	Pre and post stress electrical test	Device specification			All	All	0 Fails	
2	PC	Preconditioning	JEDS22 A113	Performed on surface mount devices prior to TC, AC, H3TRB or HAST, and IOL.	MSL1 @ 260°C temp	3	336	0 Fails	
3	EV	External Visual	JEDS22 B101	Device construction, marking, and workmanship		3	77	0 Fails	
4	PV	Parametric Verification	Device specification	Tested to device specification requirements		3	25	0 Fails	
5	HTRB	High Temperature Reverse Bias	MILSTD750-1 method M1038A	Tj= max, V=100% rated V, 1008 Hrs		3	84	0 Fails	JEDS22 A108
5a	ACBV	AC Blocking Voltage	MILSTD750-1 M1040 A	Tj= max, AC blocking V = max, 1000 Hrs		3	84		Thyristors only
5b	HTFB	High Temperature forward Bias	JESD22 A108	Ta= , V=100% rated forward V, 1000 Hrs		3	84		LEDs Only
5c	SSOP	Steady State Operational Life	MILSTD750-1 method M1038B	Tj= max, V=100% rated IZ max, 1000 Hrs		3	84		Zeners only
6	HTGB	High Temperature Gate Bias	JESD22 A108	Ta= max, Vgs=100%, 1008 Hrs		3	84	0 Fails	
	HTSL	High Temperature Storage Life	JESD22 A103	Ta= max, 1000 Hrs, 2016 Hrs	Q006	3	84	0 Fails	Cross section 1X and 2X Complete/Pass.
7	TC	Temperature Cycling	JESD22 A104; Q101 appendix 6	-55°C to +150°C, t(dwell)>15 min, 1000 Cycles	Board mounted	3	84	0 Fails	SAT, Wire pull, cross section, ball shear post stress - Complete/PASS
7a	TCHT	Temperature Cycling Hot Test	JESD22 A104; Q101 appendix 6	125°C Hot Test after TC, followed by wire pull on all wires from 5 part		3	84		Hermetic Packages only
7a alt	TCDT	Temperature Cycling Delamination Test	JESD22 A104; Q101 appendix 6	100% C-SAM inspection after TC, followed by decap, inspection or wire pull on all wires from 5 parts for 5 highest delaminated parts.		3	84	0 Fails	
7b	WBI	Wire Bond Integrity	J-STD 035	post 500 cyc		3	5	0 Fails	
8	UHAST	Unbiased HAST	JESD22 A118	Ta=130°C, 85% RH, no bias, 192 hrs		3	84	0 Fails	
8 alt	AC	Autoclave	JESD22 A102	Ta = 121°C, P = 15 PSIG, RH = 100%, 96 Hours		3	84		uHAST performed
9	HAST	Highly Accelerated Stress Test	JESD22 A110	130C/85%RH, 80% rated V or 42V max, 192 hours	Q006	3	84	0 Fails	SAT, Wire pull, cross section, ball shear post stress - Complete/PASS
9 alt	H3TRB	High Humidity, High Temperature Reversal Bias	JESD22 A101	85°C, 85% RH, V=80% rated V or 100V max. 1000 Hours		3	84		HAST performed
9a	HTHBB	High Temperature High Humidity Bias	JESD22 A101	85°C, 85% RH, FORWARD BIAS V=80% rated V or 100V max. 1000 Hours		3	84		LEDs Only
10	IOL	Intermittent Operational Life	MIL-STD-750 Method 1037	Ta=25°C Delta Tj=100°C, t(on)=t(off)= 2 min, 30K Cycles	Q006	3	84	0 Fails	
(alt)	PTC	Power and Temperature Cycle	JESD22 A105	Performed if IOL cannot be achieved		3	84		IOL performed
11	ESD	ESD - CDM/HBM	AEC Q101-001/005	Prefer CDM and HBM.		3	30	0 Fails	
12	DPA	DPA	AEC Q101-004 Section 4	Post H3TRB or HAST and TC		3	2	0 Fails	
13	PD	Physical Dimension	JESD22 B100	Verify physical dimensions to specifications		1	30	0 Fails	
14	TS	Terminal Strength	MIL-STD-750 Method 2036	Evaluate lead integrity on leaded parts only		1	30		through hole parts only
15	RTS	Resistance to Solvents	JESD22 B107	Verify marking permanency. Not required for laser marking		1	30		Not for laser marked parts
16	CA	Constant Acceleration	MIL-STD-750 Method 2006	Y1 plane, 15Kg force		1	30		For Cavity package only
17	VVF	Vibration Variable Frequency	JESD22 B103	displacement 0.06" 20Hz to 100Hz and 50g peak acceleration 100Hz to 2KHz		1	30		For Cavity package only
18	MS	Mechanical Shock	JESD22 B104	1500g's for 0.5ms, 5 blows, 3 orientations		1	30		For Cavity package only
19	HER	Hermeticity	JESD22 A109	Fine and gross leak		1	30		For Hermetic pkgs only
20	RSH	Resistance to Solder Heat	JESD22 B106	per AEC - Q101		3	30	0 Fails	
21	SD	Solderability	J-STD-002 , B102	50X		3	10	0 Fails	
22	TR	Thermal Resistance	appropriate	per device specification, pre & post process change					Per datasheet
23	WBS	Wire Bond Strength	MIL-STD-750 Method 2037	Pre & post process change		3	10	0 Fails	
24	BS	Bond Shear	AEC Q101-003			3	10	0 Fails	
25	DS	Die Shear	MIL-STD-750 Method 2017			3	5	0 Fails	
26	UIS	Unclamped Inductive Switching	AEC Q101-004 Section 2			3	5	0 Fails	
27	DI	Dielectric Integrity	AEC Q101-004 Section 3			3	5	0 Fails	
28	SCR	Short Circuit Reliability Characterization	AEC Q101 - 006	For all related solderability, solder heat resistance and whisker requirements		3	10		For Smart Power parts only
29	LF	Lead Free	AEC Q005			3	3	0 Fails	JESD201 Class 2

Notes:

NOT Recommended for Military, Medical or Aerospace

Summarized from NVMFS5C604NL S32577