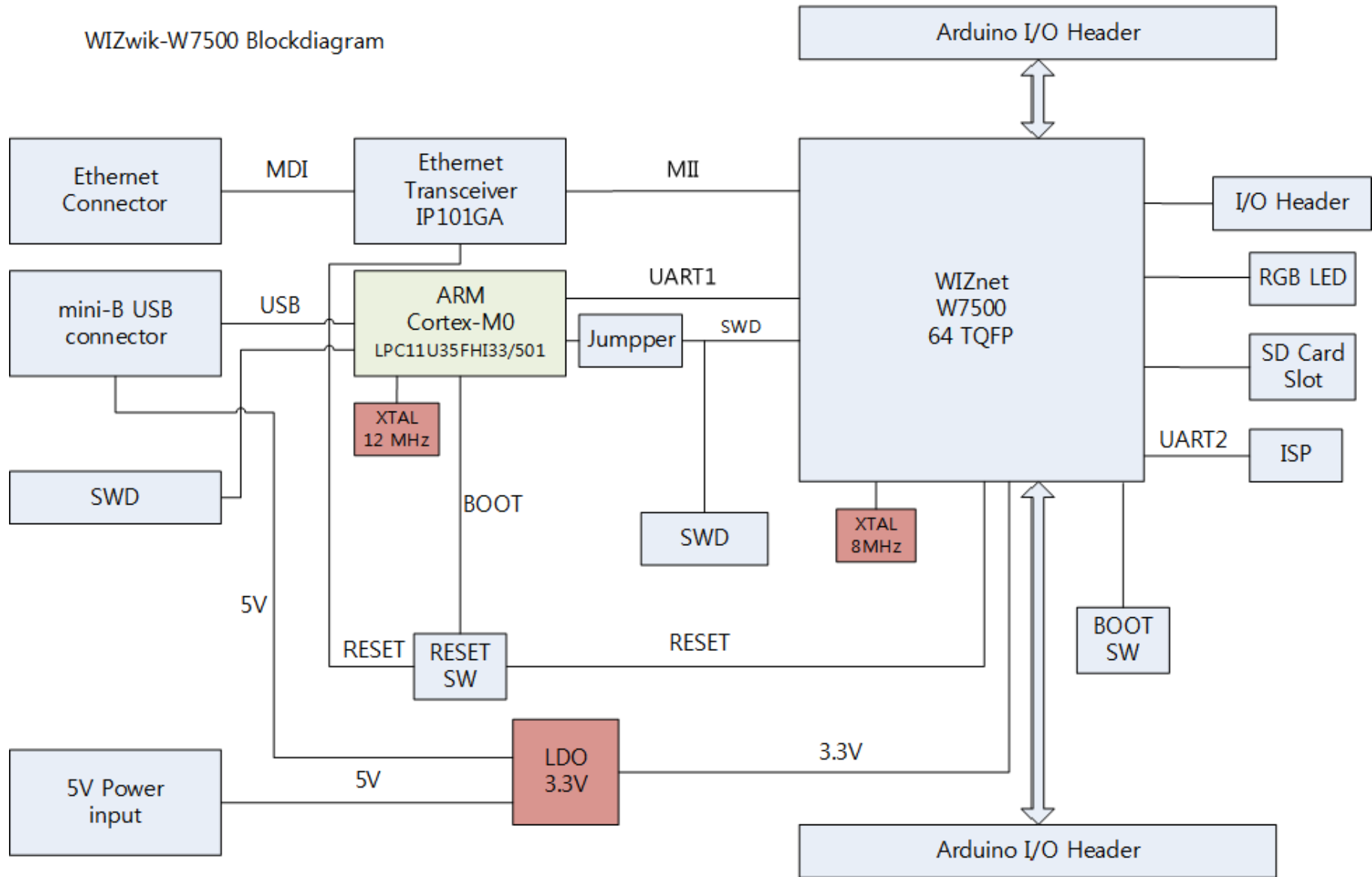
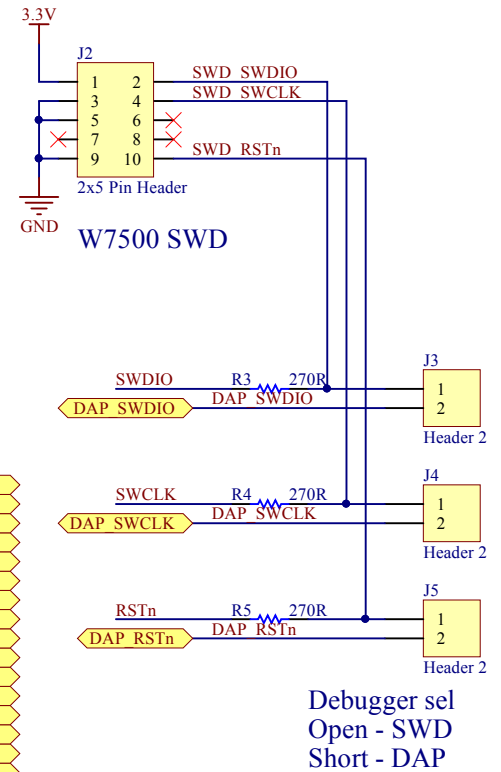
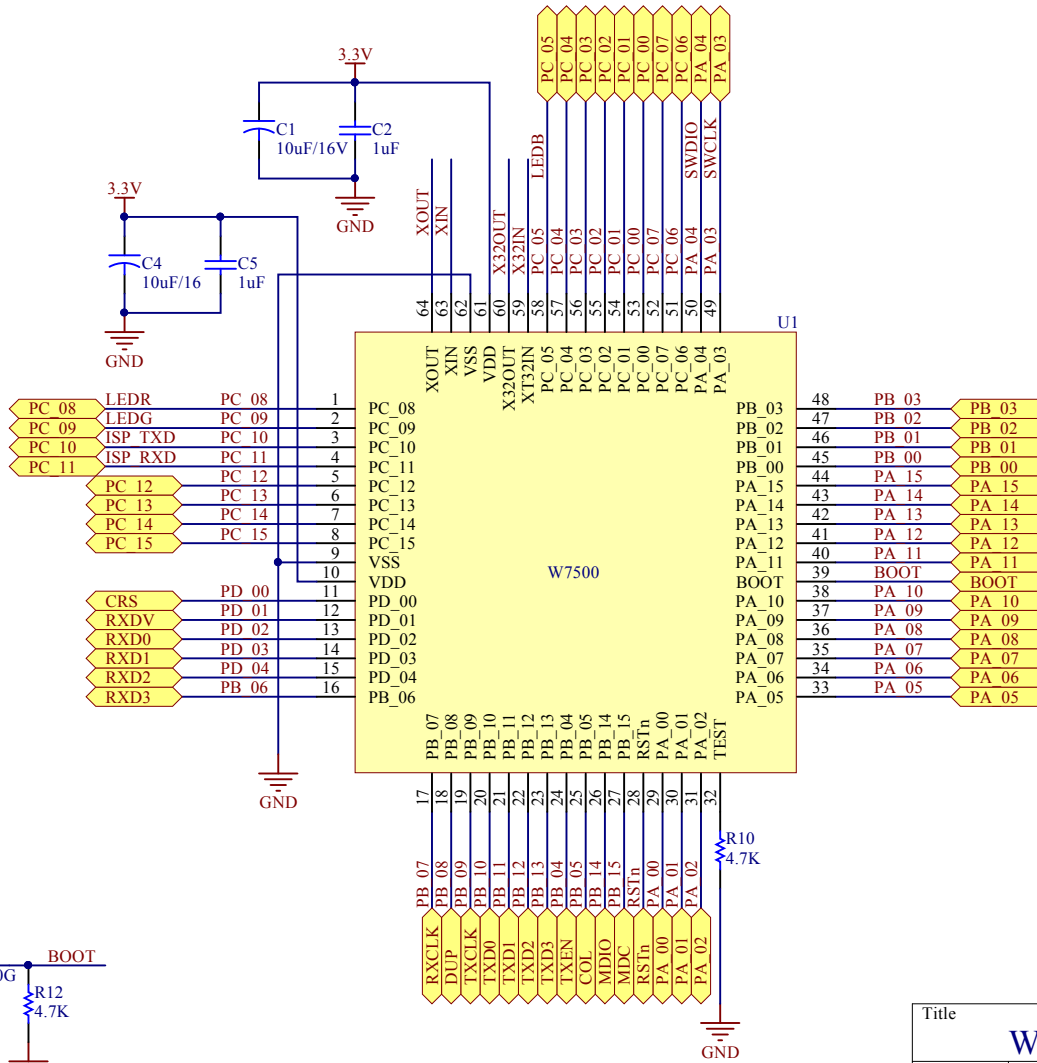
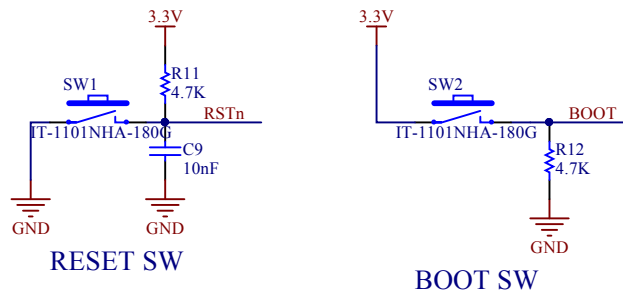
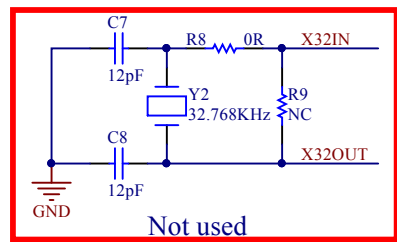
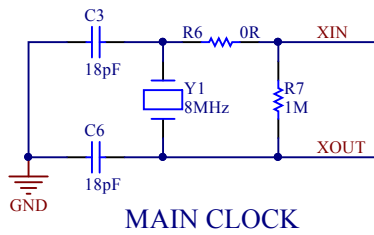
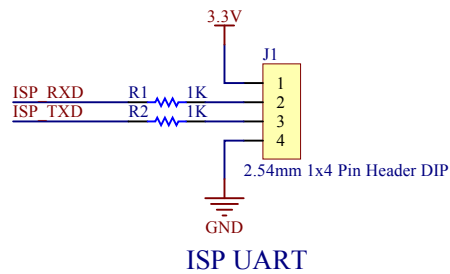


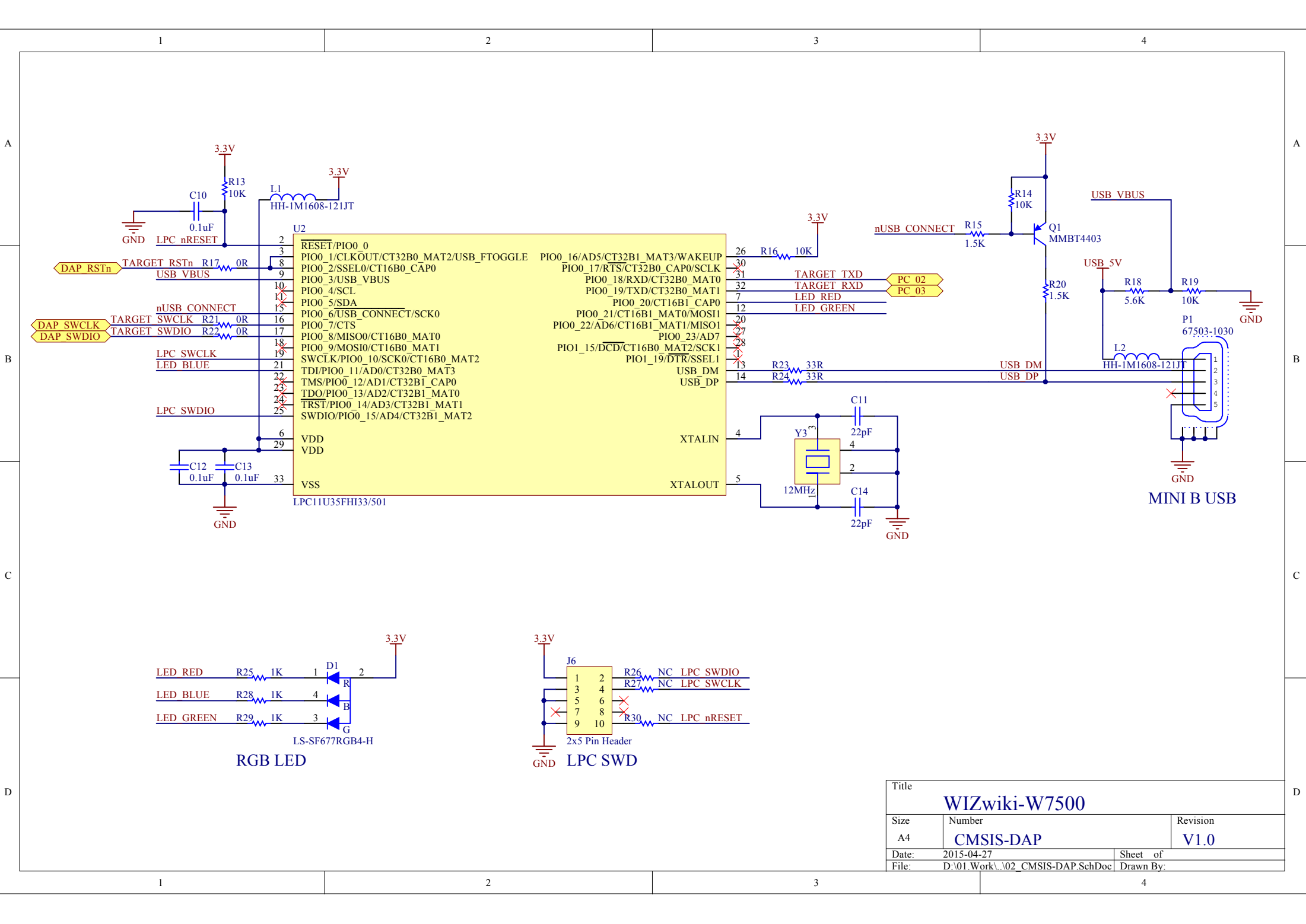
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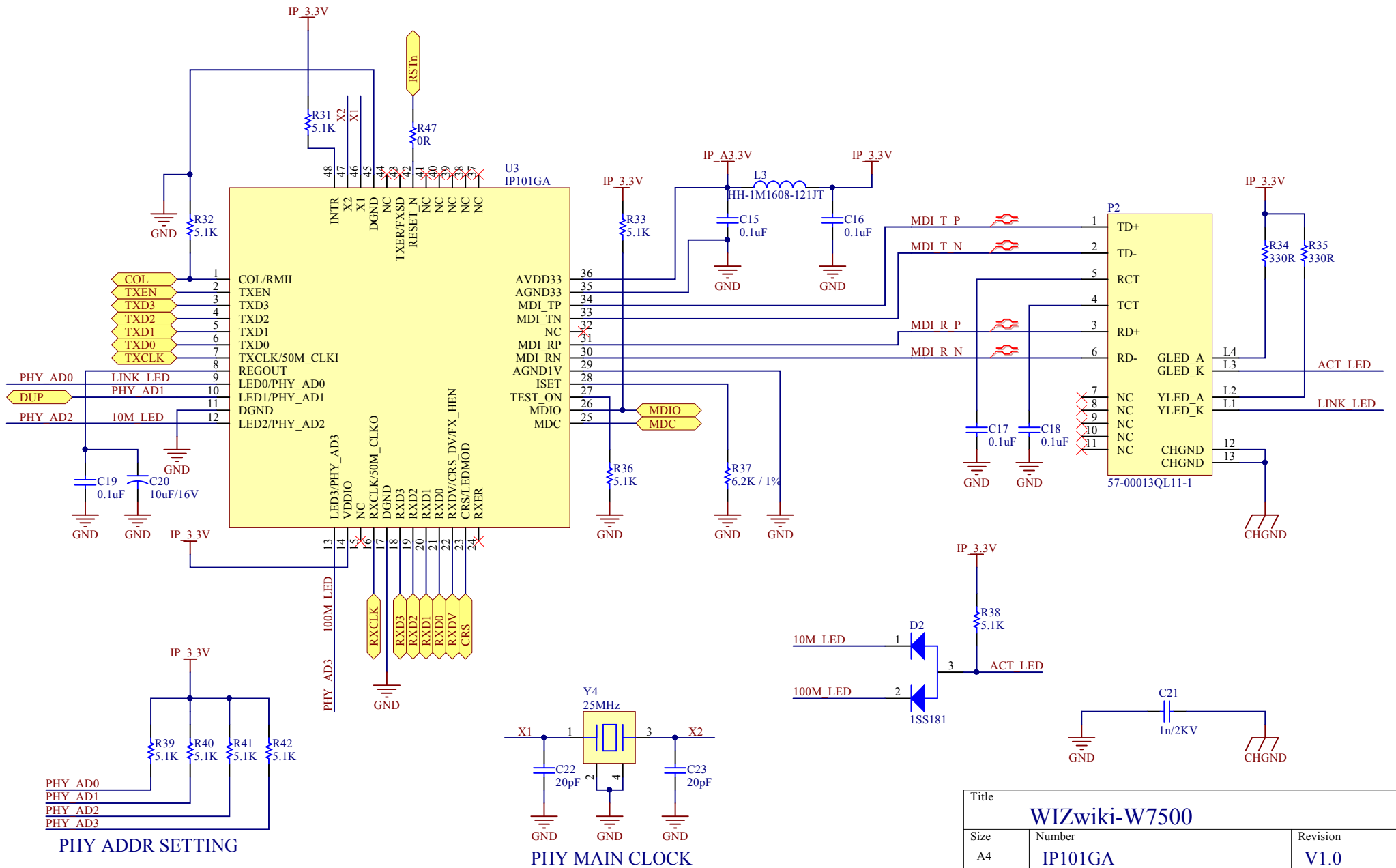
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**PHY ADDR SETTING**

**PHY MAIN CLOCK**

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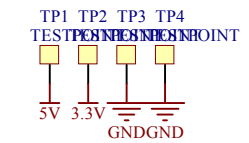
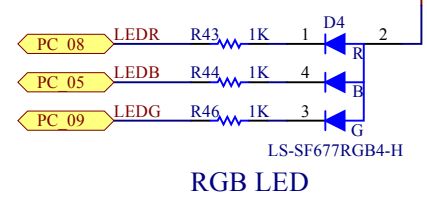
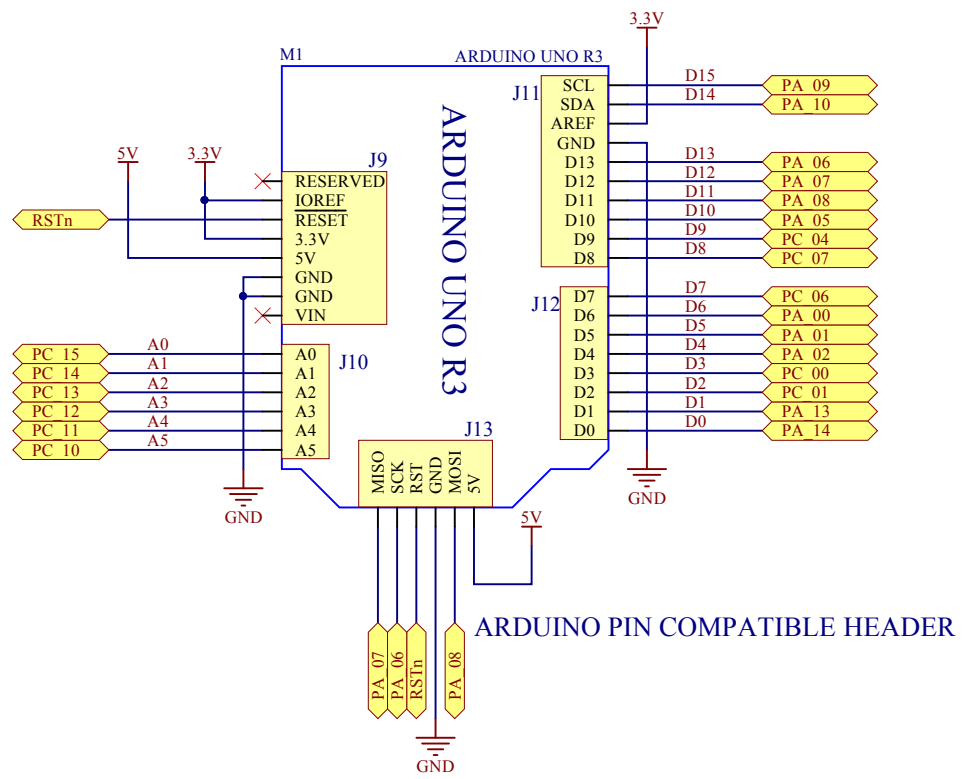
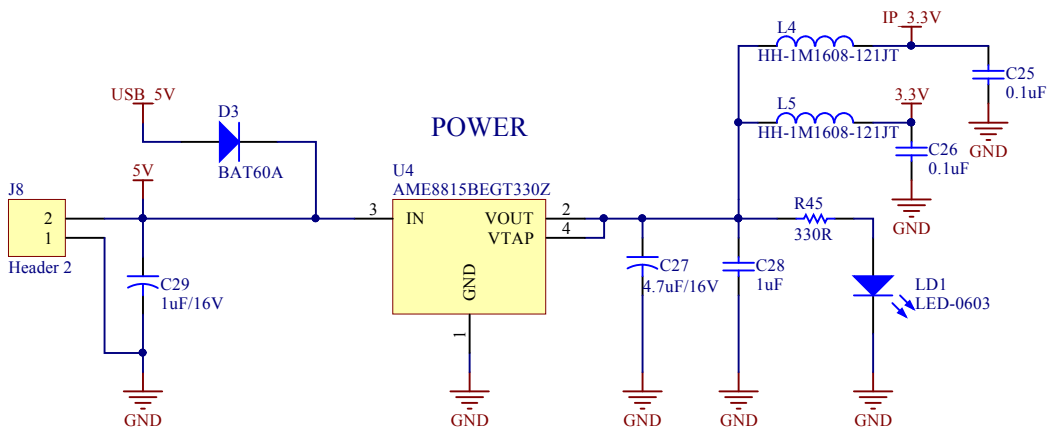
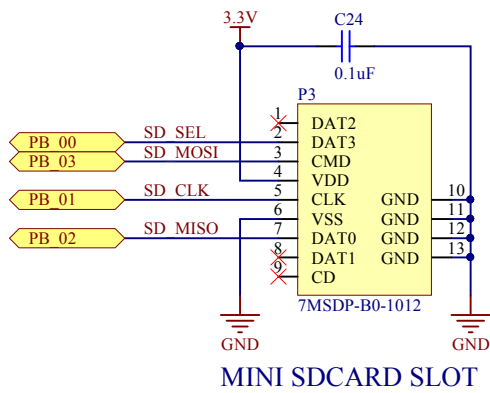
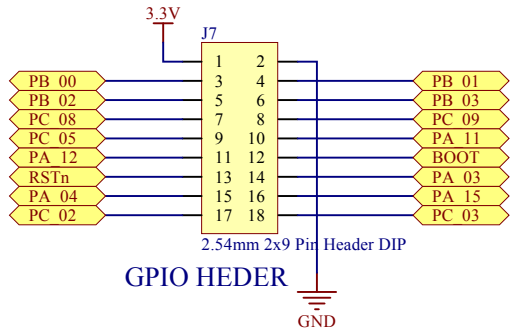
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