

### **Engineering/Process Change Notice**

**ECN/PCN No.: 4616** 

For Manufacturer								
Product Description:	Abracon Part Number / Part Series:	☐ Documentation only	⊠ Series					
Half Size DIP Low Voltage 3.3V HCMOS/TTL Crystal clock oscillator	ACHL series	⊠ ECN □ EOL	☐ Part Number					
Affected Revision:	New Revision:	Application:	☐ Safety					
М	N		⊠ Non-Safety					
<b>Prior to Change:</b> Internal Assembly: Plating on Resonator ty	pe							
After Change: Internal Assembly: Ceramic SMD mount ty	pe							
Change of internal assembly type. No form. No affect on external mechanical dimension		assembly type. No form, fit, o	or function change.					
	Change Plan							
Effective Date: 3/24/2023	Additional Remarks:							
Change Declaration:								
Issued Date:	Issued By:	Issued Department:						
3/24/2023	Arturo Longoria	Engineering						
Approval:	Approval:	Approval:						
Thomas Culhane Engineering Director	Reuben Quintanilla Quality Director	Ying Huang Purchasing Director						
Engineering Director	·	Fulcilasing Di	rector					
Look Time Day (if amplicable).	For Abracon EOL only	mhau / Daut Cavina						
Last Time Buy (if applicable): N/A	Alternate Part Nu	Alternate Part Number / Part Series: N/A						
Additional Approval:	Additional Approval: Additional Approval:							
	Customer Approval (If Applicable	)						
Qualification Status:	Constant of the constant of th	,						
Quamouton status	☐ Approved ☐ Not accepted							
Note: It is considered approved if there is n		ter ECN/PCN is released.						
Customer Part Number:	Customer Projec	Customer Project:						
Company Name:	Company Representative:	Representative Signature	::					
Customer Remarks:								



Form #7020 | Rev. G | Effective: 02/22/2021 |











## HALF SIZE DIP LOW VOLTAGE 3.3VHCMOS/TTL COMPATIBLE CRYSTAL CLOCK OSCILLATOR

**ACHL Series** 

Moisture Sensitivity Level (MSL) – This product is Hermetically Sealed and not Moisture Sensitive - MSL = N/A: Not Applicable





#### **FEATURES:**

- Tri-state Enable/Disable option
- Low supply voltage 3.3V
- HCMOS and TTL compatible
- Tight symmetry option 50% +/-5%

#### > **APPLICATIONS**:

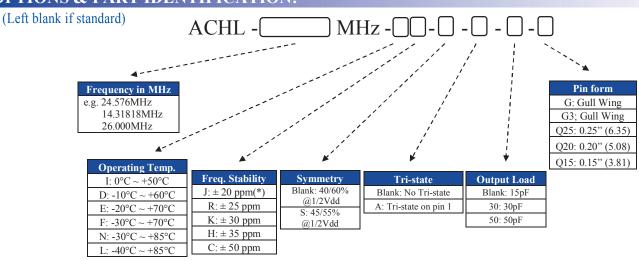
- Clock signal sources for digital chips and microprocessors
- Low power applications

#### STANDARD SPECIFICATIONS:

Parameters		Minimum	Typical	Maximum	Units	Notes	
Frequency Range		0.400		160.000	MHz		
Operating Temperature		0		+70	°C	See options	
Storage Temperature		-55		+125	°C		
Overall Frequen	cy Stabilit	.y	-100		+100	ppm	See options
Supply Voltage	(Vdd)		2.97	3.3	3.63	V	
Input Current	0.400MH	z ~ 24.999MHz			20		
	25.000MF	Hz ~ 99.999MHz			40	mA	
100.000N		Hz ~ 16000MHz			80		
Symmetry (@ 1	/2Vdd)		40	50	60	%	See options
Rise and Fall	0.400MH	z ~ 24.999MHz			10		
Time (Tr/Tf) 25.000M		Hz ~ 99.999MHz			5	ns	
		Hz ~ 16000MHz			4		
Output Load					15	pF	See options
Output Load					5	TTL	
Output Voltage		· VOH	0.9*Vdd			V	
Output Voluge		VOL			0.1*Vdd	V	
Tri-state Function		VIH	2.2			V	"1" or Open: Oscillation
		VIL			0.8	V	"0": Output disable (Hi Z)
Output Enable tim	ne				4	ms	For option A only
Output Disable time				100	ns	For option A only	
Start-up Time			1	10	ms		
Phase Jitter RMS (12~20MHz)				1	ps	Reference only. Please contact Abracon for specific frequencies	
Aging			-5.0		+5.0	ppm	@+25°C First year

<sup>\*</sup> Overall frequency stability includes initial tolerance, temperature stability, and aging.

#### **OPTIONS & PART IDENTIFICATION:**



(\*) Temp option I, D, E and 0°C ~ +70°C only.





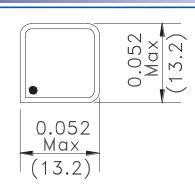
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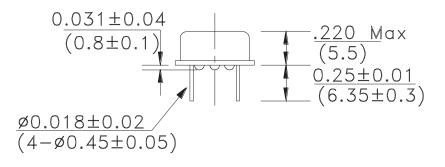
ACHL Series

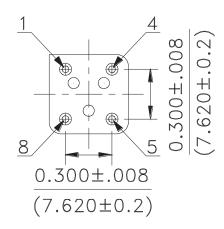




#### **OUTLINE DIMENSIONS:**







Note: Recommend using an approximately 0.01uF bypass capacitor between PIN 2 and 4.

Function	
Tri-State/NC	
GND	
Output	
Vdd	

Dimensions: inches (mm)

### Packaging

40pcs /tube

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