

Product/process change notification

PCN N° 2022-016-A

Dear customer,

Please find attached our Infineon Technologies AG PCN:

Capacity Extension by Introduction of 300mm wafer diameter for dedicated OptiMOS™ 5 30V products at Infineon Technologies Austria AG, Austria and assembly and test location at Infineon Technologies Wuxi Co., Ltd., China for PG-TDSON-8 packages

Important information for your attention:

- Please respond to this PCN by indicating your decision on the approval form, sign it and return to your sales partner before **2023-02-09**
- Infineon aligns with the widely recognized JEDEC STANDARD “**JESD46**“, which stipulates: **“Lack of acknowledgement of the PCN within 30 days constitutes acceptance of the change.”**

Your prompt reply will help Infineon to assure a smooth and well-executed transition. If Infineon does not hear from your side by the due date, we will assume your full acceptance to this proposed change and its implementation.

Your attention and response to this matter is greatly appreciated.

Infineon Technologies AG

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Chairman of the Supervisory Board Dr. Wolfgang Eder

Management Board Jochen Hanebeck (CEO), Constanze Hufenbecher, Dr. Sven Schneider, Andreas Urschitz, Dr. Rutger Wijburg

Registered office Neubiberg Commercial register Amtsgericht München HRB 126492

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► Products affected

Please refer to attached affected product list
“pcn_2022-016-A_[customer-no].pdf”

► Detailed change information

Subject	<ul style="list-style-type: none"> - Introduction of 300mm wafer diameter at Infineon Technologies Austria AG, Austria - Capacity extension at Infineon Technologies Wuxi Co., Ltd., China for PG-TDSON-8 packages 										
Reason	<ul style="list-style-type: none"> - Next phase of Front-End capacity expansion by introduction of 300mm wafer diameter to support continuous increasing customer demand - Implementation of additional assembly site for improved delivery security 										
Description	<table> <tr> <th data-bbox="618 1045 1019 1098"><u>Old</u></th><th data-bbox="1019 1045 1442 1098"><u>New</u></th></tr> <tr> <td data-bbox="302 1098 1019 1402"> Wafer Production Site and Wafer Test <ul style="list-style-type: none"> ■ Infineon Technologies Austria AG, Villach, Austria (200mm) </td><td data-bbox="1019 1098 1442 1402"> <ul style="list-style-type: none"> ■ Infineon Technologies Austria AG, Villach, Austria (200mm) <i>and</i> ■ Infineon Technologies Austria AG, Villach, Austria (300mm) </td></tr> <tr> <td data-bbox="350 1402 1019 1581"> Wafer Lot Number <ul style="list-style-type: none"> ■ VExxxxxx (Villach, 200mm) </td><td data-bbox="1019 1402 1442 1581"> <ul style="list-style-type: none"> ■ VExxxxxx (Villach,200mm) <i>and</i> ■ VFxxxxxx (Villach,300mm) </td></tr> <tr> <td data-bbox="181 1581 1019 1822"> Assembly & Final Test location for PG-TDSON-8 packages <ul style="list-style-type: none"> ■ Infineon Technologies Malaysia Sdn. Bhd., Melaka </td><td data-bbox="1019 1581 1442 1822"> <ul style="list-style-type: none"> ■ Infineon Technologies Malaysia Sdn. Bhd., Melaka <i>and</i> ■ Infineon Technologies Wuxi Co., Ltd., Wuxi </td></tr> <tr> <td data-bbox="285 1822 1019 1904"> Mould Compound for PG-TDSON-8 packages <ul style="list-style-type: none"> ■ CEL 1772 </td><td data-bbox="1019 1822 1442 1904"> <ul style="list-style-type: none"> ■ CEL 9240 </td></tr> </table>	<u>Old</u>	<u>New</u>	Wafer Production Site and Wafer Test <ul style="list-style-type: none"> ■ Infineon Technologies Austria AG, Villach, Austria (200mm) 	<ul style="list-style-type: none"> ■ Infineon Technologies Austria AG, Villach, Austria (200mm) <i>and</i> ■ Infineon Technologies Austria AG, Villach, Austria (300mm) 	Wafer Lot Number <ul style="list-style-type: none"> ■ VExxxxxx (Villach, 200mm) 	<ul style="list-style-type: none"> ■ VExxxxxx (Villach,200mm) <i>and</i> ■ VFxxxxxx (Villach,300mm) 	Assembly & Final Test location for PG-TDSON-8 packages <ul style="list-style-type: none"> ■ Infineon Technologies Malaysia Sdn. Bhd., Melaka 	<ul style="list-style-type: none"> ■ Infineon Technologies Malaysia Sdn. Bhd., Melaka <i>and</i> ■ Infineon Technologies Wuxi Co., Ltd., Wuxi 	Mould Compound for PG-TDSON-8 packages <ul style="list-style-type: none"> ■ CEL 1772 	<ul style="list-style-type: none"> ■ CEL 9240
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► Product identification

External traceability is assured via wafer lot number and country of assembly on the product barcode label

► Impact of change

NO change on electrical, thermal parameters and reliability as proven via product qualification and characterization

NO change in existing datasheet parameters

NO change in quality and reliability. Processes are optimized to meet product performance according to already applied Infineon specification

► Attachments

"pcn_2022-016-A_[customer-no].pdf" affected product list
2_cip22016_a qualification report

► Time schedule

■ Final qualification report	available
■ First samples available	on request
■ Intended start of delivery	2023-03-01

If you have any questions, please do not hesitate to contact your local sales office.

PCN 2022-016-A

Capacity Extension by Introduction of 300mm wafer diameter for dedicated OptiMOS™5 30V products at Infineon Technologies Austria AG, Austria and assembly and test location at Infineon Technologies Wuxi Co., Ltd., China for PG-TDSON-8 packages



Affected products sold to FUTURE ELECTRONICS INC. (4000624)

Sales name	SP number	OPN	Package	Customer part number
BSC0501NSI	SP001288140	BSC0501NSIATMA1	PG-TDSON-8	BSC0501NSIATMA1
BSC0502NSI	SP001288142	BSC0502NSIATMA1	PG-TDSON-8	BSC0502NSIATMA1
BSZ0501NSI	SP001281638	BSZ0501NSIATMA1	PG-TSDSON-8	BSZ0501NSIATMA1

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Capacity Extension by Introduction of 300mm wafer diameter for dedicated OptiMOS™5 30V products at Infineon Technologies Austria AG, Austria and assembly and test location at Infineon Technologies Wuxi Co., Ltd., China for PG-TDSON-8 packages




Affected products sold to FUTURE ELECTRONICS INC. (4048203)

Sales name	SP number	OPN	Package	Customer part number
BSZ0500NSI	SP001288150	BSZ0500NSIATMA1	PG-TSDSON-8	BSZ0500NSIATMA1
BSZ0506NS	SP001281636	BSZ0506NSATMA1	PG-TSDSON-8	BSZ0506NSATMA1
BSZ0589NS	SP001586396	BSZ0589NSATMA1	PG-TSDSON-8	BSZ0589NSATMA1

RESTRICTED

Qualification Test Report



PCN N° 2022-016-A

Date: 2022-11-30

Capacity Extension by Introduction of 300mm wafer diameter for dedicated OptiMOS™5 30V products at Infineon Technologies Austria AG, Austria and assembly and test location at Infineon Technologies Wuxi Co., Ltd., China for PG-TDSON-8 packages

Reason for choosing the following test vehicles:

BSC0500NSI Biggest chip of OptiMOS™5 30V products in 300mm wafer diameter at Infineon Technologies Austria AG, Austria in PG-TDSON-8 package assembled at Infineon Technologies Wuxi, China

BSC0588NSI Smallest chip of OptiMOS™5 30V products in 300mm wafer diameter at Infineon Technologies Austria AG, Austria in PG-TDSON-8 package assembled at Infineon Technologies Wuxi, China

BSZ0589NS Smallest chip of OptiMOS™5 30V products in 300mm wafer diameter at Infineon Technologies Austria AG, Austria in PG-TDSON-8 package assembled at Infineon Technologies Melaka, Malaysia

BSZ0501NSI Medium chip of OptiMOS™5 30V products in 300mm wafer diameter at Infineon Technologies Austria AG, Austria in PG-TDSON-8 package assembled at Tongfu Microelectronics Co., Ltd, China

Scope of qualification:

Qualification of the capacity extension for Introduction of 300mm wafer diameter for dedicated OptiMOS™5 30V products at Infineon Technologies Austria AG, Austria and assembly and test location at Infineon Technologies Wuxi Co., Ltd., China for PG-TDSON-8 packages

Assessment of Q-Results:

pass

Stress test	Abbreviation	Test conditions	Readout	BSC0500NSI	BSC0588NSI	BSZ0589NS	BSZ0501NSI
				fails / stressed	fails / stressed	fails / stressed	fails / stressed
MSL Preconditioning JESD22-A113	PC	MSL 1	0h	0 / 330	0 / 330	0 / 484	0 / 484
Temperature Cycling JESD22-A104	TC	with preconditioning -40/ 125°C	1000 x	0 / 77	0 / 77	0 / 77	0 / 77
Unbiased Temperature/Humidity JESD22-A118	UHAST	with preconditioning Ta = 130°C, RH = 85%	96 h	0 / 77	0 / 77	0 / 77	0 / 77
High Humidity High Temp. Reverse Bias JESD22-A101	H3TRB	with preconditioning T = 85 °C RH = 85% VDS = 80% of VDSmx but max 100V	1000 h	refer to BSZ0501NSI	refer to BSZ0501NSI	0 / 77	0 / 77
High Temperature Reverse Bias JESD22-A108	HTRB	with preconditioning Tj = 150°C VDS =100%VDSmax	1000 h	refer to BSZ0501NSI	refer to BSZ0501NSI	0 / 77	0 / 77
High Temperature Gate stress JESD22-A108	HTGS	with preconditioning Ta = 150 °C VGE = ±20 V	1000 h	0 / 77	0 / 77	0 / 77	0 / 77
Intermitted Operational Life Test MIL-STD 750/Meth.1037	IOL	Delta T = 100 K n = 15000 cyc	15000 x	0 / 77	0 / 77	0 / 77	0 / 77