

PRODUCT CHANGE NOTIFICATION PCN-000870

Date: 9DEC2022

P1/1

Semtech Corporation, 200 Flynn Road, Camarillo CA 93012							
Change Details							
Part Number(s) Affect RCLAMP2402B.TCT	Part Number(s) Affected: Customer Part Number(s) Affected: N/A						
Due to this migration the ca All other parameters are un	B.TCT is being migrated for pacitance of the 8" version changed.	om ASMC 5" wafer fab to ASMC of this device is 10% higher than					
Parameter C _J : Pin 1 to Pin 2		<mark>Vew]</mark> [Max]					
C _J : Pin 1 to Pin 3 C _J : Pin 2 to Pin 3	1.5 pF [Max] 1.65 p	F [Max]					
5" version will no longer be	available after Q1 of 2023						
Change Classification	⊠ Major ☐ Mino	Impact to Form, Fit, Function	⊠ Yes □ No				
Impact to Data Sheet	⊠ Yes □ No	New Revision or Date	⊠ N/A				
•	heet = Refer to above	_	om previous datasheet				
Implementation Date	Q1 2023	Work Week	To Be Advised				
Last Time Ship (LTS) Of unchanged product	Not Applicable	Not Applicable					
Sample Availability Immediate Qualification Report Availability Attached							
Supporting Documents for Change Validation/Attachments: • Final Qualification Report • Updated Datasheet • Test Characterization Report							

Issuing Authority								
Semtech Business Unit:	Protection Business Unit							
Semtech Contact Info:	QA representative: Les Fang Yuen Ifangyuen@semtech.com +1 949-269-4443	Digital signature Les Long yuen						
FOR FURTHER INFORMATION & WORLDWIDE SALES COVERAGE: http://www.semtech.com/contact/index.html#support								



	RCLAMP2412B NPI QUALIFICATION
Semtech Job#	7648
Accepted Date	06-13-2022
Job Type	New Device Qual.
Business Unit	Protection
Package Type	SC-75
Package Lead	3
Assembly Designator	Diode Inc
Master Process	21L
Fab Designator	ASMC21TVS
Rel Job Status	Rel Testing Complete Passes All Requirements
Comment	Note that this is for 8" wafer qual.

Completed Tasks

Sub Lot # Part			Lot Assen	nbly Lot	Date Code		
1 RClamp2402B			1 AER9	0091	222	25	
Task#	Task Code	Sam ple Size	Criteria	Failures	Task On Actual	Task Off Actual	Comments
1	Data-Prep	None	None	0	07-25-2022	07-25-2022	
2	HTRB_Pre_Elect_150°C_RT24	105	Pass on Zero Fails	0	08-03-2022	08-03-2022	
3	HTRB_150°C_Real Time_0024	105	Pass on Zero Fails	0	08-08-2022	08-09-2022	
4	HTRB_Pre_Elect	105	Pass on Zero Fails	0	07-26-2022	07-26-2022	
5	BI_BD_Valid	NA	Meet HTOL Schematics	0	07-25-2022	07-25-2022	
6	HTRB_150°C_0072	105	Pass on Zero Fails	0	07-26-2022	07-29-2022	
7	HTRB_150°C _0408	105	Pass on Zero Fails	0	07-29-2022	08-09-2022	
8	HTS_Pre_Elect	77	Pass on Zero Fails	0	08-01-2022	08-01-2022	
9	HTS_0168	77	Pass on Zero Fails	0	08-01-2022	08-08-2022	
10	HTS_0500	77	Pass on Zero Fails	0	08-08-2022	08-22-2022	
11	HTS_1000	77	Pass on Zero Fails	0	08-22-2022	09-12-2022	
12	ROSE Clean/ Test	251	Pass on Zero Fails	0	07-25-2022	07-25-2022	
13	85°C/85%RH_N/Pre_Pre Elec	20	Pass with 0 fail	0	08-03-2022	08-03-2022	
14	85°C/85%RH_BD_Valid	20	Pass on Zero Fails	0	08-02-2022	08-03-2022	
15	85/85_120hr_On/Off	20	Pass on Zero Fails	0	08-03-2022	08-08-2022	
16	Pre_Elect_Precond	231	Pass on Zero Fails	0	07-26-2022	07-26-2022	
17	Precond_Temp_Cyc_5cyc	231	Pass on Zero Fails	0	07-26-2022	07-26-2022	
18	Precond_HTS_24hr	231	Pass on Zero Fails	0	07-26-2022	07-27-2022	
19	Precond_85/85_NoElec168hr	231	Pass on Zero Fails	0	07-27-2022	08-03-2022	
20	Precond_IR_Refl_Char	231	Pass on Zero Fails	0	08-03-2022	08-03-2022	
21	T/C_Pre_Elect	77	Pass on Zero Fails	0	08-03-2022	08-03-2022	
22	T/C_wPre_0250	77	Pass on Zero Fails	0	08-03-2022	08-08-2022	

Task#	Task Code	Sam ple Size	Criteria	Failures	Task On Actual	Task Off Actual	Comments
23	T/C_wPre_0500	77	Pass on Zero Fails	0	08-08-2022	08-15-2022	
24	Cross_Section TC 500 Cyc	5	Pass on Zero Fails	0	08-15-2022	08-16-2022	
25	T/C_wPre_1000	77	Pass on Zero Fails	0	08-15-2022	08-24-2022	
26	HAST Pre_Elect	77	Pass on Zero Fails	0	08-03-2022	08-03-2022	
27	HAST_BD_Validation	N/A	Pass on Zero Fails	0	08-03-2022	08-05-2022	
28	HAST_wPRE_264 Hrs 110°C	77	Pass on Zero Fails	0	08-05-2022	08-17-2022	
29	HAST Pre_Elect	77	Pass on Zero Fails	0	08-03-2022	08-03-2022	
30	HAST_unbias_264hrs_110°C	77	Pass on Zero Fails	0	08-05-2022	08-17-2022	
31	CSAM Analysis	22	Pass on Zero Fails	0	08-05-2022	08-05-2022	
32	Precond_Temp_Cyc_5cyc	22	Pass on Zero Fails	0	08-08-2022	08-08-2022	
33	Precond_HTS_24hr	22	Pass on Zero Fails	0	08-08-2022	08-09-2022	
34	Precond_85/85_NoElec168hr	22	Pass on Zero Fails	0	08-09-2022	08-16-2022	
35	Precond_260°C_IR_Ref_Char	22	Pass on Zero Fails	0	08-16-2022	08-16-2022	
36	CSAM Analysis	22	Pass on Zero Fails	0	08-16-2022	08-16-2022	
37	Pack_Clos	0	0	0	09-14-2022	09-14-2022	

Sub Lot#	Part		L	ot A	Assembly	Lot	Date	Code
2	RClamp2402	2B	1	I	AER-9126		2227	1
Task#	Task Code	Sample Size	Criteria	Failt	ures	Task On Actual	Task Off Actual	Comments
1	Data-Prep	None	None	0		08-29-2022	08-29-2022	
2	HTRB_Pre_Elect_150°C_RT24	105	Pass on Zero Fails	0		08-31-2022	08-31-2022	
3	HTRB_150°C_Real Time_0024	105	Pass on Zero Fails	0		09-01-2022	09-01-2022	
4	HTRB_Pre_Elect	105	Pass on Zero Fails	0		08-30-2022	08-30-2022	
5	BI_BD_Valid	105	Meet HTOL Schematics	0		08-30-2022	08-30-2022	
6	HTRB_150°C_0072	105	Pass on Zero Fails	0		08-30-2022	09-02-2022	
7	HTRB_150°C _0408	105	Pass on Zero Fails	0		09-02-2022	09-15-2022	
8	HTS_Pre_Elect	77	Pass on Zero Fails	0		08-29-2022	08-29-2022	
9	HTS_0168	77	Pass on Zero Fails	0		08-29-2022	09-06-2022	
10	HTS_0500	77	Pass on Zero Fails	0		09-06-2022	09-19-2022	
11	HTS_1000	77	Pass on Zero Fails	0		09-19-2022	10-10-2022	

Task#	Task Code	Sample Size	Criteria	Failures	Task On Actual	Task Off Actual	Comments
12	Pre_Conditioning_Level_1	NA	MSL 1	0	08-31-2022	08-31-2022	
13	ROSE Clean/ Test	231	Pass on Zero Fails	0	08-29-2022	08-29-2022	
14	Pre_Elect_Precond	231	Pass on Zero Fails	0	08-30-2022	08-30-2022	
15	Precond_Temp_Cyc_5cyc	231	Pass on Zero Fails	0	08-30-2022	08-30-2022	
16	Precond_HTS_24hr	231	Pass on Zero Fails	0	08-30-2022	08-31-2022	
17	Precond_85/85_NoElec168hr	231	Pass on Zero Fails	0	08-31-2022	09-07-2022	
18	Precond_260°C_IR_Ref_Char	231	Pass on Zero Fails	0	09-07-2022	09-07-2022	
19	T/C_Pre_Elect	77	Pass on Zero Fails	0	09-07-2022	09-07-2022	
20	T/C_wPre_0250	77	Pass on Zero Fails	0	09-07-2022	09-13-2022	
21	T/C_wPre_0500	77	Pass on Zero Fails	0	09-13-2022	09-19-2022	
22	Cross_Section TC 500 Cyc	5	Pass on Zero Fails	0	09-19-2022	09-20-2022	
23	T/C_wPre_1000	77	Pass on Zero Fails	0	09-20-2022	09-28-2022	
24	HAST Pre_Elect	77	Pass on Zero Fails	0	09-07-2022	09-07-2022	
25	HAST_BD_Validation	N/A	Pass on Zero Fails	0	09-08-2022	09-08-2022	
26	HAST_wPRE_264 Hrs 110°C	77	Pass on Zero Fails	0	09-16-2022	09-28-2022	
27	HAST Pre_Elect	77	Pass on Zero Fails	0	09-07-2022	09-07-2022	
28	HAST_unbias_264hrs_110°C	77	Pass on Zero Fails	0	09-16-2022	09-28-2022	
29	CSAM Analysis	22	Pass on Zero Fails	0	09-12-2022	09-12-2022	
30	Precond_Temp_Cyc_5cyc	22	Pass on Zero Fails	0	09-12-2022	09-12-2022	
31	Precond_HTS_24hr	22	Pass on Zero Fails	0	09-12-2022	09-13-2022	
32	Precond_85/85_NoElec168hr	22	Pass on Zero Fails	0	09-13-2022	09-20-2022	
33	Precond_260°C_IR_Ref_Char	22	Pass on Zero Fails	0	09-20-2022	09-20-2022	
34	CSAM Analysis	22	Pass on Zero Fails	0	09-20-2022	09-20-2022	
35	Pack_Clos	0	0	0	10-12-2022	10-12-2022	

Sub Lot#	Sub Lot # Part		Lot	t Assembly Lot		Date Code		
3 RClamp2402B			1	AER-009211		2233		
Task#	Task Code	Sample Size	Criteria		Failures	Task On Actual	Task Off Actual	Comments
1	Data-Prep	None	None	0		09-07-2022	09-07-2022	
2	HTRB_Pre_Elect_150°C_RT24	105	Pass on Zero Fails	0		09-13-2022	09-13-2022	
3	HTRB_150°C_Real Time_0024	105	Pass on Zero Fails	0		09-14-2022	09-15-2022	
4	HTRB_Pre_Elect	105	Pass on Zero Fails	0		09-09-2022	09-09-2022	

Task#	Task Code	Sample Size	Criteria	Failures	Task On Actual	Task Off Actual	Comments
5	BI_BD_Valid	105	Meet HTOL Schematics	0	09-09-2022	09-09-2022	
6	HTRB_150°C_0072	105	Pass on Zero Fails	0	09-09-2022	09-12-2022	
7	HTRB_150°C _0408	105	Pass on Zero Fails	0	09-12-2022	09-26-2022	
8	HTS_Pre_Elect	77	Pass on Zero Fails	0	09-08-2022	09-08-2022	
9	HTS_0168	77	Pass on Zero Fails	0	09-08-2022	09-15-2022	
10	HTS_0500	77	Pass on Zero Fails	0	09-15-2022	09-29-2022	
11	HTS_1000	77	Pass on Zero Fails	0	09-29-2022	10-20-2022	
12	Pre_Conditioning_Level_1	NA	MSL 1	0	09-09-2022	09-09-2022	
13	ROSE Clean/ Test	231	Pass on Zero Fails	0	09-08-2022	09-08-2022	
14	Pre_Elect_Precond	231	Pass on Zero Fails	0	09-12-2022	09-12-2022	
15	Precond_Temp_Cyc_5cyc	231	Pass on Zero Fails	0	09-12-2022	09-12-2022	
16	Precond_HTS_24hr	231	Pass on Zero Fails	0	09-12-2022	09-13-2022	
17	Precond_85/85_NoElec168hr	231	Pass on Zero Fails	0	09-13-2022	09-20-2022	
18	Precond_260°C_IR_Ref_Char	231	Pass on Zero Fails	0	09-20-2022	09-20-2022	
19	T/C_Pre_Elect	77	Pass on Zero Fails	0	09-20-2022	09-20-2022	
20	T/C_wPre_0250	77	Pass on Zero Fails	0	09-20-2022	09-26-2022	
21	T/C_wPre_0500	77	Pass on Zero Fails	0	09-26-2022	09-30-2022	
22	Cross_Section TC 500 Cyc	5	Pass on Zero Fails	0	09-30-2022	09-30-2022	
23	T/C_wPre_1000	77	Pass on Zero Fails	0	09-30-2022	10-10-2022	
24	HAST Pre_Elect	77	Pass on Zero Fails	0	09-20-2022	09-20-2022	
25	HAST_BD_Validation	77	Pass on Zero Fails	0	09-16-2022	09-16-2022	
26	HAST_wPRE_264 Hrs 110°C	77	Pass on Zero Fails	0	09-22-2022	10-04-2022	
27	HAST Pre_Elect	77	Pass on Zero Fails	0	09-20-2022	09-20-2022	
28	HAST_unbias_264hrs_110°C	77	Pass on Zero Fails	0	09-22-2022	10-04-2022	
29	CSAM Analysis	22	Pass on Zero Fails	0	09-22-2022	09-22-2022	
30	Precond_Temp_Cyc_5cyc	22	Pass on Zero Fails	0	09-26-2022	09-26-2022	
31	Precond_HTS_24hr	22	Pass on Zero Fails	0	09-26-2022	09-27-2022	
32	Precond_85/85_NoElec168hr	22	Pass on Zero Fails	0	09-27-2022	10-04-2022	
33	Precond_260°C_IR_Ref_Char	22	Pass on Zero Fails	0	10-04-2022	10-04-2022	
34	CSAM Analysis	22	Pass on Zero Fails	0	10-04-2022	10-05-2022	
35	Pack_Clos	0	0	0	10-24-2022	10-24-2022	

© Semtech Confidential fxu@semtech.com 2022-10-25 18:48:35

Feng Xu



RClamp2402B Low Capacitance RClamp® ESD and EOS Protection

PROTECTION PRODUCTS

Description

RClamp® TVS diodes are designed to protect sensitive electronics from damage or latch-up due to ESD & EOS. These devices offer desirable characteristics for board level protection including fast response time, low operating and low clamping voltage.

RClamp2402B has a typical capacitance of only 0.7pF (pin 1 to 2). This means it can be used on circuits operating in excess of 3GHz without signal attenuation. They may be used to meet the ESD immunity requirements of IEC 61000-4-2, Level 4 (± 25 kV air, ± 15 kV contact discharge). Each device can be configured to protect one bidirectional line or two unidirectional lines.

These devices are in a small SC-75 (SOT-523) package and feature a lead-free, matte tin finish. They are compatible with both lead free and SnPb assembly techniques. They are designed for use in applications where board space is at a premium. The combination of small size, low capacitance, and high level of ESD protection makes them a flexible solution for applications such as Automatic Test Equipment and USB 2.0.

Features

- Transient protection for high-speed data lines to IEC 61000-4-2 (ESD) ±25kV (air), ±15kV (contact) IEC 61000-4-4 (EFT) 40A (5/50ns)
- Protects up to two I/O lines
- Low Line-to-Line capacitance: 0.7pF(Typical)
- Low insertion loss to >3.0GHz
- Low clamping voltage
- Operating voltage: 24V
- Solid-state silicon-avalanche technology

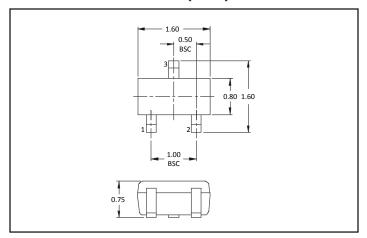
Mechanical Characteristics

- SC-75 (SOT-523) package
- · Lead Finish: Matte Tin
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Molding compound flammability rating: UL 94V-0
- Packaging: Tape and Reel

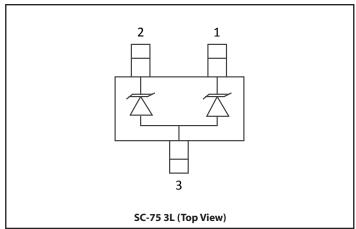
Applications

- Cellular Handsets & Accessories
- Notebook Computers
- Tablets
- Portable Instrumentation
- USB 2.0
- · Firewire Ports

Nominal Dimensions (mm)



Schematic & Pin Configuration



Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = $8/20\mu s$)	P _{PK}	200	W
Peak Pulse Current (tp = 8/20μs)	I _{PP}	4	A
ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V _{ESD}	±25 ±15	kV
Junction Temperature Operating Temperature	T _J &T _{OP}	-55 to +125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

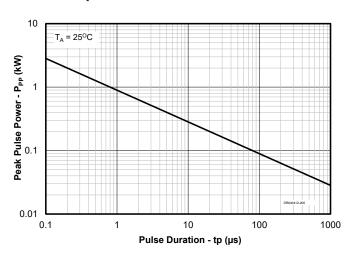
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units	
Reverse Stand-Off Voltage	V _{RWM}	Between any two pins				24	V
Reverse Breakdown Voltage	V _{BR}	It = 1mA , between an	y two pins	26.7			V
Reverse Leakage Current	I _R	V _{RWM} = 24V, between a	ny two pins			1	μΑ
		t _p = 8/20μs Pin 1 to Pin 2	I _{pp} = 1A			45	
Clamping Voltage	V _c	$t_p = 8/20\mu s$ Pin 1 or Pin 2 to Pin 3	$I_{pp} = 4A$			50	V
		t _p = 8/20μs Pin 1 to Pin 2	I _{pp} = 4A			55	
Junction Capacitance	C _J	$V_R = 0V$, $f = 1MHz$, Pin 1 to Pin 2			0.7	1	рF
Junction Capacitance	C _J	$V_R = 0V, f = 1MHz, Pin$	1 or Pin 2 to Pin 3			1.65	рF

Note:

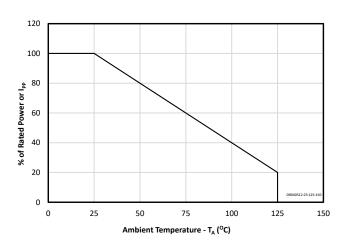
1) ESD gun return path connected to ESD ground plane.

Typical Characteristics

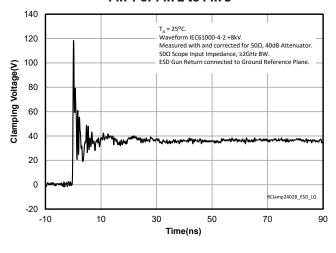
Non Repetitive Peak Pulse Power vs. Pulse Time



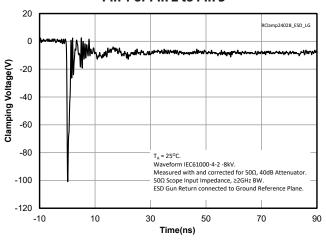
Power Derating Curve



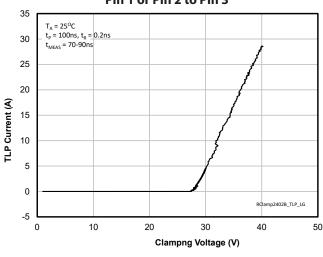
ESD Clamping (8kV Contact per IEC 61000-4-2) Pin 1 or Pin 2 to Pin 3



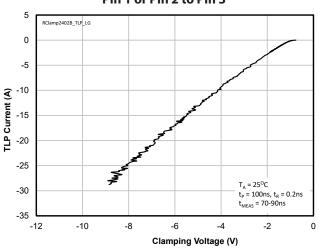
ESD Clamping (-8kV Contact per IEC 61000-4-2)
Pin 1 or Pin 2 to Pin 3



TLP Characteristic (Positive) Pin 1 or Pin 2 to Pin 3

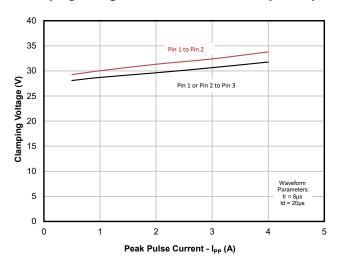


TLP Characteristic (Negative)
Pin 1 or Pin 2 to Pin 3

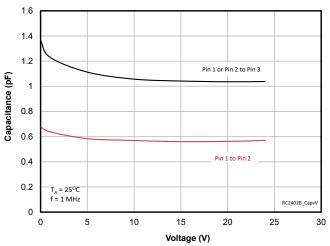


Typical Characteristics (Continued)

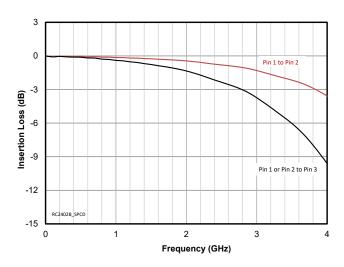
Clamping Voltage vs. Peak Pulse Current (tp=8/20µs)



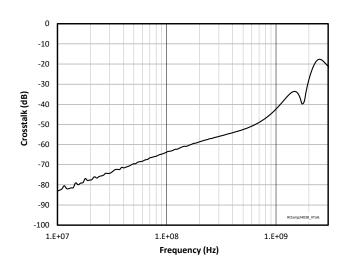
Capacitance vs. Reverse Voltage



Insertion Loss S21



Crosstalk



Application Information

Device Connection Options

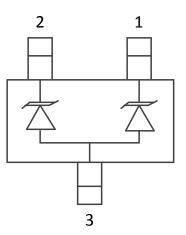
This device is optimized for protection of 1 line operating in excess of 3GHz. It may also be used to protect two lines operating in excess of 2GHz. The device is connected as follows: Protection for one line with <1pF capacitance can be achieved by connecting one data line to either pin 1 or pin 2 with the other pin connected to ground. Pin 3 is not connected. The connection to ground should be made directly to a ground plane. The path length should also be kept as short as possible to minimize parasitic inductance.

Protection of two lines is achieved by connecting data lines at pins 1 & 2. Pin 3 is connected to ground. The connection to ground should be made directly to a ground plane. The path length should also be kept as short as possible to minimize parasitic inductance.

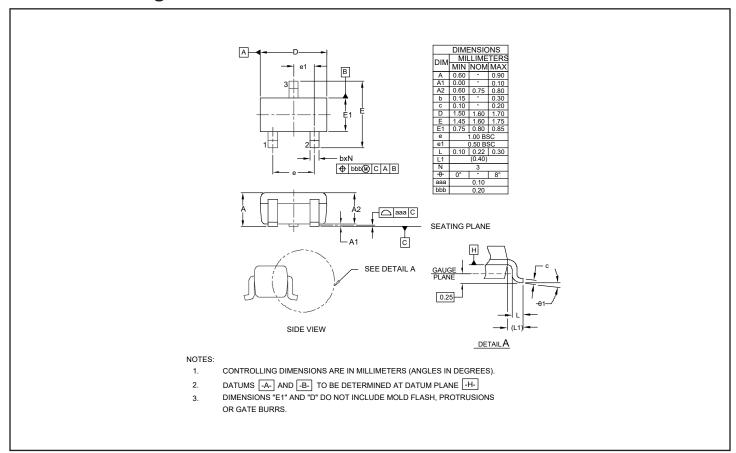
Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

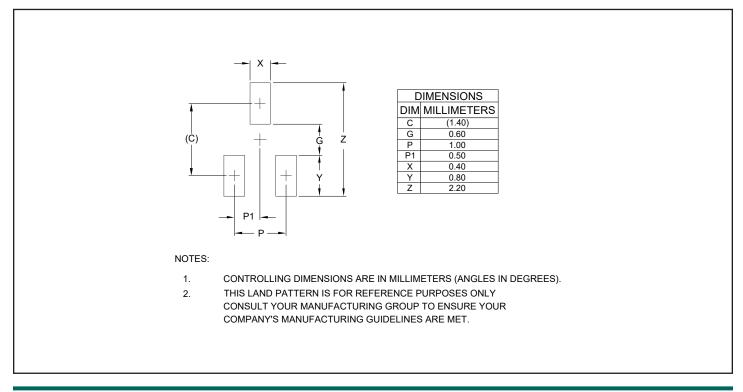
Figure 1. Pin Configuration



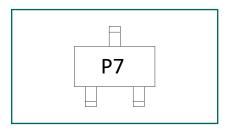
Outline Drawing - SC75 3L



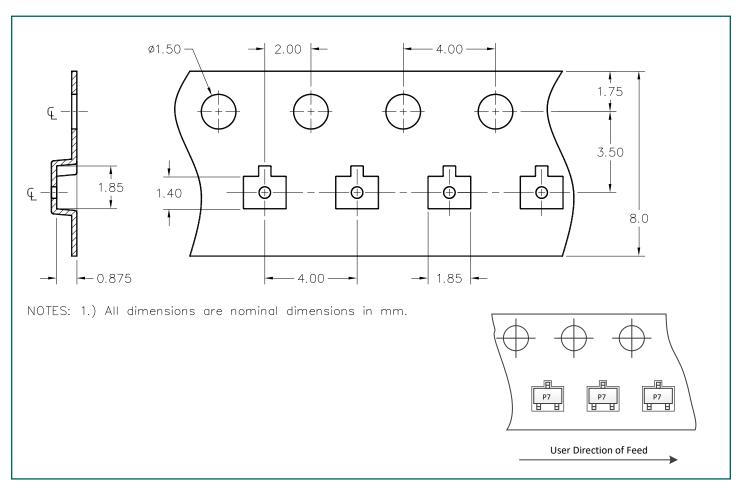
Land Pattern - SC75 3L



Marking Code



Tape and Reel Specification



Ordering Information

Part Number	Qty per Reel	Reel Size	
RClamp2402B.TCT	3,000	7"	



Important Notice

Information relating to this product and the application or design described herein is believed to be reliable, however such information is provided as a guide only and Semtech assumes no liability for any errors in this document, or for the application or design described herein. Semtech reserves the right to make changes to the product or this document at any time without notice. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. Semtech warrants performance of its products to the specifications applicable at the time of sale, and all sales are made in accordance with Semtech's standard terms and conditions of sale.

SEMTECH PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS, OR IN NUCLEAR APPLICATIONS IN WHICH THE FAILURE COULD BE REASONABLY EXPECTED TO RESULT IN PERSONAL INJURY, LOSS OF LIFE OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. INCLUSION OF SEMTECH PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE UNDERTAKEN SOLELY AT THE CUSTOMER'S OWN RISK. Should a customer purchase or use Semtech products for any such unauthorized application, the customer shall indemnify and hold Semtech and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs damages and attorney fees which could arise.

The Semtech name and logo are registered trademarks of the Semtech Corporation. All other trademarks and trade names mentioned may be marks and names of Semtech or their respective companies. Semtech reserves the right to make changes to, or discontinue any products described in this document without further notice. Semtech makes no warranty, representation or guarantee, express or implied, regarding the suitability of its products for any particular purpose. All rights reserved.

© Semtech 2022

Contact Information

Semtech Corporation 200 Flynn Road, Camarillo, CA 93012 Phone: (805) 498-2111, Fax: (805) 498-3804 www.semtech.com



RClamp2402B.TCT Characterization Report:

Les - 09/15/2022

Test Summary



RCIamp2402B.TCT								AER-9211	POR
Parameter	Symbol	Conditions		Units	Min.	Тур.	Max.	Ave.	Ave.
Reverse Stand-Off Voltage	V_{RWM}	L-G, L-L		V			24		
Reverse Breakdown Voltage	V_{BR}	I _t = 1mA, L-G. L-L		V	26.7			27.3	32.6
Reverse Leakage Current	I _R	V _{RWM} = 24V, L-G or L-L		μA			1	0.003	0.2
Clamping Voltage	V _C	tp = 8/20μs	I _{PP} = 4A, L-G	V			50	32.0	40.8
			I _{PP} = 1A, L-L				45	30.1	36.9
			I _{PP} = 4A, L-L				55	33.9	43.3
Junction Capacitance	Cì	V _R =0V, f = 1MHz	L-G	pF			1.65*	1.44	0.6
			L-L			0.6	1	0.70	0.3
Peak Pulse Current	I _{PP}	t _P = 8/20µs		Α			4	10	7
Peak Pulse Power	P _{PP}	t _P = 8/20µs		W			200	398.08	332.02
ESD per IEC 61000-4-2	V _{ESD}	Contact		kV			±15	±30	±20
		Air					±25	±30	±30

