

Product Change Notification / SYST-10UFBM142

Date:

12-May-2022

Product Category:

Clock and Timing - Clock Generation

PCN Type:

Document Change

Notification Subject:

Data Sheet - ZL30270/71 Data Sheet Document Revision

Affected CPNs:

SYST-10UFBM142_Affected_CPN_05122022.pdf SYST-10UFBM142_Affected_CPN_05122022.csv

Notification Text:

SYST-10UFBM142

Microchip has released a new Product Documents for the ZL30270/71 Data Sheet of devices. If you are using one of these devices please read the document located at ZL30270/71 Data Sheet.

Notification Status: Final

Description of Change:

- 1) xo_amp-set: Corrected the xtal_drive_level description to what it should be. Previously it was accidentally the same text as the xo_osci_sel description2) Table 4-1: In the GPIO description added new recommendation
- 3) Figure 5-1: Added a footnote to part e).
- 4) Section 5.1.4, synth_spread_spectrum_cfg, synth_spred_spectrum_rate, synth_spread_spectrum_spread: Documented these registers
- 5) Figure 9-1: Corrected VOC to VOUT in several places
- 6) Section 5.2.1: Correct last paragraph about voltages allowed for LVDS and LVPECL

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 12 May 2022

NOTE: Please be advised that this is a change to the document only the product has not been changed. Markings to Distinguish Revised from Unrevised Devices: N/A	
Attachments:	
ZL30270/71 Data Sheet	
Please contact your local Microchip sales office with questions or concerns regarding this notification.	
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Affected Catalog Part Numbers(CPN)

ZL30270LDG1S ZL30271LDG1