

#### **Product Change Notification / SYST-09AZHA626**

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10-May-2022

# **Product Category:**

**Power MOSFET Drivers** 

# **PCN Type:**

**Document Change** 

# **Notification Subject:**

Data Sheet - MIC4467/4468/4469 Data Sheet Document Revision

#### **Affected CPNs:**

SYST-09AZHA626\_Affected\_CPN\_05102022.pdf SYST-09AZHA626\_Affected\_CPN\_05102022.csv

#### **Notification Text:**

SYST-09AZHA626

Microchip has released a new Product Documents for the MIC4467/4468/4469 Data Sheet of devices. If you are using one of these devices please read the document located at MIC4467/4468/4469 Data Sheet.

Notification Status: Final

#### **Description of Change:**

- 1) Converted Micrel document MIC4467/8/9 to Microchip data sheet DS20006614A.
- 2) Minor text changes throughout.

Impacts to Data Sheet: See above details.

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 10 May 2022

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:	
MIC4467/4468/4	469 Data Sheet
Please contact you	r local Microchip sales office with questions or concerns regarding this notification.
Ferms and Conditi	ons:
home page select i	ive Microchip PCNs via email please register for our PCN email service at our PCN register then fill in the required fields. You will find instructions about registering for nail service in the PCN FAQ section.
	ge your PCN profile, including opt out, please go to the PCN home page select login myMicrochip account. Select a profile option from the left navigation bar and make ctions.



# MIC4467/8/9

# **Quad 1.2A Peak Low-Side MOSFET Drivers**

#### **Features**

- Reliable, Low-Power Bipolar/CMOS/DMOS Construction
- Latch-Up Protected to >500 mA Reverse Current
- Logic Input withstands Swing to -5V
- High 3A Peak Output Current
- · Wide 4.5V to 18V Operating Range
- · Symmetrical Rise and Fall Times
- Short <40 ns Typical Delay Time
- TTL Logic Input Independent of Supply Voltage
- · Low Equivalent 6 pF Input Capacitance
- Low 5Ω Typical Output Impedance
- Output Voltage Swings within 25 mV of Ground or  $V_S$ .

#### **Applications**

- · General-Purpose CMOS Logic Buffer
- · Driving All 4 MOSFETs in an H-Bridge
- · Direct Small Motor Driver
- · Relay or Peripheral Drivers
- · Dual Differential Output Power Drivers
- · CCD Driver
- · Pin Switching Network Driver

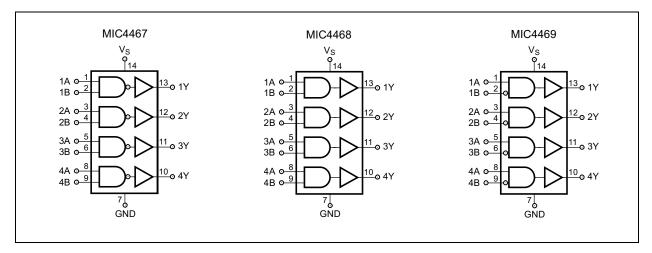
#### **General Description**

The MIC4467/8/9 family of four output CMOS buffer/drivers is an expansion from the earlier single-and dual-output drivers, to which they are functionally closely related. Because package pin count permitted it, each driver has been equipped with a dual input logic gate for added flexibility. Placing four high-power drivers in a single package also improves system reliability and reduces total system cost. In some applications, one of these drivers can replace not only two packages of single-input drivers, but some of the associated logic as well.

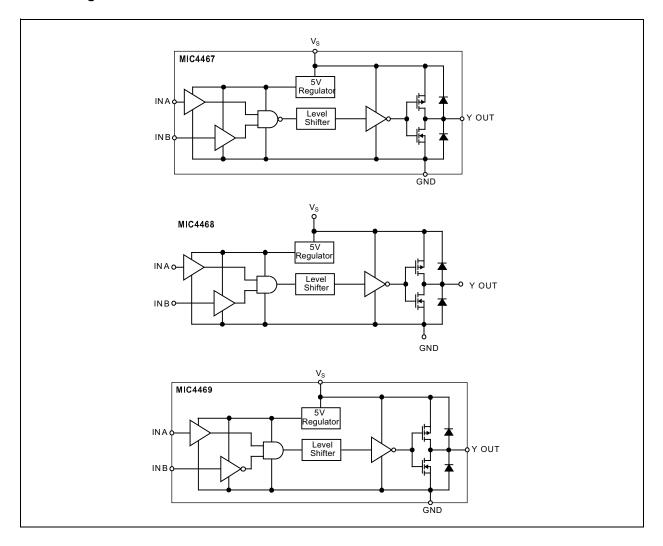
Although primarily intended for driving power MOSFETs, and similar highly capacitive loads, these drivers are equally well suited to driving any other load (capacitive, resistive, or inductive), which requires high efficiency, low-impedance driver capable of high peak currents, rail-to-rail voltage swings, and fast switching times. For example, heavily loaded clock lines, coaxial cables, and piezoelectric transducers can all be driven easily with MIC446x series drivers. The only limitation on loading is that total power dissipation in the IC must be kept within the power dissipation limits of the package.

The MIC446x series drivers are built using a BCD process. They will not latch under any conditions within their power and voltage ratings. They are not subject to damage when up to 5V of noise spiking (either polarity) occurs on the ground line. They can accept up to half an amp of inductive kickback current (either polarity) into their outputs without damage or logic upset.

# **Logic Diagrams**



# **Block Diagrams**



#### 1.0 ELECTRICAL CHARACTERISTICS

#### **Absolute Maximum Ratings †**

Supply Voltage+2:	2V
nput VoltageV <sub>S</sub> + 0.3V to GND - s	5V

#### **Operating Ratings ‡**

Power Dissipation	
N Package (14-Pin Plastic DIP)	1.5W
WM package (16-Pin Wide SOIC)	1W

**† Notice:** Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability. Specifications are for packaged product only.

**‡ Notice:** The device is not guaranteed to function outside its operating ratings.

**Note 1:** Devices are ESD sensitive. Handling precautions are recommended. Human body model, 1.5 k $\Omega$  in series with 100 pF.

#### **ELECTRICAL CHARACTERISTICS**

**Electrical Characteristics:** Measured at  $T_A = +25^{\circ}\text{C}$  with  $4.5\text{V} \le V_S \le 18\text{V}$  unless otherwise specified. (Note 1)

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Input							
Logic 1 Input Voltage	V <sub>IH</sub>	2.4	1.3	_	V	_	
Logic 0 Input Voltage	$V_{IL}$	_	1.2	0.8	V	_	
Input Current	I <sub>IN</sub>	-1	_	1	μA	$0V \le V_{ N} \le V_{S}$	
Output					-		
High Output Voltage	V <sub>OH</sub>	V <sub>S</sub> - 0.15		_	V	I <sub>LOAD</sub> = 10 mA	
Low Output Voltage	$V_{OL}$	_		0.15	V	I <sub>LOAD</sub> = 10 mA	
Output Resistance	Ro	_	5	15	Ω	I <sub>OUT</sub> = 10 mA, V <sub>S</sub> = 18V	
Peak Output Current	I <sub>PK</sub>		1.2	_	Α	_	
Latch-Up Protection Withstand Reverse Current	ı	>500		_	mA	_	
Switching Time							
Rise Time	t <sub>R</sub>	_	14	25	ns	Figure 1-1	
Fall time	t <sub>F</sub>	_	13	25	ns	Figure 1-1	
Dolov Timo	t <sub>D1</sub>	_	30	75	ns	Figure 1-1	
Delay Time	t <sub>D2</sub>	_	45	75	ns	Figure 1-1	
Power Supply							
Power Supply Current	ls	_	0.2	4	mA	—	
					•	•	

Note 1: Specification for packaged product only.

#### **ELECTRICAL CHARACTERISTICS**

**Electrical Characteristics:** Measured over operating temperature range with  $4.5V \le V_S \le 18V$  unless otherwise specified. (Note 1)

Parameter	Symbol	Min.	Тур.	Max.	Units	Conditions	
Input							
Logic 1 Input Voltage	$V_{IH}$	2.4	1.4	_	V	_	
Logic 0 Input Voltage	$V_{IL}$	_	1.0	0.8	V	_	
Input Current	I <sub>IN</sub>	-1	_	1	μA	$0V \le V_{IN} \le V_{S}$	
Output							
High Output Voltage	$V_{OH}$	V <sub>S</sub> - 0.3	_	_	V	I <sub>LOAD</sub> = 10 mA	
Low Output Voltage	$V_{OL}$	_	_	0.3	V	I <sub>LOAD</sub> = 10 mA	
Output Resistance	Ro	_	7	30	Ω	I <sub>OUT</sub> = 10 mA, V <sub>S</sub> = 18V	
Peak Output Current	I <sub>PK</sub>	_	1.2	_	Α	_	
Latch-Up Protection Withstand Reverse Current	Ι	500	_	_	mA	_	
Switching Time							
Rise Time	₽	_	17	50	ns	Figure 1-1	
Fall time	t <sub>F</sub>	_	16	50	ns	Figure 1-1	
Doloy Time	<sup>t</sup> D1	_	35	100	ns	Figure 1-1	
Delay Time	t <sub>D2</sub>	_	55	100	ns	Figure 1-1	
Power Supply	Power Supply						
Power Supply Current	l <sub>S</sub>		0.4	8	mA	_	

Note 1: Specification for packaged product only.

# **TEMPERATURE SPECIFICATIONS (Note 1)**

	•						
Parameters	Symbol	Min.	Тур.	Max.	Units	Conditions	
Temperature Ranges							
On a ratio at Austria at Tauran a rational	т		°C	Temperature Range Device: Y			
Operating Ambient Temperature	T <sub>A</sub> 0 —	_	+70	°C	Temperature Range Device: Z		
Maximum Junction Temperature	TJ	_	_	+150	°C	_	
Storage Temperature Range	Ts	<del>-</del> 65	_	+150	°C	_	
Lead Temperature	T <sub>LEAD</sub>	_	_	+300	°C	Soldering, 10 sec.	
Package Thermal Resistances							
Thermal Resistance 14-Lead PDIP	$\theta_{JA}$	_	80	_	°C/W	_	
Thermal Resistance 16-Lead Wide SOIC	$\theta_{JA}$	_	120	_	C/VV	_	

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e.,  $T_A$ ,  $T_J$ ,  $\theta_{JA}$ ).

#### **Test Circuits**

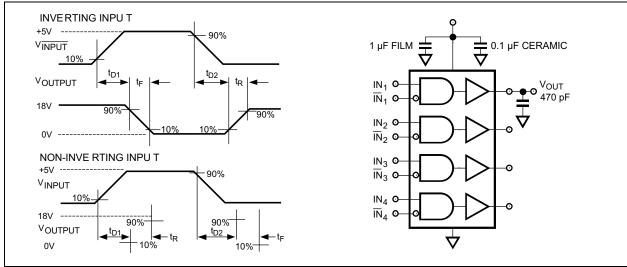


FIGURE 1-1: Inverting and Non-Inverting Input.

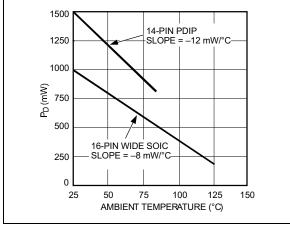


FIGURE 1-2: Package Power Dissipation.

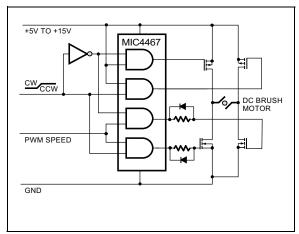


FIGURE 1-3: Quad Driver Drives H Bridge to Control motor Speed and Direction.

#### 2.0 TYPICAL PERFORMANCE CURVES

The graphs and tables provided following this note are a statistical summary based on a limited number of Note: samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

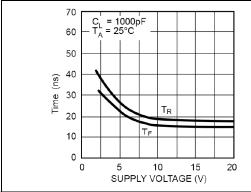


FIGURE 2-1: Supply Voltage.

Rise and Fall Time vs.

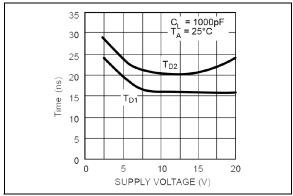


FIGURE 2-2: Voltage.

Delay Time vs. Supply

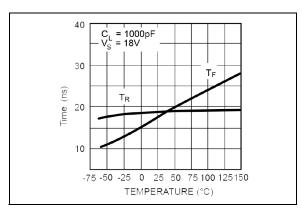


FIGURE 2-3: Temperature.

Rise and Fall Time vs.

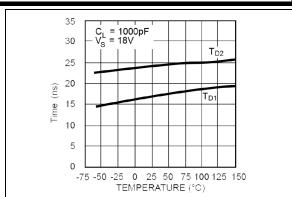


FIGURE 2-4: Temperature.

Delay Time vs.

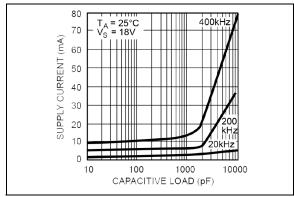


FIGURE 2-5: Capacitive Load.

Supply Current vs.

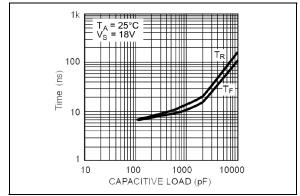


FIGURE 2-6:

Rise and Fall Time vs.

Capacitive Load.

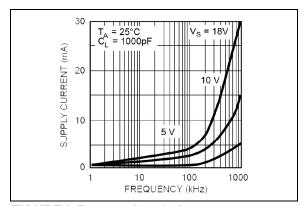


FIGURE 2-7: Frequency.

Supply Current vs.

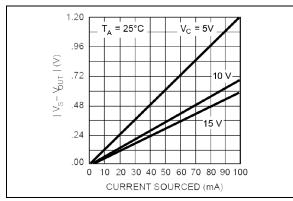


FIGURE 2-8:

High Output vs. Current.

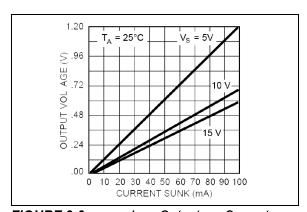


FIGURE 2-9:

Low Output vs. Current.

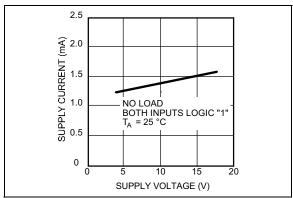


FIGURE 2-10: Quiescent Power Supply Current vs. Supply Voltage.

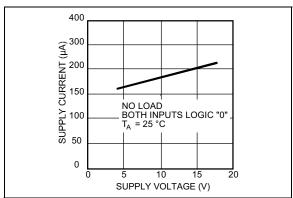
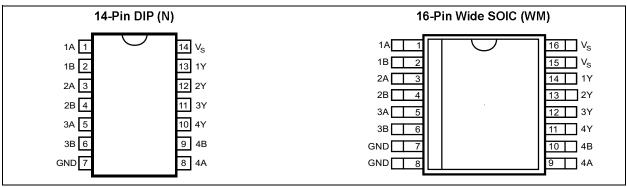


FIGURE 2-11: Quiescent Power Supply Current vs. Supply Voltage.

#### 3.0 PIN DESCRIPTIONS

# Package Types



The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

., (BLL 0 1)	1 1111 0110 11011					
Pin Number DIP	Pin Number Wide SOIC	Pin Name	Description			
1	1	1A	Input A for Driver 1. TTL/CMOS Compatible Input			
2	2	1B	Input B for Driver 1. TTL/CMOS Compatible Input			
3	3	2A	Input A for Driver 2. TTL/CMOS Compatible Input			
4	4	2B	Input B for Driver 2. TTL/CMOS Compatible Input			
5	5	3A	Input A for Driver 3. TTL/CMOS Compatible Input			
6	6	3B	Input B for Driver 3. TTL/CMOS Compatible Input			
7	7	GND	Ground			
8	_	4A	Input A for Driver 4. TTL/CMOS Compatible Input			
_	8	GND	Ground			
9	_	4B	Input B for Driver 4. TTL/CMOS Compatible Input			
_	9	4A	Input A for Driver 4. TTL/CMOS Compatible Input			
10	_	4Y	Output for Driver 4, CMOS Push-Pull Output			
_	10	4B	Input B for Driver 4. TTL/CMOS Compatible Input			
11	_	3Y	Output for Driver 3, CMOS Push-Pull Output			
_	11	4Y	Output for Driver 4, CMOS Push-Pull Output			
12	_	2Y	Output for Driver 2, CMOS Push-Pull Output			
_	12	3Y	Output for Driver 3, CMOS Push-Pull Output			
13	_	1Y	Output for Driver 1, CMOS Push-Pull Output			
_	13	2Y	Output for Driver 2, CMOS Push-Pull Output			
14		VS	Supply Input, 4.5V to 18V			
	14	1Y	Output for Driver 1, CMOS Push-Pull Output			
	15	VS	Supply Input, 4.5V to 18V			
	16	VS	Supply Input, 4.5V to 18V			

# 3.1 Truth Table

TABLE 3-2: TRUTH TABLE

Dowl No.	Inp	Output	
Part No.	А	В	Υ
1410.4407	L	X	Н
MIC4467 (Each Driver)	Х	L	Н
(Lacii Bilvei)	Н	Н	L
1410.4400	Н	Н	Н
MIC4468 (Each Driver)	L	Х	L
(Lacii Dilvei)	Х	L	L
MIC 4400	L	Х	L
MIC4469 (Each Driver)	Х	Н	L
(Lacii Dilvei)	Н	L	Н

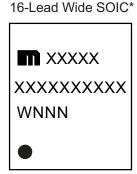
#### 4.0 PACKAGING INFORMATION

#### 4.1 Package Marking Information





Example





XX...X Product code or customer-specific information Legend: Υ Year code (last digit of calendar year) ΥY Year code (last 2 digits of calendar year) WW Week code (week of January 1 is week '01') NNN Alphanumeric traceability code Pb-free JEDEC® designator for Matte Tin (Sn) (e3) This package is Pb-free. The Pb-free JEDEC designator (@3)) can be found on the outer packaging for this package. •, ▲, ▼ Pin one index is identified by a dot, delta up, or delta down (triangle mark). Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo. Underbar (\_) and/or Overbar (\_) symbol may not be to scale.

**Note:** If the full seven-character YYWWNNN code cannot fit on the package, the following truncated codes are used based on the available marking space:

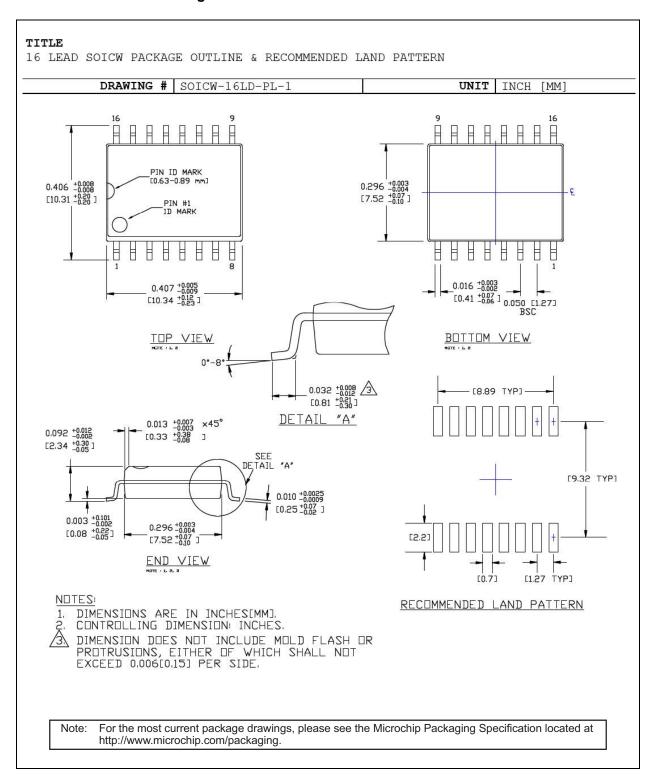
6 Characters = YWWNNN; 5 Characters = WWNNN; 4 Characters = WNNN; 3 Characters = NNN;

2 Characters = NN; 1 Character = N

#### 14-Lead Plastic DIP Package Outline and Recommended Land Pattern

# TITLE 14 LEAD PDIP PACKAGE OUTLINE & RECOMMENDED LAND PATTERN DRAWING # PDIP-14LD-PL-1 UNIT INCH LEAD FRAME Copper LEAD FINISH Matte Tin Ø.080±.005 0.013DP MAX (3 PLACES) .005 MIN .130±.008 .310 BSC .015 GAGE PLANE .085±.020 .085±.020 150 MAX 125 MIN BASE MATERIAL SECTION A-A .050 TYP .100 ⊕ .018±.004 ⊕ .010@ C For the most current package drawings, please see the Microchip Packaging Specification located at Note: http://www.microchip.com/packaging.

#### 16-Lead Wide SOIC Package Outline and Recommended Land Pattern



# APPENDIX A: REVISION HISTORY

# Revision A (May 2022)

- Converted Micrel document MIC4467/8/9 to Microchip data sheet DS20006614A.
- Minor text changes throughout.



**NOTES:** 

# PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

	v	vv	vv	Example	s:	
PART NO. Device	Temperature Range	XX   Package	<u>–XX</u>   Media Type	a) MIC4467	<b>7</b> :	1.2A-Peak, Quad Low-Side MOSFET Driver, NAND Input Logic, –40°C to +85°C Industrial Temperature Range, RoHS Compliant
				MIC4467Y\	ΛM	16-Lead SOIC Wide Package, 47/Tube
	MIC4467:	Quad 1.2A-Peak Low-S	Side MOSFET Driver	MIC4467Y\	VM-TR	16-Lead SOIC Wide Package, 1,000/Reel
	MIC4468:	with Bi-Polar/CMOS/D ing NAND Input Logic Quad 1.2A-Peak Low-		b) MIC4467	7:	1.2A-Peak, Quad Low-Side MOSFET Driver, NAND Input Logic, 0°C to +70°C Commercial Temperature Range, RoHS Compliant
Device:		with Bi-Polar/CMOS/D		MIC4467ZI	٧	14-Lead PDIP Package, 25/Tube
		featuring AND Input Lo	ogic	MIC4467Z\	MM	16-Lead SOIC Wide Package, 47/Tube
	MIC4469:	Quad 1.2A-Peak Low-	Side MOSFET Driver	MIC4467ZV	VM-TR	16-Lead SOIC Wide Package, 1000/Reel
		with Bi-Polar/CMOS/DI featuring AND with 1 Ir		c) MIC4468	3:	1.2A-Peak, Quad Low-Side MOSFET Driver, AND Input Logic, –40°C to +85°C Industrial Temperature Range, RoHS Compliant
Temperature Range:	Y =	-40°C to +85°C, Indust	trial	MIC4468YI	٧	14-Lead PDIP Package, 25/Tube
	Z =	(RoHs Compliant) 0°C to +70°C, Commer	rcial	MIC4468Y\	ΛM	16-Lead SOIC Wide Package, 47/Tube
	_	(RoHs Compliant)	. •	MIC4468Y\	VM-TR	16-Lead SOIC Wide Package, 1,000/Reel
Package:	N = WM =	14-Lead PDIP 16-Lead SOIC (Wide B	3ody)	d) MIC4468	3:	1.2A-Peak, Quad Low-Side MOSFET Driver, AND Input Logic, 0°C to +70°C Commercial Temperature Range, RoHS Compliant
				MIC4468ZI	٧	14-Lead PDIP Package, 25/Tube
Media Type:	<blank> =</blank>	25/Tube (N, PDIP)		MIC4468Z\	MM	16-Lead SOIC Wide Package, 47/Tube
,	<blank> =</blank>	47/Tube (WM, SOIC)		MIC4468ZV	VM-TR	16-Lead SOIC Wide Package, 1,000/Reel
	TR =	1,000/Reel (WM, SOIC	<i>;</i> )	MIC4469:		1.2A-Peak, Quad Low-Side MOSFET Driver, AND with 1 Inverting Input Logic, -40°C to +85°C Industrial Temperature Range, RoHS Compliant
				MIC4469Y1	٧	14-Lead PDIP Package, 25/Tube
				MIC4469Y\	ΛM	16-Lead SOIC Wide Package, 47/Tube
				MIC4469Y\	VM-TR	16-Lead SOIC Wide Package, 1,000/Reel
				MIC4469:		1.2A-Peak, Quad Low-Side MOSFET Driver, AND with 1 Inverting Input Logic, -40°C to +85°C Commercial Temperature Range, RoHS Compliant
				MIC4469ZI	٧	14-Lead PDIP Package, 25/Tube
				MIC4469Z\	MM	16-Lead SOIC Wide Package, 47/Tube
				MIC4469W	M-TR	16-Lead SOIC Wide Package, 1,000/Reel
				Note 1:	part number ordering pu package. C	Reel identifier only appears in the catalog er description. This identifier is used for urposes and is not printed on the device Check with your Microchip Sales Office for vailability with the Tape and Reel option.



**NOTES:** 

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# Affected Catalog Part Numbers(CPN)

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MIC4467ZWM

MIC4467ZWM-TR

MIC4468YN

MIC4468YWM

MIC4468YWM-TR

MIC4468ZN

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