Information note

N° 10321AERRA

Dear customer,

With this Infineon Technologies AG information note, we would like to inform you about the following

HSM_MEM Release Notes Addendum V2.0 affecting products TC3xx

On 16 April 2020, Infineon acquired Cypress.
We are now in the process of merging and consolidating our tools and processes for PCN, Information Notes, Errata and Product Discontinuance.
For further details, please visit our website:
https://www.infineon.com/cms/en/about-infineon/company/cypress-acquisition/

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Information note

N° 10321AERRA

▶ Products affected

Please refer to attached affected product list 1_cip10321

▶ Detailed change information

Subject

HSM_MEM Release Notes Addendum V2.0 affecting products TC3xx

Reason

Update of the Release Notes Addendum due to new known issues

Description

Old	New
■ Release Notes Addendum V1.0	■ Release Notes Addendum V2.0

▶ **Product identification**	Not applicable (no change of product)
▶ **Impact of change**	Assessment in Application required !
▶ **Attachments**	1_cip10321 affected product list 3_cip10321 Release Notes Addendum V2.0 (myICP link)
▶ **Intended start of delivery**	Not applicable

If you have any questions, please do not hesitate to contact your local sales office.

HSM_MEM_TC3xx

Release Notes Addendum

Version: v2.0 Released

Date: 2022-02-22

About this document

Scope and purpose

This release notes addendum (RNA) document lists all the issues identified in productive releases of the HSM_MEM_TC3xx product. Issues Processed on or before 2022-02-15 are considered in this version.

Note: Issues known at the time of release are documented in release notes. Integrators are required to take them into consideration in addition to the issues listed in this document.

Table 1 Production Releases (PR) and Maintenance Releases (MR) covered

HW Devices	SW Package names	Release Date	Release quality	Release number

TC39x BC TC39x BD TC38x AD TC38x AE TC37x AA TC37xEXT AB TC35x AB TC36x AA TC33x AA TC33xEXT AA TC32x AA	HSM_MEM_TC3xx_1.0.0	2020-07-31	PR	1.0.0
TC39x BC TC39x BD TC38x AD TC38x AE TC37x AA TC37xEXT AB TC35x AB TC36x AA TC33x AA TC33xEXT AA TC32x AA TC3Ex AA	HSM_MEM_TC3xx_1.10.0	2021-05-31	MR	1.10.0

restricted

HSM_MEM_TC3xx

Release Notes Addendum

Intended audience

This document is intended for anyone using the HSM_MEM_TC3xx Software.

Table of contents

1 Known issues v2.0

1.1 FEE

1.1.1 0000065659-164

Issue description: Unnecessary re-generation even if configuration has not changed for FEE configuration plugins.

Impact: Plugins for FEE will re-generate the configuration even though configuration has not changed. No functional impact, only extra time for re-generation.

Work around: None.

Impacted Release(s): 1.0.0

1.1.2 0000065659-201

Issue description: Due to incorrect measurement method used, the WCET data given out earlier are not correct.

Impact: WCET data for provided in earlier releases are not correct.

Work around: Refer 'TC3xx_SW_HSM_MEM_WCET_Report_1.10.0.xlsx' part of TC3xx_SW_HSM_MEM_Qualification_Report_1.10.0.zip package for approximate WCET values.

Impacted Release(s): 1.0.0

1.1.1 0000065659-203

Issue description: Incorrect name of DEM interface mentioned in user manual.

Impact: Incorrect DEM interface name may lead to integration issues.

Work around: Ignore the DEM interface name mentioned in the UM, Code is implemented with interface name 'Dem_ReportErrorStatus' as per AUTOSAR DEM module.

Impacted Release(s): 1.0.0

1.1.2 0000065659-206

Issue description: Due to error in driver software, driver will not return to IDLE state when following conditions are met together.
1) Configuration parameter FeeGcRestart and FeeBlockTypeConfigured is set to FEE_GC_RESTART_WRITE and FEE_DOUBLE_SECTOR_AND_QUASI_STATIC_DATA respectively.
2) Flash area configured for QS blocks is in virgin state (flash is completely erased) OR QS block state needs repair.
3) QS block read or write job is requested immediately after Fee_Init().

Impact: In scenario mentioned in description QS block write or read will not be executed and driver status will not return to MEMIF_IDLE.

Work around: Do not give the QS block read/write request immediately after Fee_Init().

Impacted Release(s): 1.0.0

1.1.3 0000065659-224

Issue description: Due to error in driver software Fee_GetStatus() API may provide MEMIF_IDLE instead of MEMIF_BUSY_INTERNAL when following conditions are met together.

1) Configuration parameter FeeBlockTypeConfigured and FeeGcRestart are set to FEE_DOUBLE_SECTOR_AND_QUASI_STATIC_DATA and FEE_GC_RESTART_WRITE respectively.

2) Following call sequence occurs.

Fee_Init() --> Fee_Read() for QS block or

Fee_Init() --> Fee_Read() for NVM block --> Fee_Read() for QS block

Impact: No impact on normal operations of Fee. New user request will be accepted and executed normally after driver internal status changes from MEMIF_BUSY_INTERNAL to MEMIF_IDLE.

Work around: Not applicable because there is no impact on normal operations of Fee.

Impacted Release(s): 1.0.0, 1.10.0

1.2 Fls_17_Dmu

1.2.1 0000065659-201

Issue description: Due to incorrect measurement method used, the WCET data given out earlier are not correct.

Impact: WCET data for provided in earlier releases are not correct.

Work around: Refer 'TC3xx_SW_HSM_MEM_WCET_Report_1.10.0.xlsx' part of TC3xx_SW_HSM_MEM_Qualification_Report_1.10.0.zip package for approximate WCET values.

Impacted Release(s): 1.0.0

1.2.2 0000065659-207

Issue description: Due to software error in driver, unintended timeout safety error may be reported by Fls_17_Dmu_IsHardeningRequired() API if higher priority task or interrupt is invoked while this API is being executed. This happen only if configuration parameter FeeBlockTypeConfigured is set to FEE_DOUBLE_SECTOR_AND_QUASI_STATIC_DATA.

Impact: Unintended timeout safety error from Fls_17_Dmu and hardening error notification from Fee may be reported.

Work around: Ignore the timeout safety error and hardening error notification.

Impacted Release(s): 1.0.0

2 Known issues v1.0

2.1 FEE

2.1.1 0000065659-156

Issue description: Module header files SHALL NOT include the prototype declarations of Main Functions.

Impact: Main function declaration is provided in the Fee.h file instead of Fee_Schm.h file.

Work around: User should include the Fee.h file which has the Main function declaration.

Impacted Release(s): 1.0.0

2.1.2 0000065659-167

Issue description: When Garbage Collection (GC) is ongoing and a pending user-initiated write or invalidate block request is cancelled by the Fee_Cancel() API, the driver may write unintended data in the flash and may also corrupt some internal data structures.

Impact: In the scenario mentioned in the description, some internal data structures may be corrupted leading to one of the following events:

- Data loss of user blocks

- Reading unintended data
- Trap (DAE/DSE)
- Illegal state notification

If application/NVM stack does not use Fee_Cancel() then no impact.

Work around: Do not use Fee_Cancel() API for canceling pending user-initiated write or invalidate requests.

Impacted Release(s): 1.0.0

2.1.3 0000065659-178

Issue description: Due to an error in the software implementation of FEE driver, when block resize feature is used and newly configured block size is greater than old size, then read of this block leads to overflow of user data buffer by 1 byte. This happens every time the block is read, until it is written for the first time with the new size.

Impact: In the scenario mentioned in the description, any user variable located next to user data buffer may be corrupted.

Work around: Increase the size of user data buffer by 1 byte.

Impacted Release(s): 1.0.0

2.2 Fls_17_Dmu

2.2.1 0000065659-156

Issue description: Module header files SHALL NOT include the prototype declarations of Main Functions.

Impact: Main function declaration is provided in the Fls_17_Dmu.h file instead of Fls_17_Dmu_Schm.h file.

Work around: User should include the Fls_17_Dmu.h file which has the Main function declaration.

Impacted Release(s): 1.0.0

2.2.2 0000065659-159

Issue description: OPER error handling during FLS initialization is not proper. When OPER error is detected during initialization, FEE illegal notification is called, this may lead to unintended safety error or Trap. Only applicable when FLS is used with IFX FEE.

Impact: FEE Safety is ON: Fee will raise UNINT safety error, because of Fee is not yet initialized. In this case illegal state notification will not reach NVM. Also if FLS runtime error is enabled, then Fls_17_Dmu will report the FLS_17_DMU_E_INIT_FAILED.

FEE Safety is OFF: Trap will occur because of Fee_17_IllegalStateNotification() will try dereference the NULL

pointer.

Work around: User shall configure user defined wrapper function instated of Fee_17_IllegalStateNotification() in FlsIllegalStateNotification configuration parameter.

Impacted Release(s): 1.0.0

2.2.3 0000065659-162

Issue description: In the Example code presented in the user manual, the call to Fls_17_Dmu_Write () function is incorrect. The parameter pass to this function are not in the correct order.

Impact: If the customer uses the same example mentioned in the user manual, then

1. If DET/Safety is enabled, FLS_17_DMU_E_PARAM_ADDRESS DET will be raised.

2. If DET/Safety is disabled then incorrect behavior will occur and may lead to trap.

Work around: Refer to the prototype of API Fls_17_Dmu Write() as describe in user manual and pass the parameters in the order described.

Impacted Release(s): 1.0.0

2.2.4 0000065659-163

Issue description: FLS_E_PARAM_DATA is also reported when the SourceAddressPtr is not word aligned (4 byte aligned). This information is missing in the error handling description of Fls_17_Dmu_Write API.

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Known issues v1.0

Impact: User may not be able to find out the reason why FLS_E_PARAM_DATA DET is reported even when the SourceAddressPtr passed is not NULL.

Work around: If FLS_E_PARAM_DATA DET is reported, user has to check whether the SourceAddressPtr passed is word-aligned and not NULL.

Impacted Release(s): 1.0.0

3 HW Derivative specification

This section explains the hardware derivatives supported and their respective property files. Customers must ensure that relevant range checks are implemented and tested as per applicable HW documentation.

3.1 Device support details

Table 2 AURIXTM 2G Device Support TC39x BC/TC39x BD/TC38x AD/TC38x AE/TC37x AA/TC37xEXT AB/TC35x AB/TC36x AA/TC33x AA/TC33xEXT AA/TC32x AA/TC3Ex AA

AURIX™ 2G device	Name displayed in Tresos Tool	Tresos Property File
SAK-TC332LP-32F300F	TC332	AURIX2G_TC332.properties
SAK-TC333LP-32F300F	TC333	AURIX2G_TC333.properties
SAK-TC334LP-32F300F	TC334	AURIX2G_TC334.properties
SAK-TC337LP-32F300S	TC337	AURIX2G_TC337.properties
SAK-TC336LP-32F300S	TC336	AURIX2G_TC336.properties
SAL-TC337LP-32F300S	TC337	AURIX2G_TC337.properties
SAL-TC336LP-32F300S	TC336	AURIX2G_TC336.properties
SAL-TC334LP-32F300F	TC334	AURIX2G_TC334.properties
SAL-TC333LP-32F300F	TC333	AURIX2G_TC333.properties

SAL-TC332LP-32F300F	TC332	AURIX2G_TC332.properties
SAK-TC356TA-64F300S	TC356_ADAS	AURIX2G_TC356_ADAS.properties
SAK-TC365DP-64F300W	TC365_LQFP	AURIX2G_TC365_LQFP.properties
SAK-TC364DP-64F300W	TC364_LQFP	AURIX2G_TC364_LQFP.properties
SAK-TC367DP-64F300S	TC367	AURIX2G_TC367.properties
SAK-TC364DP-64F300F	TC364_TQFP	AURIX2G_TC364_TQFP.properties
SAK-TC366DP-64F300S	TC366	AURIX2G_TC366.properties
SAL-TC365DP-64F200W	TC365	AURIX2G_TC365.properties
SAL-TC367DP-64F200S	TC367	AURIX2G_TC367.properties
SAL-TC364DP-64F200F	TC364_TQFP	AURIX2G_TC364_TQFP.properties
SAL-TC366DP-64F200S	TC366	AURIX2G_TC366.properties
SAL-TC364DP-64F200W	TC364_LQFP	AURIX2G_TC364_LQFP.properties
SAL-TC364DP-64F300W	TC364_LQFP	AURIX2G_TC364_LQFP.properties
SAL-TC377TP-96F300S	TC377	AURIX2G_TC377.properties
SAL-TC375TP-96F300W	TC375	AURIX2G_TC375.properties
SAL-TC377DP-96F300S	TC377	AURIX2G_TC377.properties
SAL-TC377TX-96F300S	TC377_ED_EX	AURIX2G_TC377_ED_EX.properties
SAK-TC389QP-160F300S	TC389	AURIX2G_TC389.properties
SAK-TC387QP-160F300S	TC387	AURIX2G_TC387.properties

AURIX™ 2G device	Name displayed in Tresos Tool	Tresos Property File
SAL-TC387QP-160F300S	TC387	AURIX2G_TC387.properties
SAL-TC389QP-160F300S	TC389	AURIX2G_TC389.properties
SAK-TC334LP-32F200F	TC334	AURIX2G_TC334.properties
SAK-TC337LP-32F200S	TC337	AURIX2G_TC337.properties
SAL-TC337LP-32F200S	TC337	AURIX2G_TC337.properties
SAL-TC334LP-32F200F	TC334	AURIX2G_TC334.properties
SAK-TC333LP-32F200F	TC333	AURIX2G_TC333.properties
SAL-TC333LP-32F200F	TC333	AURIX2G_TC333.properties
SAK-TC323LP-16F160F	TC323	AURIX2G_TC323.properties
SAK-TC324LP-16F160F	TC324	AURIX2G_TC324.properties
SAK-TC322LP-16F160F	TC322	AURIX2G_TC322.properties
SAK-TC332LP-32F200F	TC332	AURIX2G_TC332.properties
SAL-TC332LP-32F200F	TC332	AURIX2G_TC332.properties
SAK-TC323LP-24F200F	TC323	AURIX2G_TC323.properties
SAK-TC324LP-24F200F	TC324	AURIX2G_TC324.properties
SAK-TC323L-24F200F	TC323	AURIX2G_TC323.properties

SAK-TC324L-24F200F	TC324	AURIX2G_TC324.properties
SAK-TC336LP-32F200S	TC336	AURIX2G_TC336.properties
SAL-TC336LP-32F200S	TC336	AURIX2G_TC336.properties
SAL-TC323LP-16F160F	TC323	AURIX2G_TC323.properties
SAL-TC324LP-16F160F	TC324	AURIX2G_TC324.properties
SAL-TC322LP-16F160F	TC322	AURIX2G_TC322.properties
SAL-TC327LP-16F160S	TC327	AURIX2G_TC327.properties
SAK-TC333L-32F200F	TC333	AURIX2G_TC333.properties
SAK-TC334L-32F200F	TC334	AURIX2G_TC334.properties
SAL-TC333L-32F200F	TC333	AURIX2G_TC333.properties
SAL-TC334L-32F200F	TC334	AURIX2G_TC334.properties
SAK-TC327LP-16F160S	TC327	AURIX2G_TC327.properties
SAL-TC323LP-24F200F	TC323	AURIX2G_TC323.properties
SAL-TC324LP-24F200F	TC324	AURIX2G_TC324.properties
SAL-TC323L-24F200F	TC323	AURIX2G_TC323.properties
SAL-TC324L-24F200F	TC324	AURIX2G_TC324.properties
SAK-TC322LS-24F160F	TC322	AURIX2G_TC322.properties
SAK-TC323LS-24F160F	TC323	AURIX2G_TC323.properties

AURIX™ 2G device	Name displayed in Tresos Tool	Tresos Property File
SAK-TC332LS-32F200F	TC332	AURIX2G_TC332.properties
SAK-TC357TA-64F300S	TC357_ADAS	AURIX2G_TC357_ADAS.properties
SAK-TC357TH-64F300S	TC357_ADAS	AURIX2G_TC357_ADAS.properties
SAK-TC356TH-64F300S	TC356_ADAS	AURIX2G_TC356_ADAS.properties
SAK-TC356TD-48F300S	TC356_ADAS	AURIX2G_TC356_ADAS.properties
SAK-TC367VB-32F200S	TC367	AURIX2G_TC367.properties
SAK-TC367V0-64F300S	TC367	AURIX2G_TC367.properties
SAL-TC367DP-64F300S	TC367	AURIX2G_TC367.properties
SAL-TC365DP-64F300W	TC365_LQFP	AURIX2G_TC365_LQFP.properties
SAK-TC365DP-64F200W	TC365_LQFP	AURIX2G_TC365_LQFP.properties
SAK-TC367DP-48F200S	TC367	AURIX2G_TC367.properties
SAL-TC364DP-64F300F	TC364_TQFP	AURIX2G_TC364_TQFP.properties
SAK-TC364DP-48F300F	TC364_TQFP	AURIX2G_TC364_TQFP.properties
SAK-TC364DP-48F200F	TC364_TQFP	AURIX2G_TC364_TQFP.properties
SAL-TC366DP-64F300S	TC366	AURIX2G_TC366.properties
SAK-TC367DP-48F300S	TC367	AURIX2G_TC367.properties

SAK-TC364DP-64F200W	TC364_LQFP	AURIX2G_TC364_LQFP.properties
SAK-TC367DP-64F200S	TC367	AURIX2G_TC367.properties
SAK-TC364DP-64F200F	TC364_TQFP	AURIX2G_TC364_TQFP.properties
SAK-TC366DP-64F200S	TC366	AURIX2G_TC366.properties
SAK-TC377TP-96F300S	TC377	AURIX2G_TC377.properties
SAK-TC375TP-96F300W	TC375	AURIX2G_TC375.properties
SAK-TC377DP-96F300S	TC377	AURIX2G_TC377.properties
SAK-TC375DP-96F300W	TC375	AURIX2G_TC375.properties
SAL-TC375DP-96F300W	TC375	AURIX2G_TC375.properties
SAK-TC375TI-96F300W	TC375	AURIX2G_TC375.properties
SAL-TC375TI-96F300W	TC375	AURIX2G_TC375.properties
SAK-TC377TX-96F300S	TC377_ED_EX	AURIX2G_TC377_ED_EX.properties
SAK-TC377TX-64F300S	TC377_ED_EX	AURIX2G_TC377_ED_EX.properties
SAK-TC387TP-128F300S	TC387	AURIX2G_TC387.properties
SAK-TC387QN-160F300S	TC387	AURIX2G_TC387.properties
SAK-TC389QN-160F300S	TC389	AURIX2G_TC389.properties
SAL-TC387TP-128F300S	TC387	AURIX2G_TC387.properties
SAK-TC387TP-160F300S	TC387	AURIX2G_TC387.properties

AURIX™ 2G device	Name displayed in Tresos Tool	Tresos Property File
SAL-TC387TP-160F300S	TC387	AURIX2G_TC387.properties
SAL-TC399XX-256F300S	TC399	AURIX2G_TC399.properties
SAL-TC399XP-256F300S	TC399	AURIX2G_TC399.properties
SAL-TC397XP-256F300S	TC397	AURIX2G_TC397.properties
SAK-TC399XP-256F300S	TC399	AURIX2G_TC399.properties
SAK-TC399XX-256F300S	TC399	AURIX2G_TC399.properties
SAK-TC397XP-256F300S	TC397	AURIX2G_TC397.properties
SAK-TC397XA-256F300S	TC397_ADAS	AURIX2G_TC397_ADAS.properties
SAK-TC397QA-160F300S	TC397_ADAS	AURIX2G_TC397_ADAS.properties
SAK-TC397QP-192F300S	TC397	AURIX2G_TC397.properties
SAK-TC397QP-256F300S	TC397	AURIX2G_TC397.properties
SAK-TC397XZ-256F300S	TC397	AURIX2G_TC397.properties
SAK-TC397XM-256F300S	TC397	AURIX2G_TC397.properties
SAL-TC397QP-192F300S	TC397	AURIX2G_TC397.properties
SAL-TC397QP-256F300S	TC397	AURIX2G_TC397.properties
SAL-TC397XZ-256F300S	TC397	AURIX2G_TC397.properties

SAL-TC397XX-256F300S	TC397	AURIX2G_TC397.properties
SAK-TC399QP-192F300S	TC399	AURIX2G_TC399.properties
SAK-TC397XX-256F300S	TC397	AURIX2G_TC397.properties
SAK-TC3E7QX-192F300S	TC3E7	AURIX2G_TC3E7.properties
SAL-TC3E7QG-160F300S	TC3E7	AURIX2G_TC3E7.properties
SAL-TC3E7QX-192F300S	TC3E7	AURIX2G_TC3E7.properties

Note: 1. *For TC38x, TC39x, TC37x, TC37xEXT, TC36x, TC35x, TC33x, TC33xEXT, TC32x,TC3ExAA marking option device support, range check has to be imposed by user, and not in the MCAL code.*

2. TC3Ex devices are supported only from REL 1.10.0 MR.

4 Compiler Known issues

4.1 Tasking

4.1.1 0000065659-210

Issue description: Tasking compiler errata TCVX-44549 impact on HSM Mem drivers.

Impact: Issue not seen/reproduced during internal testing of HSM Mem drivers with the published compiler options. Customer shall analyze the impact of compiler errata TCVX-44549 for their application with HSM Mem drivers.

Work around: No impact seen in HSM Mem drivers. Refer workarounds in compiler errata if customer application is affected.

Impacted Release(s): 1.0.0, 1.10.0.

5 Acronyms, Abbreviations and Integration support

Please refer to AURIX 2G HW user manual and HSM_MEM_TC3xx user manual. In case of any queries or support required to implement the work around, contact field application engineers.

Revision History

Date	Version	Description
2022-02-22	v2.0	Updated for issues processed until 15th February 2022. Updated device support details. Added Compiler Known Issues List.
2021-04-28	v1.0	Initial version Reviewed and Released.

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Information note N° 10321AERRA

HSM_MEM Release Notes Addendum V2.0 affecting products TC3xx

Sales name	SP number	OPN	Package
SAK-TC322LP-16F160F AA	SP004264612	TC322LP16F160FAAKXUMA1	PG-TQFP-80-7
SAK-TC322LS-24F160F AA	SP005424932	TC322LS24F160FAAKXUMA1	PG-TQFP-80-7
SAK-TC323LP-16F160F AA	SP001724344	TC323LP16F160FAAKXUMA1	PG-TQFP-100-23
SAK-TC323LP-24F200F AA	SP005408909	TC323LP24F200FAAKXUMA1	PG-TQFP-100-23
SAK-TC332LP-32F200F AA	SP004264616	TC332LP32F200FAAKXUMA1	PG-TQFP-80-7
SAK-TC332LP-32F300F AA	SP004974864	TC332LP32F300FAAKXUMA1	PG-TQFP-80-7
SAK-TC333LP-32F200F AA	SP001724318	TC333LP32F200FAAKXUMA1	PG-TQFP-100-23
SAK-TC333LP-32F300F AA	SP004974874	TC333LP32F300FAAKXUMA1	PG-TQFP-100-23
SAK-TC334LP-32F200F AA	SP001724294	TC334LP32F200FAAKXUMA1	PG-TQFP-144-27
SAK-TC334LP-32F300F AA	SP004974878	TC334LP32F300FAAKXUMA1	PG-TQFP-144-27
SAK-TC336LP-32F300S AA	SP004974908	TC336LP32F300SAAKXUMA1	PG-LFBGA-180-1
SAK-TC337DA-32F300S AA	SP004974938	TC337DA32F300SAAKXUMA1	PG-LFBGA-292-13
SAK-TC337DZ-32F200S AA	SP002268356	TC337DZ32F200SAAKXUMA1	PG-LFBGA-292-13
SAK-TC337LP-32F300S AA	SP004974944	TC337LP32F300SAAKXUMA1	PG-LFBGA-292-11
SAK-TC356TA-64F300S AB	SP003833202	TC356TA64F300SABKXUMA1	PG-LFBGA-180-1
SAK-TC356TD-48F300S AB	SP005424938	TC356TD48F300SABKXUMA1	PG-LFBGA-180-1
SAK-TC356TH-64F300S AB	SP004818890	TC356TH64F300SABKXUMA1	PG-LFBGA-180-1
SAK-TC357TA-64F300S AB	SP003803252	TC357TA64F300SABKXUMA1	PG-LFBGA-292-13
SAK-TC357TH-64F300S AB	SP003803258	TC357TH64F300SABKXUMA1	PG-LFBGA-292-13
SAK-TC364DP-48F300F AA	SP004577254	TC364DP48F300FAAKXUMA1	PG-TQFP-144-27
SAK-TC364DP-64F200F AA	SP005578089	TC364DP64F200FAAKXUMA1	PG-TQFP-144-27
SAK-TC364DP-64F300F AA	SP001713956	TC364DP64F300FAAKXUMA1	PG-TQFP-144-27
SAK-TC364DP-64F300W AA	SP001714740	TC364DP64F300WAAKXUMA1	PG-LQFP-144-25
SAK-TC365DP-64F200W AA	SP005351242	TC365DP64F200WAAKXUMA1	PG-LQFP-176-22
SAK-TC365DP-64F300W AA	SP001724126	TC365DP64F300WAAKXUMA1	PG-LQFP-176-22
SAK-TC366DP-64F300S AA	SP002268324	TC366DP64F300SAAKXUMA1	PG-LFBGA-180-1
SAK-TC367DP-64F300S AA	SP001694656	TC367DP64F300SAAKXUMA1	PG-LFBGA-292-11
SAK-TC367V0-64F300S AA	SP005411327	TC367V064F300SAAKXUMA1	PG-LFBGA-292-11
SAK-TC367VB-32F200S AA	SP005411324	TC367VB32F200SAAKXUMA1	PG-LFBGA-292-11
SAK-TC375TP-96F300W AA	SP001724106	TC375TP96F300WAAKXUMA1	PG-LQFP-176-22
SAK-TC377DP-96F300S AA	SP004987108	TC377DP96F300SAAKXUMA1	PG-LFBGA-292-11
SAK-TC377TP-96F300S AA	SP001694648	TC377TP96F300SAAKXUMA1	PG-LFBGA-292-11
SAK-TC377TX-96F300S AB	SP004950416	TC377TX96F300SABKXUMA1	PG-LFBGA-292-13
SAK-TC377VS-96F300S AA	SP005546304	TC377VS96F300SAAKXUMA1	PG-LFBGA-292-11
SAK-TC387QP-160F300S AD	SP002921224	TC387QP160F300SADKXUMA1	PG-LFBGA-292-11
SAK-TC387QP-160F300S AE	SP005351247	TC387QP160F300SAEKXUMA1	PG-LFBGA-292-11
SAK-TC387TP-128F300S AD	SP002921230	TC387TP128F300SADKXUMA1	PG-LFBGA-292-11
SAK-TC387TP-128F300S AE	SP005351248	TC387TP128F300SAEKXUMA1	PG-LFBGA-292-11
SAK-TC387TP-128F300S AE	SP005425390	TC387TP128F300SAEKXQMA1	PG-LFBGA-292-11
SAK-TC389QP-160F300S AD	SP002921222	TC389QP160F300SADKXUMA1	PG-FBGA-516-1

Information note N° 10321AERRA

HSM_MEM Release Notes Addendum V2.0 affecting products TC3xx

Sales name	SP number	OPN	Package
SAK-TC389QP-160F300S AE	SP005351252	TC389QP160F300SAEKXUMA1	PG-FBGA-516-1
SAK-TC397QA-160F300S BC	SP002739588	TC397QA160F300SBCKXUMA1	PG-LFBGA-292-12
SAK-TC397QA-160F300S BD	SP005351257	TC397QA160F300SBDKXUMA1	PG-LFBGA-292-12
SAK-TC397XA-256F300S BC	SP002739594	TC397XA256F300SBCKXUMA1	PG-LFBGA-292-12
SAK-TC397XA-256F300S BD	SP005351382	TC397XA256F300SBDKXUMA1	PG-LFBGA-292-12
SAK-TC397XP-256F300S BC	SP002739600	TC397XP256F300SBCKXUMA1	PG-LFBGA-292-10
SAK-TC397XP-256F300S BD	SP005351385	TC397XP256F300SBDKXUMA1	PG-LFBGA-292-10
SAK-TC397XP-256F300S BD	SP005433583	TC397XP256F300SBDKXQMA1	PG-LFBGA-292-10
SAK-TC397XX-256F300S BC	SP002725526	TC397XX256F300SBCKXUMA1	PG-LFBGA-292-10
SAK-TC397XX-256F300S BD	SP005351387	TC397XX256F300SBDKXUMA1	PG-LFBGA-292-10
SAK-TC399XP-256F300S BC	SP002725524	TC399XP256F300SBCKXUMA1	PG-LFBGA-516-10
SAK-TC399XP-256F300S BD	SP005351394	TC399XP256F300SBDKXUMA1	PG-LFBGA-516-10
SAK-TC399XX-256F300S BC	SP002725518	TC399XX256F300SBCKXUMA1	PG-LFBGA-516-10
SAK-TC399XX-256F300S BD	SP005351395	TC399XX256F300SBDKXUMA1	PG-LFBGA-516-10
SAL-TC333LP-32F200F AA	SP001724322	TC333LP32F200FAALXUMA1	PG-TQFP-100-23
SAL-TC333LP-32F300F AA	SP005424908	TC333LP32F300FAALXUMA1	PG-TQFP-100-23
SAL-TC364DP-64F300F AA	SP001724134	TC364DP64F300FAALXUMA1	PG-TQFP-144-27
SAL-TC367DP-64F300S AA	SP001724120	TC367DP64F300SAALXUMA1	PG-LFBGA-292-11
SAL-TC375TI-96F300W AA	SP005428963	TC375TI96F300WAALXUMA1	PG-LQFP-176-22
SAL-TC375TI-96F300W AA	SP005572121	TC375TI96F300WAALXUMA2	PG-LQFP-176-22
SAL-TC375TP-96F300W AA	SP001724110	TC375TP96F300WAALXUMA1	PG-LQFP-176-22
SAL-TC377DP-96F300S AA	SP004987116	TC377DP96F300SAALXUMA1	PG-LFBGA-292-11
SAL-TC377TP-96F300S AA	SP001724092	TC377TP96F300SAALXUMA1	PG-LFBGA-292-11
SAL-TC377TX-96F300S AB	SP004950414	TC377TX96F300SABLXUMA1	PG-LFBGA-292-13
SAL-TC387QP-160F300S AD	SP002921220	TC387QP160F300SADLXUMA1	PG-LFBGA-292-11
SAL-TC387QP-160F300S AE	SP005351250	TC387QP160F300SAELXUMA1	PG-LFBGA-292-11
SAL-TC387TP-128F300S AD	SP003021930	TC387TP128F300SADLXUMA1	PG-LFBGA-292-11
SAL-TC387TP-128F300S AE	SP005398494	TC387TP128F300SAELXUMA1	PG-LFBGA-292-11
SAL-TC389QP-160F300S AD	SP002921216	TC389QP160F300SADLXUMA1	PG-FBGA-516-1
SAL-TC389QP-160F300S AE	SP005351253	TC389QP160F300SAELXUMA1	PG-FBGA-516-1
SAL-TC397XP-256F300S BC	SP002725522	TC397XP256F300SBCLXUMA1	PG-LFBGA-292-10
SAL-TC397XP-256F300S BD	SP005351392	TC397XP256F300SBDLXUMA1	PG-LFBGA-292-10
SAL-TC399XP-256F300S BC	SP002725520	TC399XP256F300SBCLXUMA1	PG-LFBGA-516-10
SAL-TC399XP-256F300S BD	SP005351397	TC399XP256F300SBDLXUMA1	PG-LFBGA-516-10
SAL-TC399XX-256F300S BD	SP005351398	TC399XX256F300SBDLXUMA1	PG-LFBGA-516-10