ADVISORY PRODUCT CHANGE NOTICE

Product Group: Vishay Siliconix/ February 25th, 2022 - A**PCN-SIL-5022022**

DG2002E Datasheet Changes

DESCRIPTION OF CHANGE: On resistance test limit changes on the analog switch channel to better represents the product characteristics. There is no design, BOM, or manufacture process change.

Switch max resistance limits at 4.5V, 2.2V and 1.8V V+ power conditions are updated as below:

Part Number		V+=4.5V, Vcom=3V, Ino/nc=10mA		V+=2.2V, Vcom=1.0V, Ino/nc=10mA		V+=1.8V, Vcom=1.0V, Ino/nc=10mA		Units
		Current Limit	*Updated Limit*	*Current Limit*	*Updated Limit*	*Current Limit*	*Updated Limit*	
DG9411E	Room Temp	8	**9**	27	**29.5**	42	**45**	*Ohm*
	Full Temp	10	**11**	28	**30.5**	44	**47**	

CLASSIFICATION OF CHANGE: Datasheet

REASON FOR CHANGE: Updated limits more accurately present product resistance characteristics.

EXPECTED INFLUENCE ON PERFORMANCE/QUALITY/RELIABILTY: There will be no effect on performance, quality, or reliability.

PRODUCT CATAGORY: ICs

PART NUMBERS AFFECTED: DG2002EDL-T1-GE3

VISHAY BRAND(s): Vishay-Siliconix

TIME SCHEDULE: Immediately, Feb 25, 2022

SAMPLE AVAILABILITY: Samples available immediately

QUALIFICATION DATA: Additional data available upon request.

This APCN is for notification purposes only. Your response is not required. If you have any questions, please contact your local Vishay Sales Office.

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ADVISORY PRODUCT CHANGE NOTICE

Product Group: Vishay Siliconix/ February 25th, 2022 - A**PCN-SIL-5022022**

SIC461/2/3/4 Datasheet – Doc#65124

Revision O – March 2021

ELECTRICAL SPECIFICATIONS (V_{IN} = V_{CIN} = 48 V, V_{EN} = 5 V, T_J = -40 °C to +125 °C, unless otherwise stated)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Power Supplies						
V_{DD} supply	V_{DD}	V_{IN} = V_{CIN} = 6 V to 60 V	4.75	5	5.25	V
		V_{IN} = V_{CIN} = 5 V	4.7	5	-	
V_{DD} dropout	$V_{DD\_DROPOUT}$	V_{IN} = V_{CIN} = 5 V, I_{VDD} = 1 mA	-	70	-	mV
V_{DD} UVLO threshold, rising	V_{DD_UVLO}		4	4.25	4.5	V
V_{DD} UVLO hysteresis	$V_{DD\_UVLO\_HYST}$		-	225	-	mV
Maximum V_{DD} current	I_{DD}	V_{IN} = V_{CIN} = 6 V to 60 V	3	-	-	mA
V_{DRV} supply	V_{DRV}	V_{IN} = V_{CIN} = 6 V to 60 V	5.1	5.3	5.55	V
		V_{IN} = V_{CIN} = 5 V	4.8	5	5.2	
V_{DRV} dropout	$V_{DRV\_DROPOUT}$	V_{IN} = V_{CIN} = 5 V, I_{VDD} = 10 mA	-	160	-	mV
Maximum V_{DRV} current	V_{DRV}	V_{IN} = V_{CIN} = 6 V to 60 V	50	-	-	mA
V_{DRV} UVLO threshold, rising	V_{DRV_UVLO}		4	4.25	4.5	V
V_{DRV} UVLO hysteresis	$V_{DRV\_UVLO\_HYST}$		-	295	-	mV
Input current	I_{VCIN}	Non-switching, V_{FB} > 0.8 V	-	235	325	µA
Shutdown current	I_{VCIN_SHDN}	V_{FN} = 0 V	-	4	8	

Revision P – November 2021

ELECTRICAL SPECIFICATIONS (V_{IN} = V_{CIN} = 48 V, V_{EN} = 5 V, T_J = -40 °C to +125 °C, unless otherwise stated)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Power Supplies						
V_{DD} supply	V_{DD}	V_{IN} = V_{CIN} = 6 V to 60 V	4.75	5	5.25	V
		V_{IN} = V_{CIN} = 5 V	4.7	5	-	
V_{DD} dropout	$V_{DD\_DROPOUT}$	V_{IN} = V_{CIN} = 5 V, I_{VDD} = 1 mA	-	70	-	mV
V_{DD} UVLO threshold, rising	V_{DD_UVLO}		4	4.25	4.5	V
V_{DD} UVLO hysteresis	$V_{DD\_UVLO\_HYST}$		-	225	-	mV
Maximum V_{DD} current	I_{DD}	V_{IN} = V_{CIN} = 6 V to 60 V	3	-	-	mA
V_{DRV} supply	V_{DRV}	V_{IN} = V_{CIN} = 6 V to 60 V	4.75	5.3	5.55	V
		V_{IN} = V_{CIN} = 5 V	4.8	5	5.2	
V_{DRV} dropout	$V_{DRV\_DROPOUT}$	V_{IN} = V_{CIN} = 5 V, I_{VDD} = 10 mA	-	160	-	mV
Maximum V_{DRV} current	V_{DRV}	V_{IN} = V_{CIN} = 6 V to 60 V	30	-	-	mA
V_{DRV} UVLO threshold, rising	V_{DRV_UVLO}		4	4.25	4.5	V
V_{DRV} UVLO hysteresis	$V_{DRV\_UVLO\_HYST}$		-	295	-	mV
Input current	I_{VCIN}	Non-switching, V_{FB} > 0.8 V	-	235	325	µA
Shutdown current	I_{VCIN_SHDN}	V_{EN} = 0 V	-	4	8	

ADVISORY PRODUCT CHANGE NOTICE

Product Group: Vishay Siliconix/ February 25th, 2022 - A**PCN-SIL-5022022**

SIC471/2/3/4 Datasheet – Doc#75786

Revision F – March 2021

ELECTRICAL SPECIFICATIONS (V_{IN} = V_{CIN} = 48 V, V_{EN} = 5 V, T_J = -40 °C to +125 °C, unless otherwise stated)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Power Supplies						
V_{DD} supply	V_{DD}	V_{IN} = V_{CIN} = 6 V to 55 V	4.75	5	5.25	V
		V_{IN} = V_{CIN} = 5 V	4.7	5	-	
V_{DD} dropout	$V_{DD\_DROPOUT}$	V_{IN} = V_{CIN} = 5 V, I_{VDD} = 1 mA	-	70	-	mV
V_{DD} UVLO threshold, rising	V_{DD_UVLO}		4	4.25	4.5	V
V_{DD} UVLO hysteresis	$V_{DD\_UVLO\_HYST}$		-	225	-	mV
Maximum V_{DD} current	I_{DD}	V_{IN} = V_{CIN} = 6 V to 55 V	3	-	-	mA
V_{DRV} supply	V_{DRV}	V_{IN} = V_{CIN} = 6 V to 55 V	5.1	5.3	5.55	V
		V_{IN} = V_{CIN} = 5 V	4.8	5	5.2	
V_{DRV} dropout	$V_{DRV\_DROPOUT}$	V_{IN} = V_{CIN} = 5 V, I_{VDD} = 10 mA	-	160	-	mV
Maximum V_{DRV} current	V_{DRV}	V_{IN} = V_{CIN} = 6 V to 55 V	50	-	-	mA
V_{DRV} UVLO threshold, rising	V_{DRV_UVLO}		4	4.25	4.5	V
V_{DRV} UVLO hysteresis	$V_{DRV\_UVLO\_HYST}$		-	295	-	mV
Input current	I_{VCIN}	Non-switching, V_{FB} > 0.8 V	-	235	325	µA
Shutdown current	I_{VCIN_SHDN}	V_{EN} = 0 V	-	4	8	

Revision G – November 2021

ELECTRICAL SPECIFICATIONS (V_{IN} = V_{CIN} = 48 V, V_{EN} = 5 V, T_J = -40 °C to +125 °C, unless otherwise stated)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Power Supplies						
V_{DD} supply	V_{DD}	V_{IN} = V_{CIN} = 6 V to 55 V	4.75	5	5.25	V
		V_{IN} = V_{CIN} = 5 V	4.7	5	-	
V_{DD} dropout	$V_{DD\_DROPOUT}$	V_{IN} = V_{CIN} = 5 V, I_{VDD} = 1 mA	-	70	-	mV
V_{DD} UVLO threshold, rising	V_{DD_UVLO}		4	4.25	4.5	V
V_{DD} UVLO hysteresis	$V_{DD\_UVLO\_HYST}$		-	225	-	mV
Maximum V_{DD} current	I_{DD}	V_{IN} = V_{CIN} = 6 V to 55 V	3	-	-	mA
V_{DRV} supply	V_{DRV}	V_{IN} = V_{CIN} = 6 V to 55 V	4.75	5.3	5.55	V
		V_{IN} = V_{CIN} = 5 V	4.8	5	5.2	
V_{DRV} dropout	$V_{DRV\_DROPOUT}$	V_{IN} = V_{CIN} = 5 V, I_{VDD} = 10 mA	-	160	-	mV
Maximum V_{DRV} current	V_{DRV}	V_{IN} = V_{CIN} = 6 V to 55 V	30	-	-	mA
V_{DRV} UVLO threshold, rising	V_{DRV_UVLO}		4	4.25	4.5	V
V_{DRV} UVLO hysteresis	$V_{DRV\_UVLO\_HYST}$		-	295	-	mV
Input current	I_{VCIN}	Non-switching, V_{FB} > 0.8 V	-	235	325	µA
Shutdown current	I_{VCIN_SHDN}	V_{EN} = 0 V	-	4	8	