



## Product Change Notification / SYST-22HJCC445

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### Date:

24-Feb-2022

### Product Category:

USB Hubs

### PCN Type:

Document Change

### Notification Subject:

ERRATA - USB7002 Silicon Errata and Data Sheet Clarification

### Affected CPNs:

[SYST-22HJCC445\\_Affected\\_CPN\\_02242022.pdf](#)

[SYST-22HJCC445\\_Affected\\_CPN\\_02242022.csv](#)

### Notification Text:

SYST-22HJCC445

Microchip has released a new Product Documents for the USB7002 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [USB7002 Silicon Errata and Data Sheet Clarification](#).

**Notification Status:** Final

**Description of Change:** 1) Added USB7006, USB7016, and USB7052N to document. 2) Added new module.

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 24 Feb 2022

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[USB7002 Silicon Errata and Data Sheet Clarification](#)

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Affected Catalog Part Numbers (CPN)

USB7002-I/KDX  
USB7002-I/KDXV02  
USB7002-I/KDXVAO  
USB7002/KDX  
USB7002T-I/KDX  
USB7002T-I/KDXV01  
USB7002T-I/KDXV03  
USB7002T-I/KDXVAO  
USB7002T/KDX  
USB7006-I/KDX  
USB7006-I/KDXVAO  
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USB7016T-I/KDX  
USB7016T-I/KDXVAO  
USB7016T/KDX  
USB7052N-I/KDX  
USB7052N-I/KDXVAO  
USB7052N/KDX  
USB7052NT-I/KDX  
USB7052NT-I/KDXVAO  
USB7052NT/KDX

## USB7002/USB7006/USB7016/USB7052N Silicon Errata

This document describes known silicon errata for the USB7002/USB7006/USB7016/USB7052N devices. The silicon errata discussed in this document are for silicon revisions as listed in [Table 1](#). A summary of USB70xx silicon errata is provided in [Table 2](#).

**TABLE 1: AFFECTED SILICON REVISIONS**

Part Numbers	Silicon Revision
USB7002, USB7006, USB7016, USB7052N	B0

**TABLE 2: SILICON ISSUE SUMMARY**

Item Number	Silicon Issue Summary
1.	<a href="#">Ganged port power control mode limitations</a>
2.	<a href="#">Incompatibility issue with SST25 series SPI flash devices</a>
3.	<a href="#">Undefined SMBus OTP access commands execute an OTP read</a>
4.	<a href="#">Unsupported 'Set Interface' command does not issue a STALL response</a>
5.	<a href="#">Suspend Current Consumption delta between SPI FW execution and ROM FW execution</a>
6.	<a href="#">VCONN overcurrent loops infinite retries while in DCP mode</a>
7.	<a href="#">Invalid memory region access is not blocked</a>
8.	<a href="#">Force DCP feature overcurrent bug</a>
9.	<a href="#">Multi-Host Endpoint Reflector cannot be used on downstream ports 5 and 6</a>

### Silicon Errata Issues

#### Module 1: Ganged port power control mode limitations

##### DESCRIPTION

'Ganged mode' port power control is not recommended when using Type-C ports, as Type-C ports require individual VBUS enable controls to operate in a fully compliant way. The hub should only be configured for 'ganged mode' operation if all of the downstream ports are re-configured for legacy Type-A operation via the PORT\_CFG\_SEL register.

##### END USER IMPLICATIONS

None. Ganged port power control mode should not be used if any downstream ports are configured as Type-C ports.

##### Work Around

None.

##### PLAN

There is no plan to address this item.

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## Module 2: Incompatibility issue with SST25 series SPI flash devices

### DESCRIPTION

The Hub ROM firmware fails to retrieve the JEDEC ID for SST25 series SPI Flash when hub has booted and is executing the firmware from the SPI Flash device. The resulting issue is that attempts to program the pseudo-OTP memory space within the SPI flash device will fail. It is possible to program the pseudo OTP when running from ROM

### END USER IMPLICATIONS

Updating the pseudo-OTP memory will not work in the standard way when running and external firmware image from an SST25 series SPI flash.

#### Work Around

If running the firmware image from an external SST25 series SPI Flash device, and it is required to update the pseudo-OTP, the hub must be forced to reset and boot from internal ROM firmware image first. The pseudo OTP of the SPI Flash can then be updated while the hub is running from internal ROM firmware.

### PLAN

There is no plan to address this item.

## Module 3: Undefined SMBus OTP access commands execute an OTP read

### DESCRIPTION

The OTP Read via SMBus command is 0x07. Commands 0x0A - 0xFF are undefined commands, but operate as OTP Read if issued.

### END USER IMPLICATIONS

If undefined SMBus OTP commands 0x0A to 0xFF are issued, then an OTP Read will occur. This should have no negative impact on any applications.

#### Work Around

None.

### PLAN

There is no plan to address this item.

## Module 4: Unsupported 'Set Interface' command does not issue a STALL response

### DESCRIPTION

When configuring an unsupported firmware interface & alternate setting combination through a SET\_INTERFACE command from host to the hub's internal Hub Feature Controller device (UDC0), the Hub Feature Controller will not issue a STALL response.

### END USER IMPLICATIONS

None. The end user should never issue unsupported SET\_INTERFACE commands.

#### Work Around

None.

### PLAN

There is no plan to address this item.

## **Module 5: Suspend Current Consumption delta between SPI FW execution and ROM FW execution**

### DESCRIPTION

Suspend current of the hub is typically 16mA when running from internal ROM firmware, and 11.8mA when running from external SPI firmware.

### END USER IMPLICATIONS

While power consumption of the hub IC while in suspend is higher when running from internal ROM firmware image than with an external SPI firmware image, the total system power consumption may still lower when executing from internal ROM firmware image because this figure does not take into account the amount of power required to power a SPI Flash device.

#### **Work Around**

None.

### PLAN

There is no plan to address this item.

## **Module 6: VCONN overcurrent loops infinite retries while in DCP mode**

### DESCRIPTION

When a USB port has battery charging enabled, and the port is in the DCP state (no USB host is present), a persistent fault on the VCONN supply will retry endlessly.

For comparison, if a persistent fault has been detected on VBUS while in the DCP state, the hub will latch off VBUS after several consecutive failed retries.

### END USER IMPLICATIONS

A consumer with a defective electronically marked cable may observe constant toggling of power to the attached device. This may result in rapid appearance and disappearance of a charging icon or indicator on an attached device.

#### **Work Around**

Use the latest firmware version executed externally from a connected SPI Flash memory.

### PLAN

This issue has been fixed in an updated firmware release. The latest firmware release may be loaded to an external SPI flash device and executed by the hub. There is no plan to update the hub ROM firmware image at this time.

## **Module 7: Invalid memory region access is not blocked**

### DESCRIPTION

The hub firmware does not prevent access for the invalid memory region 0xBF90\_0000 - 0xBF90\_2FFF. Attempting to access this memory region will result in a firmware exception.

### END USER IMPLICATIONS

None. These memory addresses have no use, and should never be accessed in a normal application.

#### **Work Around**

Use the latest firmware version executed externally from a connected SPI Flash memory.

### PLAN

This issue has been fixed in an updated firmware release. The latest firmware release may be loaded to an external SPI flash device and executed by the hub. There is no plan to update the hub ROM firmware image at this time.

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## Module 8: Force DCP feature overcurrent bug

### DESCRIPTION

When using the “Force DCP” feature on a port by issuing the SET\_PORT\_DCP\_MODE vendor request, the hub memory read/write functionality will cease to work following an overcurrent event on the port.

### END USER IMPLICATIONS

The “Force DCP” feature if using the hub ROM firmware.

#### **Work Around**

Use the latest firmware version executed externally from a connected SPI Flash memory.

### PLAN

This issue has been fixed in an updated firmware release. The latest firmware release may be loaded to an external SPI flash device and executed by the hub. There is no plan to update the hub ROM firmware image at this time.

## Module 9: Multi-Host Endpoint Reflector cannot be used on downstream ports 5 and 6

### DESCRIPTION

For USB7006 and USB7016 hubs, the Multi-Host Endpoint Reflector device cannot be enabled on downstream ports 5 and 6.

### END USER IMPLICATIONS

End system integrators should ensure their systems limit Multi-Host Endpoint Reflector operation to downstream ports 1-4 only.

#### **Work Around**

None.

### PLAN

There is no plan to address this item.

## APPENDIX A: DOCUMENT REVISION HISTORY

Revision Level & Date	Section/Figure/Entry	Correction
DS80000811B (02-10-22)	All	Added USB7006, USB7016, and USB7052N to document.
	Module 9.	Added new module.
DS80000811A (11-26-18)	All	Initial release



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