



## Product Change Notification / SYST-12REGB920

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**Date:**

13-Jan-2022

**Product Category:**

Wireless Modules

**PCN Type:**

Document Change

**Notification Subject:**

ERRATA - ATWINC3400-MR210XA122 Errata

**Affected CPNs:**

[SYST-12REGB920\\_Affected\\_CPN\\_01132022.pdf](#)

[SYST-12REGB920\\_Affected\\_CPN\\_01132022.csv](#)

**Notification Text:**

SYST-12REGB920

Microchip has released a new Product Documents for the ATWINC3400-MR210XA122 Errata of devices. If you are using one of these devices please read the document located at [ATWINC3400-MR210XA122 Errata](#).

**Notification Status:** Final

**Description of Change:**

- Initial revision

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Productivity

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 13 Jan 2022

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

[ATWINC3400-MR210XA122 Errata](#)

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Affected Catalog Part Numbers (CPN)

ATWINC3400-MR210CA122  
ATWINC3400-MR210CA122-T  
ATWINC3400-MR210UA122



# ATWINC3400-MR210XA122

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## ATWINC3400-MR210XA122 Errata

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### Introduction

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This errata sheet applies to the following ATWINC3400 family of devices:

- ATWINC3400-MR210UA122
- ATWINC3400-MR210UA122-T
- ATWINC3400-MR210CA122
- ATWINC3400-MR210CA122-T

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## 1. Errata Details

If the ATWINC Flash is marginally programmed, the ATWINC Flash corruption may occur where random Flash bytes can randomly show '0' or '1' logic upon Flash read. Marginal programming means that the ATWINC Flash was subject to an unstable voltage level on its VDDIO rail during the programming process. Its symptoms are as follows:

- ATWINC part is unable to boot
- ATWINC part is unable to start
- ATWINC part is unable to complete initialization
- ATWINC part hangs during run time due to fetching incorrect/incomplete instructions from the Flash

The ATWINC Flash is usually programmed during a firmware upgrade, which is done via a host MCU over SPI, host PC tools over I<sup>2</sup>C/serial bridge or built-in OTA. Additionally, the ATWINC firmware uses the Flash to save hashed AP credentials for fast reconnection.

### Affected Versions

- A part that is running 1.2.2 or earlier firmware or driver
- A part that is upgraded to any version using 1.2.2 or earlier built-in OTA
- A part that is upgraded to any version using 1.2.2 or earlier host driver SPI Flash APIs
- A part that is upgraded to any version using 1.2.2 or earlier PC tools

### Fix Version

- ATWINC 1.2.3+ firmware, driver and PC tools

### Work around

Follow either of the following two actions/recommendations:

- The ATWINC 1.2.3+ firmware, driver and PC tools double program the Flash to strengthen the Flash cells electrical level and minimize or mitigate the impact of unstable VDDIO levels. To protect a working ATWINC part or to recover a marginally-programmed ATWINC part, upgrade to the 1.2.3+ firmware and driver. The firmware upgrade must be done using:
  - PC tools from 1.2.3+ release package
  - SPI Flash APIs from 1.2.3+ host driver

**Note:** OTA does not protect a working part against a possible marginally-programmed Flash, nor does it recover a marginally-programmed part. This is a one-time action; OTA can be used later to update to other, newer versions of the ATWINC firmware.

- Make sure that the ATWINC VDDIO that supplies the ATWINC Flash voltage is stable all the time within the recommended operating range: 2.7V – 3.6V (typical 3.3V).

## 2. Document Revision History

Table 2-1. Errata Sheet Revision History

Revision	Date	Section	Description
A	01/2022	Document	Initial revision

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- **Product Support** – Data sheets and errata, application notes and sample programs, design resources, user's guides and hardware support documents, latest software releases and archived software
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- Technical Support

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