



Product Change Notification / SYST-03RAKQ027

Date:

13-Sep-2021

Product Category:

Microprocessors

PCN Type:

Document Change

Notification Subject:

ERRATA - SAMA5D2 Series Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-03RAKQ027_Affected_CPN_09132021.pdf](#)

[SYST-03RAKQ027_Affected_CPN_09132021.csv](#)

Notification Text:

SYST-03RAKQ027

Microchip has released a new Product Documents for the SAMA5D2 Series Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [SAMA5D2 Series Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change: Updated 1. Silicon Issue Summary.

Added in 6. Controller Area Network (MCAN):

- 6.12 Debug message handling state machine not reset to Idle state when CCCR.INIT is set

- 6.13 Message order inversion when transmitting from dedicated Tx Buffers configured with same message ID

Added in 23. Watchdog Timer (WDT):

- 23.1 Restart command of WDT may reset the DDR controller

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 13 Sept 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAMA5D2 Series Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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