



Product Change Notification / ASER-15CAKZ286

Date:

11-May-2021

Product Category:

Capacitive Touch Sensors, USB Transceivers

PCN Type:

Manufacturing Change

Notification Subject:

CCB 4630.001 Initial Notice: Qualification of STA as an additional assembly site for selected CAP1188 and USB33xx device families available in 24L VQFN (4x4x0.9mm) package.

Affected CPNs:

[ASER-15CAKZ286_Affected_CPN_05112021.pdf](#)

[ASER-15CAKZ286_Affected_CPN_05112021.csv](#)

Notification Text:

PCN Status:Initial notification.

PCN Type:Manufacturing Change

Microchip Parts Affected:Please open one of the files found in the Affected CPNs section.

NOTE: For your convenience Microchip includes identical files in two formats (.pdf and .xls).

Description of Change:Qualification of STA as an additional assembly site for selected CAP1188 and USB33xx device families available in 24L VQFN (4x4x0.9mm) package.

Pre and Post Change Summary:

	Pre Change		Post Change		
Assembly Site	ASE Inc. (ASE)		ASE Inc. (ASE)	STATS Chippac Ltd. (STA)	
Wire material	CuPd	Au	CuPd	Au	CuPdAu
Die attach material	EN-4900F		EN-4900F		8290

Molding compound material	G631B	G631B	G700E
Lead frame material	C194	C194	C194

Impacts to Data Sheet:None

Change Impact:None

Reason for Change:To improve manufacturability by qualifying STA as an additional assembly site

Change Implementation Status:

In Progress

Estimated Qualification Completion Date:September 2021

Note: Please be advised the qualification completion times may be extended because of unforeseen business conditions however implementation will not occur until after qualification has completed and a final PCN has been issued. The final PCN will include the qualification report and estimated first ship date. Also note that after the estimated first ship date guided in the final PCN customers may receive pre and post change parts.

Time Table Summary:

	May 2021					-->	September 2021				
Workweek	1 9	2 0	2 1	2 2	2 3		3 6	3 7	3 8	3 9	4 0
Initial PCN Issue Date	X										
Qual Report Availability									X		
Final PCN Issue Date									X		

Method to Identify Change:Traceability code

Qualification Plan:Please open the attachments included with this PCN labeled as PCN_#_Qual_Plan.

Revision History:

May 5, 2021: Issued initial notification.**May 11, 2021:** Re-issued initial notification. Corrected the attached Qualification Plan and Pre and Post Change Summary.

The change described in this PCN does not alter Microchip's current regulatory compliance regarding the material content of the applicable products.

Attachments:

[PCN_ASER-15CAKZ286_Pre and Post Change Summary.pdf](#)

[PCN_ASER-15CAKZ286_Qual_Plan.pdf](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

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Affected Catalog Part Numbers(CPN)

CAP1188-1-CP-TR

USB3343-CP

USB3343-CP-TR

USB3318-CP

USB3318-CP-TR

USB3311C-CP-TR

USB3315C-CP-TR

USB3317C-CP-TR

USB3318C-CP-TR

CCB 4630.001
Pre and Post Change Summary
PCN #: ASER-15CAKZ286



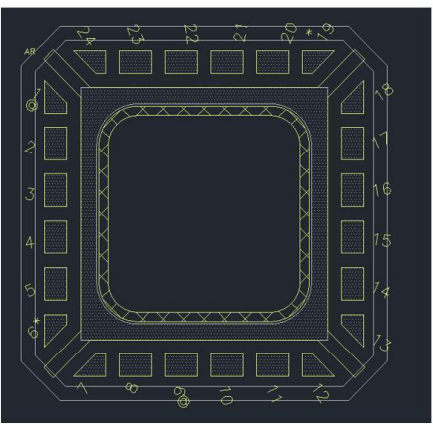
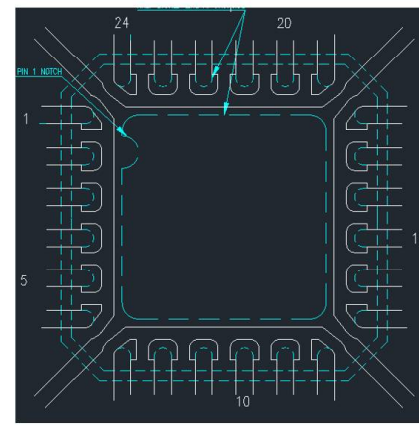
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**Qualification of STA as an additional assembly site for selected
CAP1188 and USB33xx device families available in 24L VQFN
(4x4x0.9mm) package.**

Lead frame Comparison

ASE	STA
<p data-bbox="360 840 505 873">ASEK</p>  <p>The ASEK layout diagram shows a square chip with a central square core. The core is surrounded by a ring of memory blocks, which are further surrounded by a ring of logic blocks. The layout is symmetrical and includes a central square core.</p>	<p data-bbox="1117 840 1263 873">STA</p>  <p>The STA layout diagram shows a square chip with a central square core. The core is surrounded by a ring of memory blocks, which are further surrounded by a ring of logic blocks. The layout is symmetrical and includes a central square core.</p>



MICROCHIP

QUALIFICATION PLAN SUMMARY

PCN#: ASER-15CAKZ286

Date
March 22, 2021

Qualification of STA as an additional assembly site for selected LAN9303 device family available in 56L VQFN (8x8x0.9mm) package. The qualification for selected CAP1188 and USB33xx device families available in 24L VQFN (4x4x0.9mm) package will qualify by similarity (QBS).

Purpose: Qualification of STA as an additional assembly site for selected LAN9303 device family available in 56L VQFN (8x8x0.9mm) package. The qualification for selected CAP1188 and USB33xx device families available in 24L VQFN (4x4x0.9mm) package will qualify by similarity (QBS).

CCB No: 4630 & 4630.001

<u>Misc.</u>	Assembly site	STA
	MP Code (MPC)	TA3017RTXB0C
	Part Number (CPN)	LAN9303I-ABZJ
	MSL information	MSL-3 @260C
	Assembly Shipping Media (T/R, Tube/Tray)	UBOT Tray (UF08081.01026XB 02)
	Base Quantity Multiple (BQM)	Tray - 260
	Reliability Site	MTAI
<u>Lead-Frame</u>	Paddle size	236X236 mils
	Material	C194
	DAP Surface Prep	Double Ring
	Treatment	Non-Rough
	Process	Etched
	Lead-lock (With Locking Holes)	Yes
	Lead Plating	Matte Sn
	Strip Size	250mmX70 mm
	Strip Density	108 units / strip
<u>Bond Wire</u>	Material	CuPdAu
<u>Die Attach</u>	Part Number	8290
	Conductive	Yes
<u>MC</u>	Part Number	G700E
<u>PKG</u>	PKG Type	VQFN
	Pin/Ball Count	56L
	PKG width/size	8X8X0.9 mm

Test Name	Conditions	Sample Size	Min. Qty of Spares per Lot (should be properly marked)	Qty of Lots	Total Units	Fail Accept Qty	Est. Dur. Days	ATE Test Site	REL Test Site	Pkg. Type	Special Instructions
Standard Pb-free Solderability	J-STD-002D; Perform 8 hour steam aging for Matte tin finish and 1 hour steam aging for NiPdAu finish prior to testing. Standard Pb-free: Matte tin/ NiPdAu finish, SAC solder, wetting temp 245°C for both SMD & through hole packages.	22	5	1	27	> 95% lead coverage	5	MTAI	MTAI	56LVQFN	Standard Pb-free solderability is the requirement. SnPb solderability (backward solderability- SMD reflow soldering) is required for any plating related changes and highly recommended for other package BOM changes.
Wire Bond Pull - WBP	Mil. Std. 883-2011	5	0	1	5	0	5	MTAI	MTAI	56LVQFN	30 bonds from a min. 5 devices.
Wire Bond Shear - WBS	CDF-AEC-Q100-001	5	0	1	5	0	5	MTAI	MTAI	56LVQFN	30 bonds from a min. 5 devices.
Physical Dimensions	Measure per JESD22 B100 and B108	10	0	3	30	0	5	MTAI	MTAI	56LVQFN	
External Visual	Mil. Std. 883-2009/2010	All devices prior to submission for qualification testing	0	3	ALL	0	5	MTAI	MTAI	56LVQFN	
Preconditioning - Required for surface mount devices	+150°C Bake for 24 hours, moisture loading requirements per MSL level + 3X reflow at peak reflow temperature per Jedec-STD-020E for package type; Electrical test pre and post stress at at room temp 25°C and hot temp 100°C. MSL3 / 260c	231	15	3	738	0	15	MTAI	MTAI	56LVQFN	Spares should be properly identified. 77 parts from each lot to be used for HAST, uHAST, Temp Cycle test.
UHAST	+130°C/85% RH for 96 hrs or +110°C/85% RH for 264 hrs. Electrical test pre and post stress at room temp 25°C. Requires 2X Rel stress Testing	77	5	3	246	0	10	MTAI	MTAI	56LVQFN	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.
Temp Cycle	-65°C to +150°C for 500 cycles. Electrical test pre and post stress at hot temp 100°C; 3gram force WBP, on 5 devices from 1 lot, test following Temp Cycle stress. Requires 2X Rel stress Testing	77	5	3	246	0	15	MTAI	MTAI	56LVQFN	Spares should be properly identified. Use the parts which have gone through Pre-conditioning.