



Product Change Notification / SYST-08VLIV229

Date:

09-Oct-2020

Product Category:

Power Management - Power Switches

PCN Type:

Document Change

Notification Subject:

Data Sheet - MIC94161/2/3/4/5 Data Sheet

Affected CPNs:

[SYST-08VLIV229_Affected_CPN_10092020.pdf](#)

[SYST-08VLIV229_Affected_CPN_10092020.csv](#)

Notification Text:

SYST-08VLIV229

Microchip has released a new Product Documents for the MIC94161/2/3/4/5 Data Sheet of devices. If you are using one of these devices please read the document located at [MIC94161/2/3/4/5 Data Sheet](#).

Notification Status: Final

Description of Change: 1) Converted Micrel document MIC94161/2/3/4/5 to Microchip data sheet template DS20006439A.
2) Minor grammatical text changes throughout.

Impacts to Data Sheet: None

Reason for Change: Conversion of Micrel datasheet to Microchip datasheet conventions.

Change Implementation Status: Complete

Date Document Changes Effective: 09 Oct 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[MIC94161/2/3/4/5 Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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Affected Catalog Part Numbers (CPN)

MIC94161YCS-TR

MIC94162YCS-TR

MIC94163YCS-TR

MIC94164YCS-TR

MIC94165YCS-TR

3A High-Side Load Switch with Reverse Blocking

Features

- 1.5 mm × 1 mm 6-Ball WLCSP Package
- 14.5 mΩ $R_{DS(ON)}$
- 1.7V to 5.5V Input Voltage Range
- 3A Continuous Operating Current
- Reverse Current Flow Blocking (No “Body Diode”)
- Internal Level Shift for CMOS/TTL Control Logic
- Ultra-Low Quiescent Current
- Micropower Shutdown Current
- Soft-Start: MIC94161/4/5 (2.7 ms)
- Load Discharge Circuit: MIC94162/4
- Ultra-Fast Turn-Off Time
- Junction Operating Temperature from –40°C to +125°C

Applications

- Solid State Drives (SSD)
- Smartphones and Tablets
- Personal Media Players (PMP)
- Ultra-Mobile PCs
- Portable Instrumentation
- GPS Modules
- Datacom Equipment

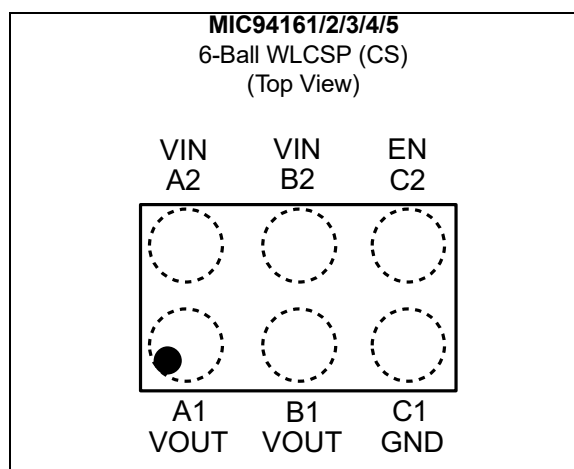
General Description

The MIC94161, MIC94162, MIC94163, MIC94164, and MIC94165 are a family of high-side load switches designed to operate from 1.7V to 5.5V input voltage. The load switch pass element is an internal 14.5 mΩ $R_{DS(ON)}$ N-Channel MOSFET that enables the device to support up to 3A of continuous current. Additionally, the load switch supports 1.5V logic level control and shutdown features in a tiny 1.5 mm × 1 mm 6-ball WLCSP package.

The MIC94161/2/3/4/5 provide reverse current protection when the device is disabled. The device will not allow the flow of current from the output to the input when the device is turned OFF. Additionally, the MIC94161 features overvoltage protection to protect the load when the input voltage is above 4.55V, as well as a precise enable threshold that keeps the MIC94161 in the default OFF state until the EN pin rises above 1.15V.

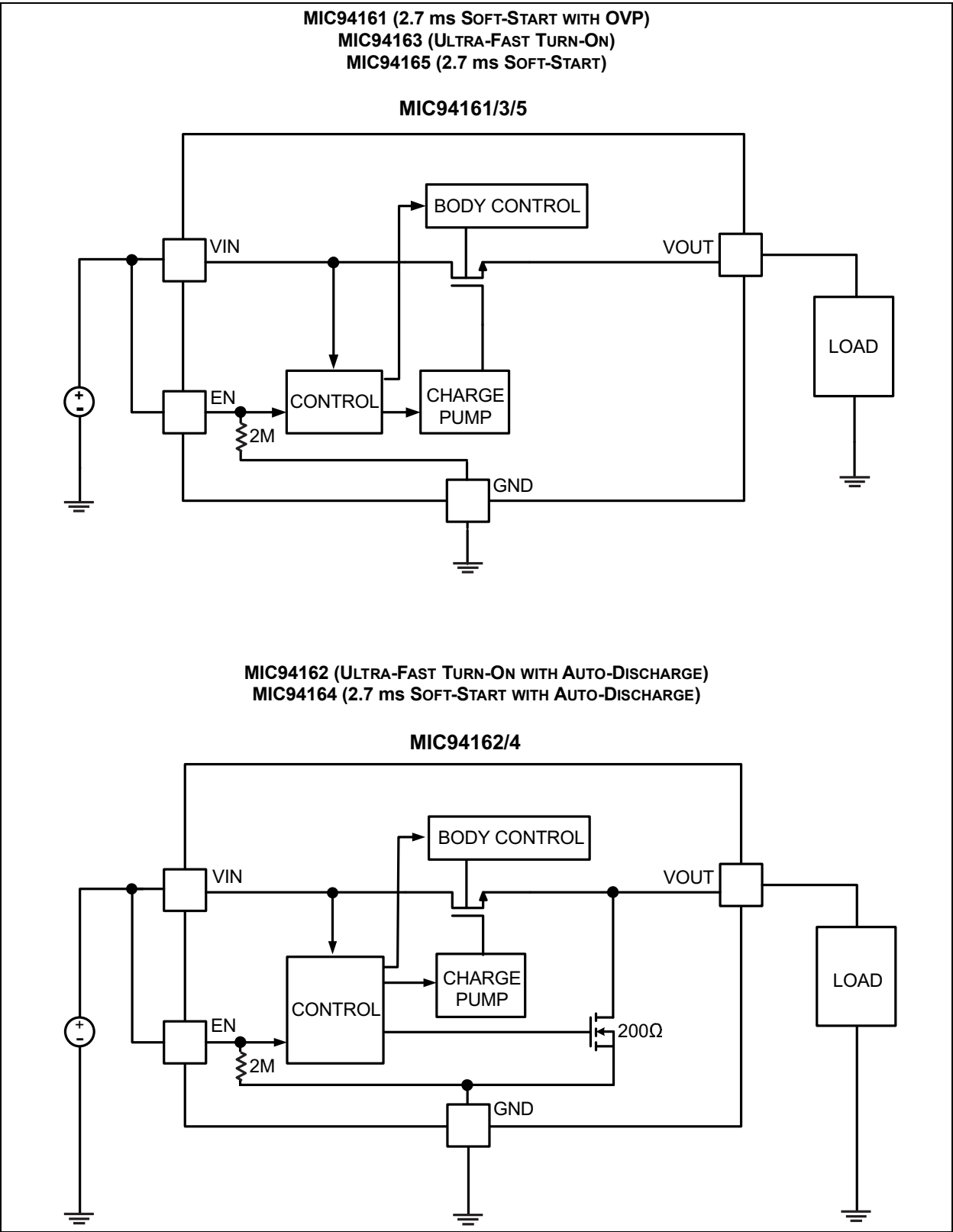
The MIC94161/2/3/4/5 operating voltage range makes them ideal for Lithium-ion and NiMH/NiCad/Alkaline battery powered systems, as well as non battery powered applications. The devices provide low quiescent current and low shutdown current to maximize battery life.

Package Type



MIC94161/2/3/4/5

Typical Application Circuits



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Input Voltage (V_{IN})	–0.3V to +6V
Enable Voltage (V_{EN})	–0.3V to +6V
Continuous Drain Current (I_D) (Note 1)	±3A
ESD Rating (Note 2)	2 kV

Operating Ratings ††

Input Voltage (V_{IN})	+1.7V to +5.5V
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† **Notice:** Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.

†† **Notice:** The device is not guaranteed to function outside its operating ratings.

Note 1: With thermal contact to PCB (see Application Information).

2: Devices are ESD sensitive. Handling precautions are recommended. Human body model, 1.5 kΩ in series with 100 pF.

ELECTRICAL CHARACTERISTICS

Electrical Characteristics: $T_A = +25^\circ\text{C}$. **Bold** values valid for $-40^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$, unless noted.

Parameter	Sym.	Min.	Typ.	Max.	Units	Conditions
General						
Operating Input Voltage Range	V _{IN}	1.7	—	5.5	V	—
Enable Threshold Voltage (MIC94161)	V _{ENTH}	1.15	—	1.5	V	V _{IN} = 1.7V to 5.5V, I _{OUT} = 250 μA
Enable Threshold Voltage (MIC94162/3/4/5)		—	—	0.375	V	Logic Low, OFF; V _{IN} = 1.8V to 5.5V; I _{OUT} = 250 μA; −40°C ≥ T _J ≤ +85°C
		1.2	—	—		Logic High, ON; V _{IN} = 1.7V to 5.5V; I _{OUT} = 250 μA; −40°C ≥ T _J ≤ +85°C
Enable Input Current	I _{EN}	—	2	4	μA	V _{IN} = V _{EN} = 3.6V; I _{OUT} = 0
Quiescent Current (MIC94161)	I _Q	—	40	80	μA	V _{IN} = V _{EN} = 3.6V; I _{OUT} = 0
Quiescent Current (MIC94162/3)	I _Q	—	25	55	μA	V _{IN} = V _{EN} = 3.6V; I _{OUT} = 0
Quiescent Current (MIC94164/5)	I _Q	—	15	35	μA	V _{IN} = V _{EN} = 3.6V; I _{OUT} = 0
Shutdown Current	I _{SD}	—	0.1	1	μA	V _{IN} = 5.5V; V _{EN} = 0V; I _{OUT} = Open
OFF State Leakage Current	I _{LEAK}	—	0.1	1	μA	V _{IN} = 5.5V; V _{EN} = 0V; I _{OUT} = Short
Reverse Leakage Current (MIC94161/3/5)	I _{LEAKR}	—	0.1	1	μA	V _{IN} = 0V; V _{OUT} = 5.5V; V _{EN} = 0V

MIC94161/2/3/4/5

ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: $T_A = +25^\circ\text{C}$. **Bold** values valid for $-40^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$, unless noted.

Parameter	Sym.	Min.	Typ.	Max.	Units	Conditions
N-Channel ON-Resistance	$R_{DS(ON)}$	—	14.5	—	m Ω	$V_{IN} = 5.5\text{V}; V_{EN} = 1.5\text{V}; I_{OUT} = 3\text{A}$
		—	15.5	—		$V_{IN} = 4.5\text{V}; V_{EN} = 1.5\text{V}; I_{OUT} = 3\text{A}$
		—	17.5	—		$V_{IN} = 3.6\text{V}; V_{EN} = 1.5\text{V}; I_{OUT} = 3\text{A}$
		—	21	—		$V_{IN} = 2.7\text{V}; V_{EN} = 1.5\text{V}; I_{OUT} = 3\text{A}$
		—	34	—		$V_{IN} = 1.8\text{V}; V_{EN} = 1.5\text{V}; I_{OUT} = 3\text{A}$
		—	40	—		$V_{IN} = 1.7\text{V}; V_{EN} = 1.5\text{V}; I_{OUT} = 3\text{A}$
Overvoltage Protection Threshold (MIC94161)	V_{OVP}	4.5	4.75	5	V	$V_{IN} = V_{EN}; I_{OUT} = 0; V_{IN}$ rising
Active Discharge Resistance (MIC94162/4)	R_{AD}	—	200	400	Ω	$V_{IN} = 3.6\text{V}; I_{TEST} = 1\text{ mA}; V_{EN} = 0\text{V}$
Timing Characteristics						
Turn-On Delay Time (MIC94162/3)	t_{ON}	—	10	—	μs	$V_{IN} = 3.6\text{V}; R_{LOAD} = 1.2\Omega; C_{OUT} = 200\text{ }\mu\text{F}; V_{EN} = 1.5\text{V}$
Turn-On Rise Time (MIC94162/3)	t_R	—	60	—	μs	$V_{IN} = 3.6\text{V}; R_{LOAD} = 1.2\Omega; C_{OUT} = 200\text{ }\mu\text{F}; V_{EN} = 1.5\text{V}$
Turn-On Delay Time (MIC94161/4/5)	t_{ON}	—	0.4	—	ms	$V_{IN} = 3.6\text{V}; R_{LOAD} = 1.2\Omega; C_{OUT} = 200\text{ }\mu\text{F}; V_{EN} = 1.5\text{V}$
Turn-On Rise Time (MIC94161/4/5)	t_R	—	2.7	—	ms	$V_{IN} = 3.6\text{V}; R_{LOAD} = 1.2\Omega; C_{OUT} = 200\text{ }\mu\text{F}; V_{EN} = 1.5\text{V}$
Turn-Off Delay Time	t_{OFF}	—	25	—	μs	$V_{IN} = 3.6\text{V}; R_{LOAD} = 1.2\Omega; C_{OUT} = 200\text{ }\mu\text{F}; V_{EN} = 1.5\text{V}$
Turn-Off Fall Time	t_F	—	500	—	μs	$V_{IN} = 3.6\text{V}; R_{LOAD} = 1.2\Omega; C_{OUT} = 200\text{ }\mu\text{F}; V_{EN} = 1.5\text{V}$

TEMPERATURE SPECIFICATIONS

Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Temperature Ranges						
Junction Temperature Range	T_J	-40	—	+125	$^\circ\text{C}$	Note 1
Storage Temperature Range	T_S	-55	—	+150	$^\circ\text{C}$	—
Package Thermal Resistance						
Thermal Resistance, WLCSP 6-Ball	θ_{JA}	—	108	—	$^\circ\text{C/W}$	—

Note 1: Sustained junction temperatures above $+125^\circ\text{C}$ can impact the device reliability.

Timing Diagrams

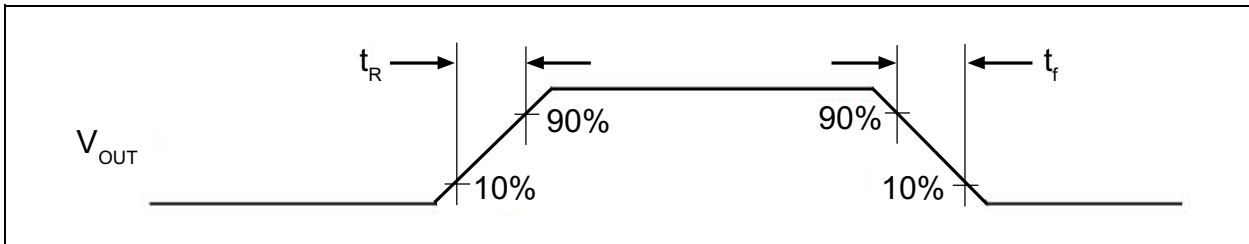


FIGURE 1-1: Output Voltage Rise and Fall Time Measurements.

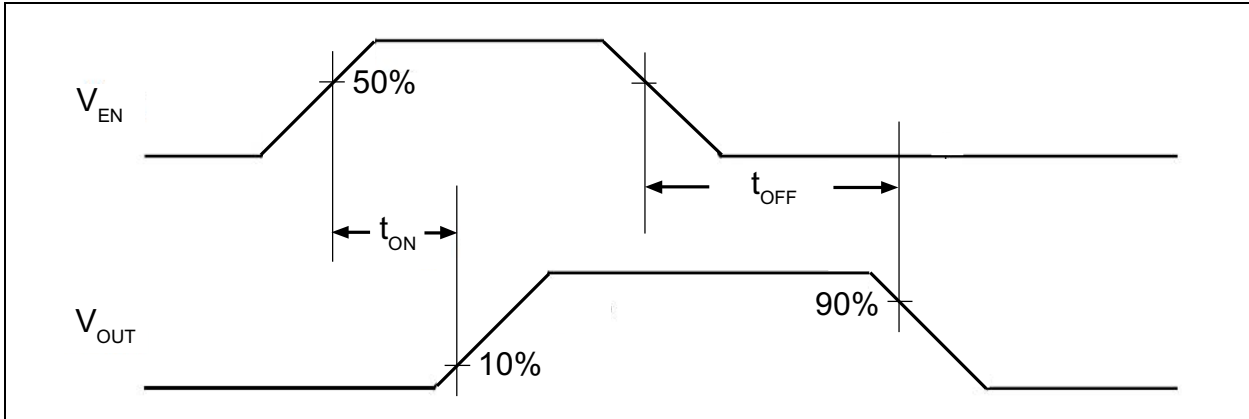


FIGURE 1-2: Output Voltage Turn On and Turn Off Measurements.

2.0 TYPICAL OPERATING CHARACTERISTICS

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

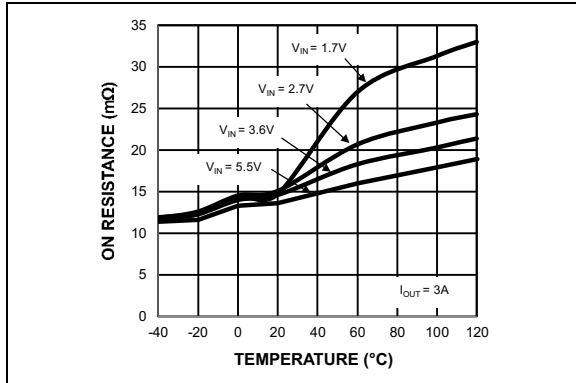


FIGURE 2-1: ON Resistance vs. Temperature.

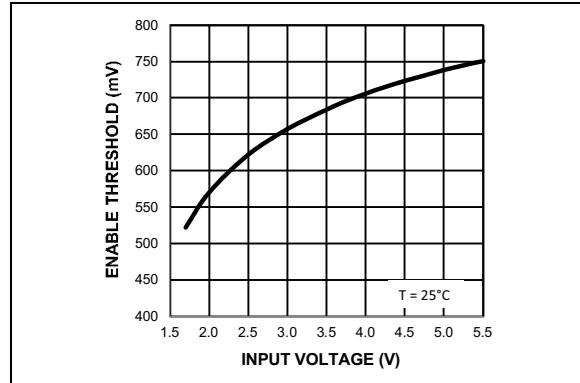


FIGURE 2-4: Enable Threshold vs. Input Voltage (MIC94162/3/4/5).

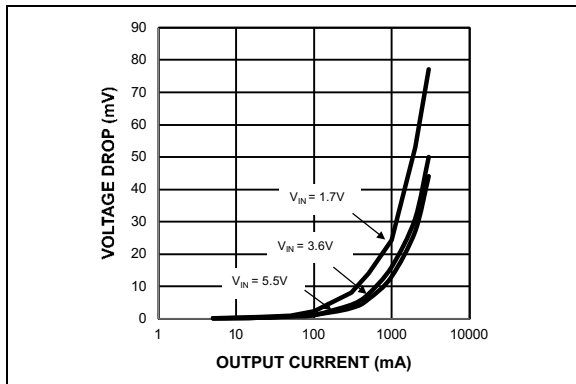


FIGURE 2-2: Voltage Drop vs. Output Current.

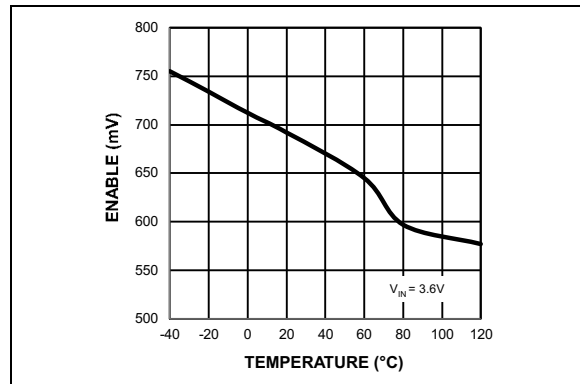


FIGURE 2-5: Enable Threshold vs. Temperature (MIC94162/3/4/5).

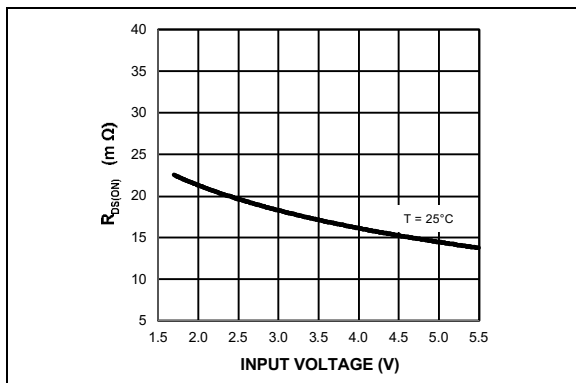


FIGURE 2-3: $R_{DS(ON)}$ vs. Input Voltage.

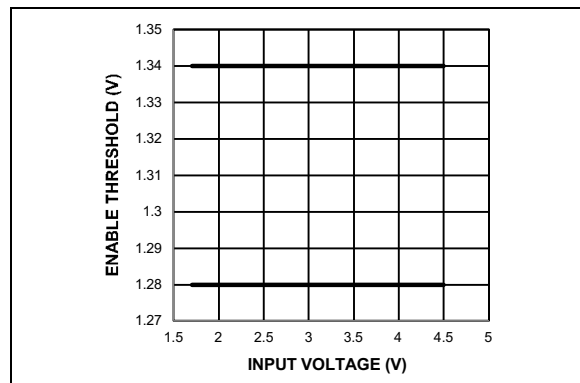


FIGURE 2-6: Enable Threshold vs. Input Voltage (MIC94161).

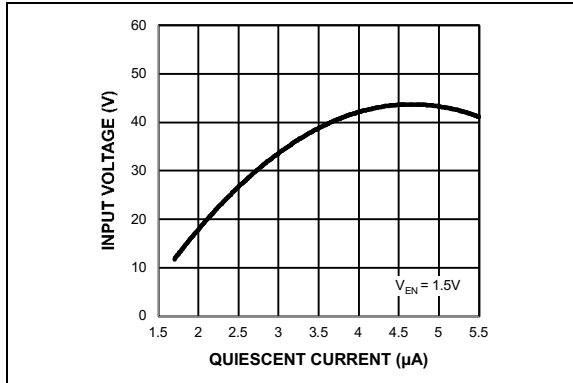


FIGURE 2-7: Quiescent Current vs. Input Voltage (MIC94161).

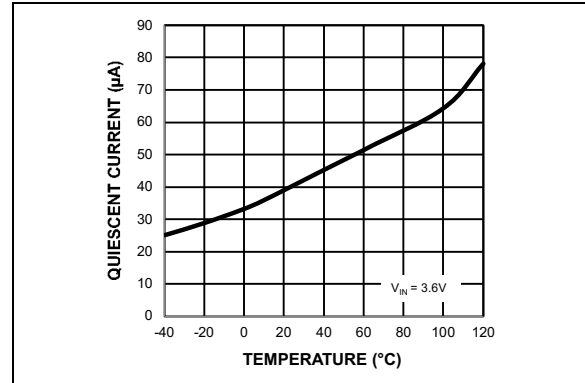


FIGURE 2-10: Quiescent Current vs. Temperature (MIC94161).

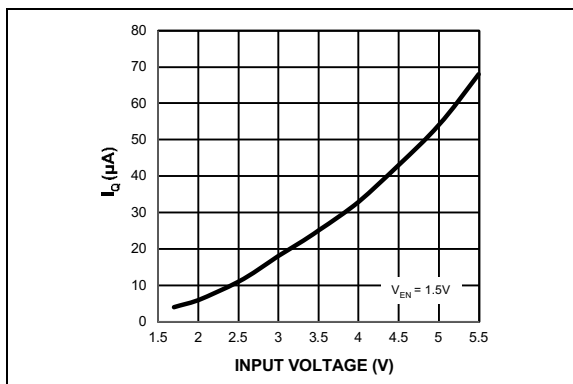


FIGURE 2-8: Quiescent Current vs. Input Voltage (MIC94162/3).

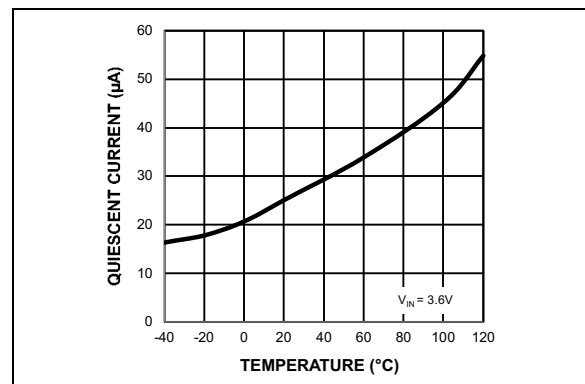


FIGURE 2-11: Quiescent Current vs. Temperature (MIC94162/3).

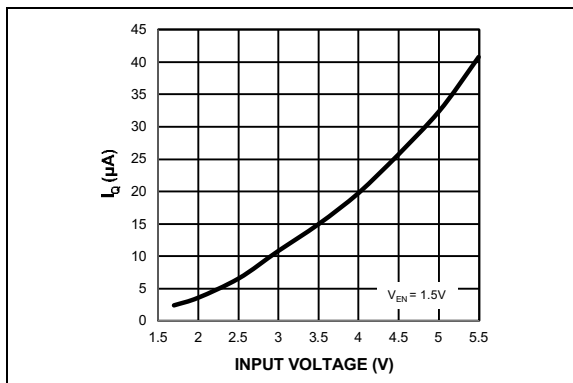


FIGURE 2-9: Quiescent Current vs. Input Voltage (MIC94164/5).

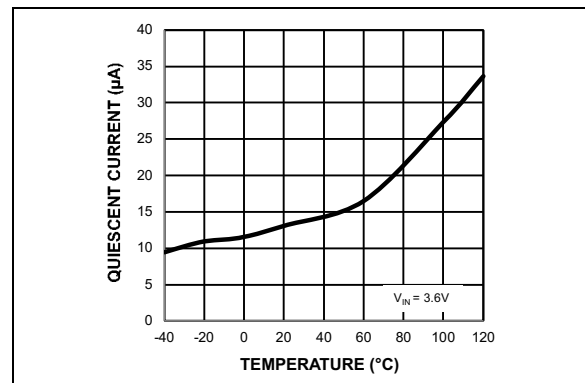


FIGURE 2-12: Quiescent Current vs. Temperature (MIC94164/5).

MIC94161/2/3/4/5

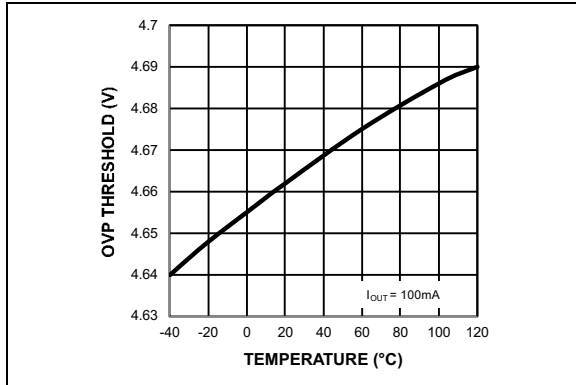


FIGURE 2-13: OVP Threshold vs. Temperature (MIC94161).

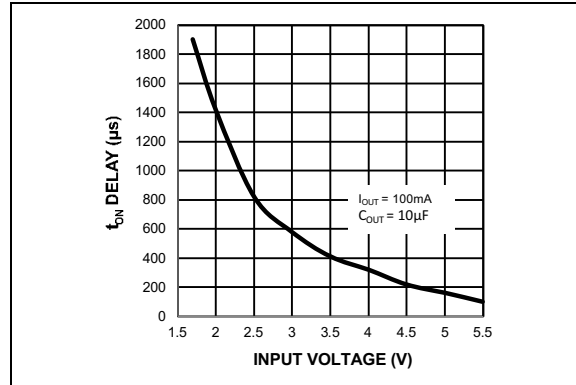


FIGURE 2-16: t_{ON} Delay vs. Input Voltage (MIC94161/4/5).

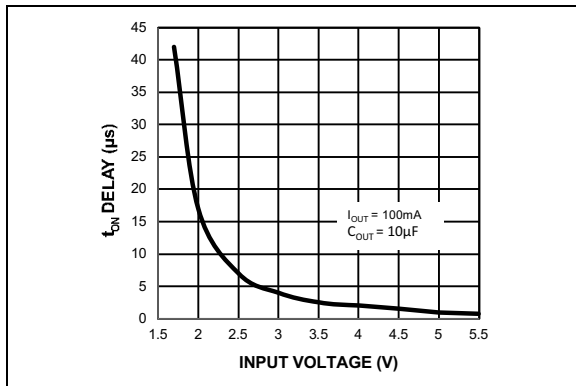


FIGURE 2-14: t_{ON} Delay vs. Input Voltage (MIC94162/3).

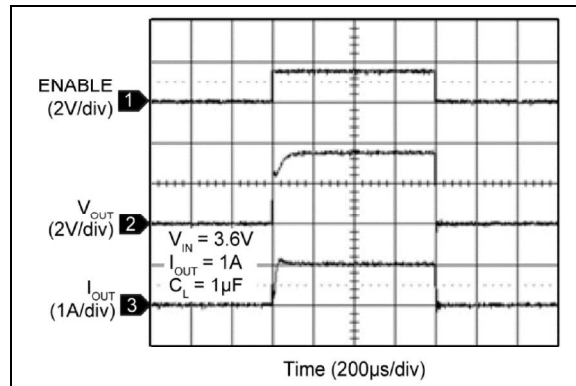


FIGURE 2-17: MIC94162/3 Start-Up ($I_{OUT} = 1A$).

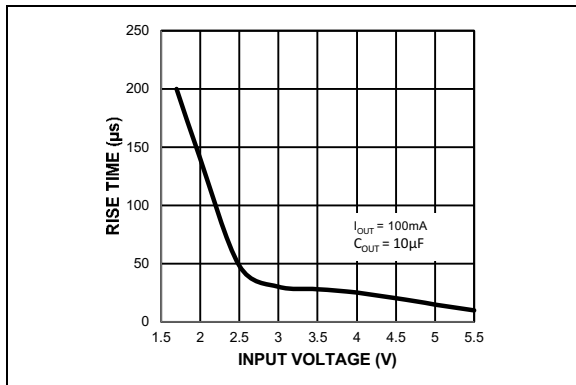


FIGURE 2-15: Rise Time vs. Input Voltage (MIC94162/3).

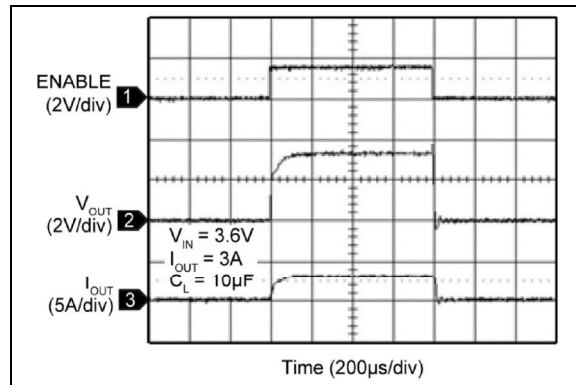


FIGURE 2-18: MIC94162/3 Start-Up ($I_{OUT} = 3A$).

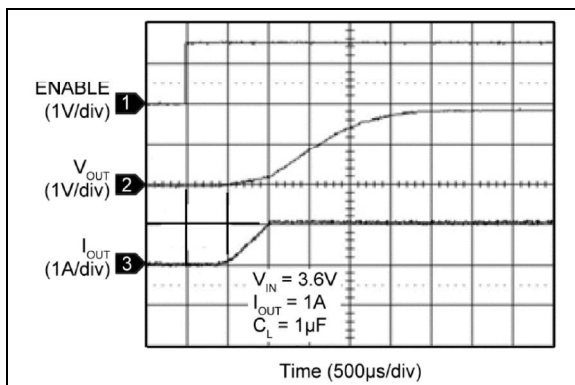


FIGURE 2-19: MIC94161/4/5 Start-Up ($I_{OUT} = 1A$).

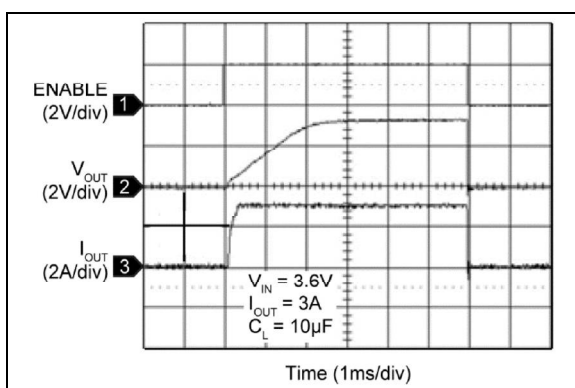


FIGURE 2-20: MIC94161/4/5 Start-Up ($I_{OUT} = 3A$).

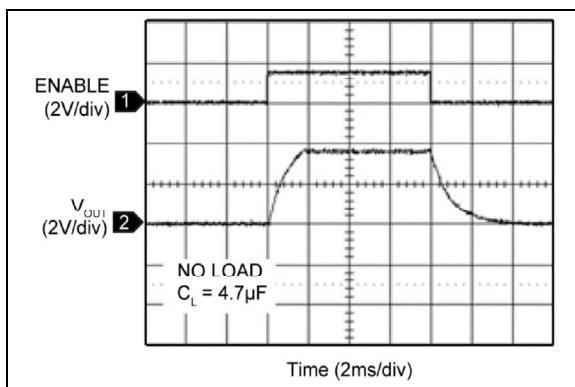


FIGURE 2-21: MIC94164 Auto Discharge.

MIC94161/2/3/4/5

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in [Table 3-1](#).

TABLE 3-1: PIN FUNCTION TABLE

Pin Number	Pin Name	Description
A1, B1	VOUT	Source of N-channel MOSFET.
C1	GND	Ground.
A2, B2	VIN	Input Supply: Drain of N-channel MOSFET.
C2	EN	Enable (Input): Active-high control input for switch. Internal 2 M Ω pull-down resistor. Output will be off if this pin is left floating.

4.0 FUNCTIONAL DESCRIPTION

The MIC94161/2/3/4/5 is a family of high-side load switches designed to operate from 1.7V to 5.5V input voltage. The load switch pass element is an internal 14.5 mΩ $R_{DS(ON)}$ N-Channel MOSFET that enables the device to support up to 3A of continuous current. Additionally, the load switch supports 1.5V logic level control and shutdown features in a tiny 1.5 mm × 1 mm 6-ball WLCSP package.

The MIC9416x provides reverse current protection when the device is disabled. The device will not allow the flow of current from the output to the input when the device is turned OFF. Additionally, the MIC94161 features overvoltage protection to protect the load when the input voltage is above 4.55V, as well as a precise enable threshold that keeps the MIC94161 in the default OFF state until the EN pin rises above 1.15V.

The MIC94162/3 features rapid turn on for applications that require a quick startup time. The MIC94161/4/5 provides a slew rate controlled soft-start turn-on of 2.7 ms. The soft-start feature is provided to prevent an in-rush current event from pulling down the input supply voltage.

The MIC94162/4 feature an active load discharge circuit that switches in a 200Ω load when the switch is disabled to automatically discharge a capacitive load.

An active pull-down on the enable input keeps the MIC94161/2/3/4/5 in a default OFF state until the enable pin is pulled above 1.2V. Internal level shift circuitry allows low voltage logic signals to switch higher supply voltages. The enable voltage can be as high as 5.5V and is not limited by the input voltage.

4.1 Power Switch SOA

The safe operating area (SOA) curve represents the boundary of maximum safe operating current and maximum safe operating junction temperature.

Figure 4-1 illustrates the SOA for various input voltages, with the package mounted on a typical 1 layer, 1 square inch copper board.

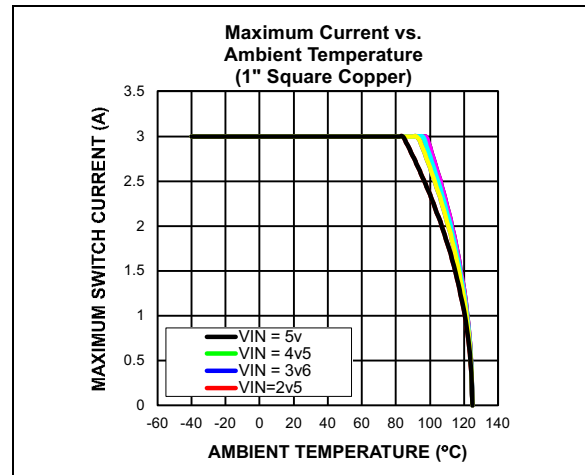


FIGURE 4-1: Safe Operating Area (SOA) Graph.

4.2 Power Dissipation Considerations

As with all power switches, the current rating of the switch is limited mostly by the thermal properties of the package and the PCB it is mounted on. There is a simple ohms law type relationship between thermal resistance, power dissipation and temperature, which are analogous to an electrical circuit:

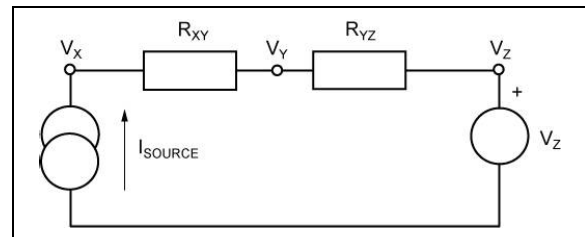


FIGURE 4-2: Simple Electrical Circuit.

From this simple circuit we can calculate V_X if we know I_{SOURCE} , V_Z and the resistor values R_{XY} and R_{YZ} using Equation 4-1:

EQUATION 4-1:

$$V_X = I_{SOURCE} \times (R_{XY} + R_{YZ}) + V_Z$$

Thermal circuits can be considered using these same rules and can be drawn similarly by replacing current sources with power dissipation (in watts), resistance with thermal resistance (in °C/W) and voltage sources with temperature (in °C).

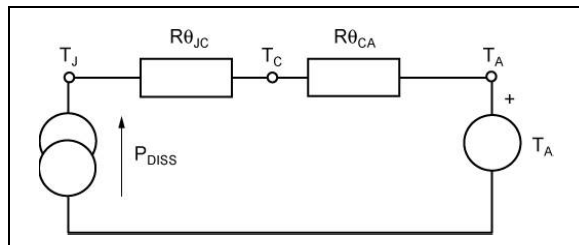


FIGURE 4-3: Simple Thermal Circuit.

Replacing the variables in the equation for V_X , one can find the junction temperature (T_J) from power dissipation, ambient temperature, and the known thermal resistance of the PCB ($R_{\theta_{CA}}$) and the package ($R_{\theta_{JC}}$).

P_{DISS} is calculated as $I_{SW}^2 \times R_{SW(MAX)}$. $R_{\theta_{JC}}$ is found in the operating ratings section of the data sheet and $R_{\theta_{CA}}$ (the PCB thermal resistance) values for various PCB copper areas is discussed in the document Designing with Low Dropout Voltage Regulators.

Example:

A switch is intended to drive a 3A load and is placed on a printed circuit board which has a ground plane area of at least 25 mm × 25 mm (625 mm²). The voltage source is a Li-ion battery with a lower operating threshold of 3V and the ambient temperature of the assembly can be up to 80°C.

Summary of variables:

- $I_{SW} = 3A$
- $V_{IN} = 3V$ to 4.2V
- $T_A = 80^\circ C$
- $R_{\theta_{JA}} = 108^\circ C/W$
- $P_{DISS} = I_{SW}^2 \times R_{SW}$

The worst case switch resistance (R_{SW}) at the lowest V_{IN} of 3V is not available in the data sheet, so the next lower value of V_{IN} is used, as shown in Equation 4-2:

EQUATION 4-2:

$$R_{SW@2.7V} = 21m\Omega$$

If this were a figure for worst case R_{SW} for 25°C, an additional consideration is to allow for the maximum junction temperature of 125°C, in this case can be 30% higher Figure 2-1:

EQUATION 4-3:

$$R_{SW(MAX)} = 27m\Omega$$

Therefore:

$$T_{RISE} = ((3A)^2 \times 27m\Omega) \times 108^\circ C/W = 26.2^\circ C$$

$$T_J = T_{RISE} + T_A = 26.2^\circ C + 80^\circ C = 106.2^\circ C$$

This is below the maximum of 125°C.

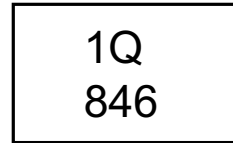
5.0 PACKAGING INFORMATION

5.1 Package Marking Information

6-Ball WLCSP*



Example



Note: The two-character code in the first line of the package marking is different for each device. MIC94161 (1Q), MIC94162 (2Q), MIC94163 (3Q), MIC94164 (4Q), and MIC94165 (ZQ).

Legend:	XX...X	Product code or customer-specific information
	Y	Year code (last digit of calendar year)
	YY	Year code (last 2 digits of calendar year)
	WW	Week code (week of January 1 is week '01')
	NNN	Alphanumeric traceability code
	(e3)	Pb-free JEDEC® designator for Matte Tin (Sn)
	*	This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package.
	●, ▲, ▼	Pin one index is identified by a dot, delta up, or delta down (triangle mark).
Note:	In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.	
	Underbar (_) and/or Overbar (¯) symbol may not be to scale.	

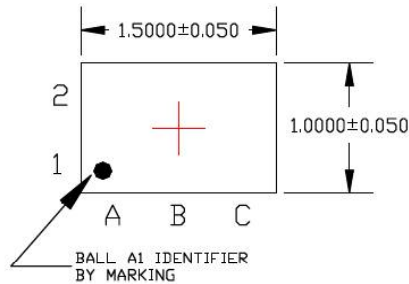
MIC94161/2/3/4/5

6-Ball WLCSP Package Outline and Recommended Land Pattern

TITLE

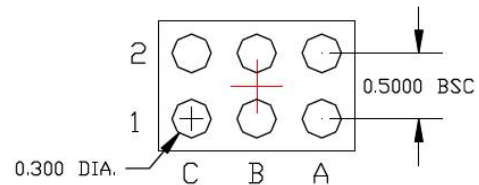
6 BALL WLCSP 1.5x1.0mm PACKAGE OUTLINE & RECOMMENDED LAND PATTERN

DRAWING #	WLCSP1510D-6BL-PL-9	UNIT	MM
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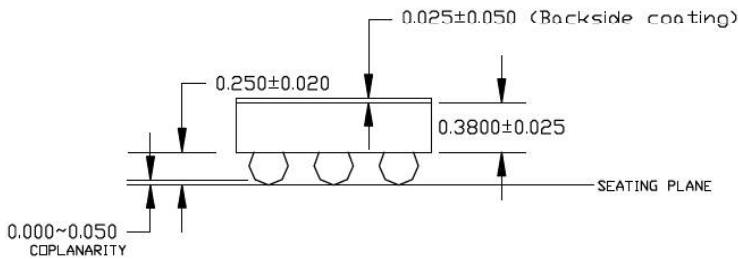
TOP VIEW

NOTE: 1, 2



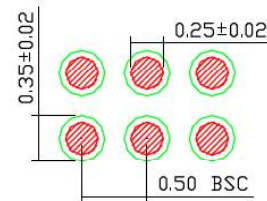
BOTTOM VIEW

NOTE: 1, 2



SIDE VIEW

NOTE: 1, 2



RECOMMENDED LAND PATTERN

NOTE: 3, 4

NOTE:

1. MAX PACKAGE WARPAGE IS 0.05 MM
2. MAX ALLOWABLE BURR IS 0.076MM IN ALL DIRECTIONS
3. NON-SOLDERMASK DEFINED PADS ARE RECOMMENDED FOR BOARD LAYOUT
4. SHADED RED CIRCLES REPRESENT CONTACT PAD AREA, GREEN CIRCLES REPRESENT SOLDER MASK OPENING

Note: For the most current package drawings, please see the Microchip Packaging Specification located at <http://www.microchip.com/packaging>.

APPENDIX A: REVISION HISTORY

Revision A (October 2020)

- Converted Micrel document MIC94161/2/3/4/5 to Microchip data sheet template DS20006439A.
- Minor grammatical text changes throughout.

MIC94161/2/3/4/5

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

<u>Device</u>	<u>X</u>	<u>XX</u>	<u>-XX</u>	Examples:
Part No.	Temperature Range	Package	Media Type	
Device:	MIC94161:	3A High-Side Load Switch with Reverse Blocking, OVP, and Soft-Start		a) MIC94161YCS-TR: MIC94161, -40°C to +125°C, 6-Ball WLCSP, 3,000/Reel
	MIC94162:	3A High-Side Load Switch with Reverse Blocking, Ultra-Fast Turn-On, and Auto-Discharge		b) MIC94162YCS-TR: MIC94162, -40°C to +125°C, 6-Ball WLCSP, 3,000/Reel
	MIC94163:	3A High-Side Load Switch with Reverse Blocking and Ultra-Fast Turn-On		c) MIC94163YCS-TR: MIC94163, -40°C to +125°C, 6-Ball WLCSP, 3,000/Reel
	MIC94164:	3A High-Side Load Switch with Reverse Blocking, Soft-Start, and Auto-Discharge		d) MIC94164YCS-TR: MIC94164, -40°C to +125°C, 6-Ball WLCSP, 3,000/Reel
	MIC94165:	3A High-Side Load Switch with Reverse Blocking and Soft-Start		e) MIC94165YCS-TR: MIC94165, -40°C to +125°C, 6-Ball WLCSP, 3,000/Reel
Temperature Range:	Y =	-40°C to +125°C		Note 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.
Package:	CS =	6-Ball 1.5 mm x 1.0 mm WLCSP		
Media Type:	TR =	3,000/Reel		

MIC94161/2/3/4/5

NOTES:

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specifications contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is secure when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods being used in attempts to breach the code protection features of the Microchip devices. We believe that these methods require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Attempts to breach these code protection features, most likely, cannot be accomplished without violating Microchip's intellectual property rights.
- Microchip is willing to work with any customer who is concerned about the integrity of its code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of its code. Code protection does not mean that we are guaranteeing the product is "unbreakable." Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

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