

# Product Change Notification - SYST-11SRZW931

### Date:

15 Jun 2020

Product Category:

Switching Regulators

# Affected CPNs:

**7** 

# Notification subject:

Data Sheet - MIC2288 - 1A 1.2MHz PWM Boost Converter in Thin SOT23 and DFN Packages

# Notification text:

SYST-11SRZW931 Microchip has released a new Product Documents for the MIC2288 - 1A 1.2MHz PWM Boost Converter in Thin SOT23 and DFN Packages of devices. If you are using one of these devices please read the document located at <u>MIC2288 - 1A 1.2MHz PWM Boost</u> <u>Converter in Thin SOT23 and DFN Packages</u>.

# Notification Status: Final

**Description of Change:** The following is the list of modifications:  $\&\mu \delta \delta \sigma \tau$ ; Updated part numbers C2 and L1 (Figure 6-1).  $\&\mu \delta \delta \sigma \tau$ ; Updated part number L1 (Figure 6-3).  $\&\mu \delta \delta \sigma \tau$ ; Updated part number C2 (Figure 6-5).  $\&\mu \delta \delta \sigma \tau$ ; Updated part numbers C2 and L1 (Figure 6-8). **Impacts to Data Sheet:** None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

# Date Document Changes Effective: 15 Jun 2020

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.

# Markings to Distinguish Revised from Unrevised Devices: N/A Attachment(s):

MIC2288 - 1A 1.2MHz PWM Boost Converter in Thin SOT23 and DFN Packages

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# **MIC2288**

# 1A, 1.2 MHz PWM Boost Converter in Thin SOT23 and DFN Packages

## Features

- 2.5V to 10V Input Voltage Range
- Output Voltage Adjustable to 34V
- Over 1A Switch Current
- 1.2 MHz PWM Operation
- Stable with Ceramic Capacitors
- <1% Line and Load Regulation
- Low Output Voltage Ripple
- <1 µA Shutdown Current</li>
- Undervoltage Lockout
- Output Overvoltage Protection (MIC2288YML)
- Overtemperature Shutdown
- Thin 5-Lead SOT23 Package Option
- 2 mm x 2 mm Leadless 8-Lead DFN Package Option
- -40°C to +125°C Junction Temperature Range

### Applications

- Organic EL Power Supply
- TFT-LCD Bias Supply
- 12V Supply for DSL Applications
- Multi-Output DC/DC Converters
- Positive and Negative Output Regulators
- SEPIC Converters

# **Package Types**

#### **MIC2288 MIC2288** 8-Lead DFN (ML) 5-Lead Thin SOT23 (D5) (Top View) (Top View) GND SW FB OVP 8 PGND 3 2 11 71SW VIN 6 FB ΕN AGND EΡ INC 5 4 ΕN VIN

# General Description

The MIC2288 is a 1.2 MHz PWM, DC/DC boost switching regulator available in low profile Thin SOT23 and 2 mm x 2 mm DFN package options. High-power density is achieved with the MIC2288 device's internal 34V/1A switch, allowing it to power large loads in a tiny footprint.

The MIC2288 implements a constant frequency, 1.2 MHz PWM, Current-mode control scheme with internal compensation that offers excellent transient response and output regulation performance. The high-frequency operation saves board space by allowing small, low profile, external components. The fixed frequency PWM topology also reduces spurious switching noise and ripple to the input power source.

The MIC2288 is available in a low profile 5-lead Thin SOT23 package and a 2 mm x 2 mm 8-lead DFN lead-less package. The DFN package option has an output overvoltage protection feature.

The MIC2288 has a junction temperature range of -40°C to +125°C.

# **Typical Application Circuit**



# **Functional Block Diagram**



# 1.0 ELECTRICAL CHARACTERISTICS

# Absolute Maximum Ratings†

Supply Voltage (V <sub>IN</sub> )	+12V
Switch Voltage (V <sub>SW</sub> )	
Enable Pin Voltage (V <sub>FN</sub> )	
FB Voltage (V <sub>FB</sub> )	+6.0V
Switch Current (I <sub>SW</sub> )	2A
ESD Rating (Note 1)	

# Operating Ratings<sup>††</sup>

Supply Voltage (V <sub>IN</sub> )+2.	.5V to +10V
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- **†** Notice: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.
- **†† Notice:** The device is not ensured to function outside its operating ratings.
  - Note 1: Devices are ESD-sensitive. Handling precautions are recommended. Human body model, 1.5 k $\Omega$  in series with 100 pF.

# **ELECTRICAL CHARACTERISTICS**

Parameter	Sym.	±125°C. <sup>(1)</sup> n. Min. Typ. Max.		Units	Conditions	
	-					
Supply Voltage Range	V <sub>IN</sub>	2.5		10	V	
Undervoltage Lockout	V <sub>UVLO</sub>	1.8	2.1	2.4	V	
Quiescent Current	I <sub>VIN</sub>	—	2.8	5	mA	V <sub>FB</sub> = 2V, not switching
Shutdown Current	I <sub>SD</sub>	—	0.1	1	μA	V <sub>EN</sub> = 0V (Note 2)
Feedback Voltage	V <sub>FB</sub>	1.227	1.24	1.252	V	±1%
		1.215	_	1.265		±2%, overtemperature
Feedback Input Current	I <sub>FB</sub>	_	-450	_	nA	V <sub>FB</sub> = 1.24V
Line Regulation	_	_	0.1	1	%	$3V \le V_{IN} \le 5V$
Load Regulation	_	—	0.2	_	%	5 mA ≤ I <sub>OUT</sub> ≤ 40 mA
Maximum Duty Cycle	D <sub>MAX</sub>	85	90	_	%	
Switch Current Limit	lsw	_	1.2	_	А	
Switch Saturation Voltage	V <sub>SW</sub>	—	550	—	mV	I <sub>SW</sub> = 1A
Switch Leakage Current	I <sub>sw</sub>	—	0.01	5	μA	V <sub>EN</sub> = 0V, V <sub>SW</sub> = 10V
Enable Threshold	V <sub>EN</sub>	1.5	_		V	Turn on
		—		0.4		Turn off
Enable Pin Current	I <sub>EN</sub>	—	20	40	μA	V <sub>EN</sub> = 10V
Oscillator Frequency	f <sub>SW</sub>	1.05	1.2	1.35	MHz	
Output Overvoltage Protection	V <sub>OVP</sub>	30	32	34	V	DFN package option only
Overtemperature	ТJ	—	150		°C	
Threshold Shutdown	-		10	_		Hysteresis

Note 1: Specification for packaged product only.

2: I<sub>SD</sub> = I<sub>VIN</sub>.

# TEMPERATURE SPECIFICATIONS<sup>(1)</sup>

Parameters	Sym.	Min.	Тур.	Max.	Units	Conditions
Temperature Ranges						
Junction Operating Temperature	ТJ	-40	_	+125	°C	
Storage Temperature Range	Τs	-65	_	+150	°C	
Package Thermal Resistances						
Thermal Resistance, 2x2 8-Lead DFN	$\theta_{JA}$		93	_	°C/W	
Thermal Resistance, 5-Lead TSOT23	θ <sub>JA</sub>		256	_	°C/W	

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the th1l resistance from junction to air (i.e., T<sub>A</sub>, T<sub>J</sub>, θ<sub>JA</sub>). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum +125°C rating. Sustained junction temperatures above +125°C can impact the device reliability.

#### 2.0 TYPICAL PERFORMANCE CURVES

The graphs and tables provided following this note are a statistical summary based on a limited number of Note: samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.





FIGURE 2-4: Current Limit vs. Supply Voltage.



FIGURE 2-5: Temperature.

Current Limit vs.



Voltage.

Switch Saturation vs. Supply



FIGURE 2-7: Current.



FIGURE 2-8: Temperature.



FIGURE 2-9:

Frequency vs. Temperature.



FIGURE 2-10: Maximum Duty Cycle vs. Supply Voltage.



FIGURE 2-11: Maximum Duty Cycle vs. Temperature.



**FIGURE 2-12:** FB Pin Current vs. Temperature.



FIGURE 2-13: Enable Characteristics.



FIGURE 2-14:

Line Transient Response.



FIGURE 2-15: Load Transient Response.



FIGURE 2-16: Output Voltage Ripple and Switching Waveforms.

# 3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

Pin Number 5-Lead TSOT23	Pin Number 8-Lead DFN	Pin Name	Description
1	7	SW	Switch Node (Input): Internal power bipolar collector.
2	—	GND	Ground (Return): Ground.
3	6	FB	Feedback (Input): 1.24V output voltage sense node.
4	3	EN	Enable (Input): Logic high enables regulator. Logic low shuts down regulator. Do not leave floating.
5	2	VIN	Supply (Input): 2.5V to 10V input voltage.
_	1	OVP	Output Overvoltage Protection (Input): Tie this pin to V <sub>OUT</sub> to clamp the output voltage to 34V maximum in Fault conditions. Tie this pin to ground if OVP function is not required.
_	5	NC	No Connect: No internal connection to die.
	4	AGND	Analog ground.
	8	PGND	Power ground.
	EP	GND	Exposed backside pad.

# 4.0 FUNCTIONAL DESCRIPTION

The MIC2288 is a constant frequency, PWM Current-mode boost regulator. See the **Functional Block Diagram**. The MIC2288 is composed of an oscillator, slope compensation ramp generator, current amplifier, g<sub>m</sub> error amplifier, PWM generator and a 1A bipolar output transistor. The oscillator generates a 1.2 MHz clock. The clock's two functions are to trigger the PWM generator that turns on the output transistor and to reset the slope compensation ramp generator. The current amplifier is used to measure the switch current by amplifying the voltage signal from the internal sense resistor. The output of the current amplifier is summed with the output of the slope compensation ramp generator. This summed current loop signal is fed to one of the inputs of the PWM generator. The  $g_m$  error amplifier measures the feedback voltage through the external feedback resistors, and amplifies the error between the detected signal and the 1.24V reference voltage. The output of the  $g_m$  error amplifier provides the voltage loop signal that is fed to the other input of the PWM generator. When the current loop signal exceeds the voltage loop signal, the PWM generator turns off the bipolar output transistor. The next clock period initiates the next switching cycle, maintaining the constant frequency Current-mode PWM control.

# 5.0 APPLICATION INFORMATION

# 5.1 DC/DC PWM Boost Conversion

The MIC2288 is a constant frequency boost converter. It operates by taking a DC input voltage and regulating a higher DC output voltage. Figure 5-1 shows a typical circuit. Boost regulation is achieved by turning on an internal switch, which draws current through the inductor (L1). When the switch turns off, the inductor's magnetic field collapses, causing the current to be discharged into the output capacitor through an external Schottky diode (D1). Voltage regulation is achieved by modulating the pulse width or Pulse-Width Modulation (PWM).



# 5.2 Duty Cycle Considerations

Duty cycle refers to the switch on-to-off time ratio and can be calculated as follows for a boost regulator:

# **EQUATION 5-1:**

$$D = 1 - \frac{V_{IN}}{V_{OUT}}$$

The duty cycle required for voltage conversion should be less than the maximum duty cycle of 85%. Also, in light load conditions where the input voltage is close to the output voltage, the minimum duty cycle can cause pulse skipping. This is due to the energy stored in the inductor causing the output to overshoot slightly over the regulated output voltage. During the next cycle, the error amplifier detects the output as being high and skips the following pulse. This effect can be reduced by increasing the minimum load or by increasing the inductor value. Increasing the inductor value reduces peak current, which in turn, reduces energy transfer in each cycle.

# 5.3 Overvoltage Protection

For the DFN package option, there is an overvoltage protection function. If the feedback resistors are disconnected from the circuit, or the feedback pin is shorted to ground, the feedback pin will fall to ground potential. This will cause the MIC2288 to switch at full duty cycle in an attempt to maintain the feedback voltage. As a result, the output voltage will climb out of control. This may cause the switch node voltage to exceed its maximum voltage rating, possibly damaging the IC and the external components. To ensure the highest level of protection, the MIC2288 OVP pin will shut the switch off when an overvoltage condition is detected, saving the regulator and other sensitive circuitry downstream.

# 5.4 Component Selection

# 5.4.1 INDUCTOR

Inductor selection is a balance between efficiency, stability, cost, size and rated current. For most applications, 10  $\mu$ H is the recommended inductor value. It is usually a good balance between these considerations.

Larger inductance values reduce the peak-to-peak ripple current, affecting efficiency. This has the effect of reducing both the DC losses and the transition losses. There is also a secondary effect of an inductor's DC Resistance (DCR). The DCR of an inductor will be higher for more inductance in the same package size. This is due to the longer windings required for an increase in inductance. Because the majority of input current (minus the MIC2288 operating current) is passed through the inductor, higher DCR inductors will reduce efficiency.

To maintain stability, increasing the inductor value will have to be associated with an increase in output capacitance. This is due to the unavoidable "right half plane zero" effect for the continuous current boost converter topology. The frequency at which the right half plane zero occurs can be calculated as follows:

### **EQUATION 5-2:**

$$f_{RHPZ} = \frac{V_{IN}^{2}}{V_{OUT} \times L \times I_{OUT} \times 2\pi}$$

The right half plane zero has the undesirable effect of increasing gain, while decreasing phase. This requires that the loop gain is rolled off before this has significant effect on the total loop response. This can be accomplished by either reducing inductance (increasing RHPZ frequency) or increasing the output capacitor value (decreasing loop gain).

# 5.4.2 OUTPUT CAPACITOR

Output capacitor selection is also a trade-off between performance, size and cost. Increasing output capacitance will lead to an improved transient response, but also an increase in size and cost. X5R or X7R dielectric ceramic capacitors are recommended for designs with the MIC2288. Y5V values may be used, but to compensate their drift over temperature, more capacitance is required. The following table shows the recommended ceramic (X5R) output capacitor value vs. output voltage.

# TABLE 5-1:OUTPUT CAPACITORSELECTION

Output Voltage	Recommended Output Capacitance
<6V	22 µF
<16V	10 µF
<34V	4.7 µF

## 5.4.3 DIODE SELECTION

The MIC2288 requires an external diode for operation. A Schottky diode is recommended for most applications due to their lower forward voltage drop and reverse recovery time. Ensure the diode selected can deliver the peak inductor current and the maximum reverse voltage is rated greater than the output voltage.

# 5.4.4 INPUT CAPACITOR

A minimum 1  $\mu$ F ceramic capacitor is recommended for designing with the MIC2288. Increasing input capacitance will improve performance and greater noise immunity on the source. The input capacitor should be as close as possible to the inductor and the MIC2288, with short traces for good noise performance.

# 5.4.5 FEEDBACK RESISTORS

The MIC2288 utilizes a feedback pin to compare the output to an internal reference. The output voltage is adjusted by selecting the appropriate feedback resistor network values. The R2 resistor value must be less than or equal to 5 k $\Omega$  (R2  $\leq$  5 k $\Omega$ ). The desired output voltage can be calculated as follows:

## **EQUATION 5-3:**

$$V_{OUT} = V_{REF} \times \left(\frac{R1}{R2} + 1\right)$$

Where: V<sub>RFF</sub> = 1.24V

# 6.0 APPLICATION CIRCUITS



@ 400 mA.

Ref	Description	Part Number	Vendor
C1	4.7 μF, 6.3V, 0805, X5R, Cer Cap	08056D475MAT	AVX
C2	22 µF, 6.3V, 0805, X5R, Cer Cap	12066D226KAT	AVX
D1	1A, 40V, Schottky Diode	MBRM140T3	On <sup>®</sup> Semi.
L1	4.7 μH, 650 mA, Inductor	LQH32CN4R7M33L	Murata



**FIGURE 6-2:** 3.0V to 4.2V V<sub>IN</sub> to 9 V<sub>OUT</sub> @ 180 mA.

Ref	Description	Part Number	Vendor
C1	2.2 μF, 10V, 0805, X5R, Cer Cap	08052D225KAT	AVX
C2	10 μF, 16V, 1206, X5R, Cer Cap	1206YD106MAT	AVX
D1	1A, 40V, Schottky Diode	MBRM140T3	On <sup>®</sup> Semi.
L1	10 μH, 650 mA, Inductor	LQH43CN100K03	Murata



**FIGURE 6-3:** 3.0V to 4.2V V<sub>IN</sub> to 12 V<sub>OUT</sub> @ 100 mA.

Ref	Description	Part Number	Vendor
C1	4.7 μF, 6.3V, 0805, X5R, Cer Cap	08056D475MAT	AVX
C2	10 µF, 16V, 1206, X5R, Cer Cap	1206YD106MAT	AVX
D1	1A, 40V, Schottky Diode	MBRM140T3	On <sup>®</sup> Semi.
L1	10 μH, 650 mA, Inductor	LQH43CN100K03	Murata



**FIGURE 6-4:** 3.0V to 4.2V V<sub>IN</sub> to 15 V<sub>OUT</sub> @ 100 mA.

Ref	Description	Part Number	Vendor
C1	2.2 µF, 10V, 0805, X5R, Cer Cap	08052D225KAT	AVX
C2	10 μF, 16V, 1206, X5R, Cer Cap	1206YD106MAT	AVX
D1	1A, 40V, Schottky Diode	MBRM140T3	On <sup>®</sup> Semi.
L1	10 μH, 650 mA, Inductor	LQH43CN100K03	Murata



**FIGURE 6-5:** 3.0V to 4.2V V<sub>IN</sub> to 24 V<sub>OUT</sub> @ 50 mA.

Ref	Description	Part Number	Vendor
C1	2.2 μF, 10V, 0805, X5R, Cer Cap	08052D225KAT	AVX
C2	4.7 μF, 25V, 1206, X5R, Cer Cap	12063D475KAT	AVX
D1	1A, 40V, Schottky Diode	MBRM140T3	On <sup>®</sup> Semi.
L1	10 µH, 650 mA, Inductor	LQH43CN100K03	Murata



FIGURE 6-6:

5 V<sub>IN</sub> to 9 V<sub>OUT</sub> @ 330 mA.

Ref	Description	Part Number	Vendor	
C1	2.2 µF, 10V, 0805, X5R, Cer Cap	08052D225KAT	AVX	
C2	10 μF, 16V, 1206, X5R, Cer Cap	1206YD106MAT	AVX	
D1	1A, 40V, Schottky Diode	MBRM140T3	On <sup>®</sup> Semi.	
L1	10 μH, 650 mA, Inductor	LQH43CN100K03	Murata	



FIGURE 6-7: 5 V<sub>IN</sub> to 12 V<sub>OUT</sub> @ 250 mA.

Ref	Description	Part Number	Vendor	
C1	2.2 μF, 10V, 0805, X5R, Cer Cap	08052D225KAT	AVX	
C2	10 μF, 16V, 1206, X5R, Cer Cap	1206YD106MAT	AVX	
D1	1A, 40V, Schottky Diode	MBRM140T3	On <sup>®</sup> Semi.	
L1	10 µH, 650 mA, Inductor	LQH43CN100K03	Murata	



FIGURE 6-8:

5 V<sub>IN</sub> to 24 V<sub>OUT</sub> @ 80 mA.

Ref	Description	Part Number	Vendor	
C1	2.2 μF, 10V, 0805, X5R, Cer Cap	08052D225KAT	AVX	
C2	4.7 μF, 25V, 1206, X5R, Cer Cap	12063D475KAT	AVX	
D1	1A, 40V, Schottky Diode	MBRM140T3	On <sup>®</sup> Semi.	
L1	10 μH, 650 mA, Inductor	LQH43CN100K03	Murata	

# 7.0 PACKAGING INFORMATION

# 7.1 Package Marking Information



Legend:	Y YY WW NNN (e3) *	Product code or customer-specific information Year code (last digit of calendar year) Year code (last 2 digits of calendar year) Week code (week of January 1 is week '01') Alphanumeric traceability code Pb-free JEDEC <sup>®</sup> designator for Matte Tin (Sn) This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package. Pin one index is identified by a dot, delta up, or delta down (triangle
t d	be carried characters the corpora	t the full Microchip part number cannot be marked on one line, it will over to the next line, thus limiting the number of available for customer-specific information. Package may or may not include ate logo. _) and/or Overbar ( <sup>-</sup> ) symbol may not be to scale.

# 5-Lead Thin SOT23 Package Outline and Recommended Land Pattern





# 8-Lead 2 mm x 2 mm Thin DFN Package Outline and Recommended Land Pattern

# **MIC2288**

NOTES:

# APPENDIX A: REVISION HISTORY

# Revision C (June 2020)

The following is the list of modifications:

- Updated part numbers C2 and L1 (Figure 6-1).
- Updated part number L1 (Figure 6-3).
- Updated part number C2 (Figure 6-5).
- Updated part numbers C2 and L1 (Figure 6-8).

# **Revision B (September 2018)**

• Updated values for C2 in the table beneath Figure 6-3.

# Revision A (May 2018)

- Converted Micrel document MIC2288 to Microchip data sheet template DS20006034A.
- Minor grammatical text changes throughout.
- Updated Low Output Voltage Ripple in Features.
- Added clarification to EN description in Table 3-1.
- Updated drawing for EN in Figure 5-1.
- Updated drawings and figure captions for each entry in Section 6.0 "Application Circuits".

# **MIC2288**

NOTES:

# **PRODUCT IDENTIFICATION SYSTEM**

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

					Exampl	es:			
Device Part No.	<u>X</u> Junction Temp. Range	<u>XX</u> Package	- <u>XX</u> Media Type		a) MIC2:	288YD5-TX:	MIC2288, -40°C to +125°C Temperature Range, 5-Lead TSOT23, 3,000/Reel (Reverse T/R)		
Device: Junction	MIC2288:	1A, 1.2 MI	Hz PWM Boost Convert	er	b) MIC2	288YD5-TR:	MIC2288, -40°C to +125°C Temperature Range, 5-Lead TSOT23, 3,000/Reel		
Temperature Range:	Y =	-40°C to +125°C	, RoHS-Compliant		c) MIC22	288YML-TR:	MIC2288, -40°C to +125°C Temperature Range, 8-Lead TDFN, 5,000/Reel		
Package:	D5 = ML =	5-Lead Thin SO 8-Lead 2 mm x 2			Note 1:	catalog part number description. This identifier is used for ordering purposes and is not printed on			
Media Type:	TX = TR = TR =	3,000/Reel (Rev 3,000/Reel (TSC 5,000/Reel (TDF					kage. Check with your Microchip r package availability with the Tape n.		

# **MIC2288**

NOTES:

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Advance Information

SYST-11SRZW931 - Data Sheet - MIC2288 - 1A 1.2MHz PWM Boost Converter in Thin SOT23 and DFN Pa

Affected Catalog Part Numbers(CPN)

MIC2288YD5-TR MIC2288YD5-TX MIC2288YML-TR