

PCN # 1702R

DATE: May 1, 2020

EXPECTED PCN SHIP DATE: May 1, 2020



Quality Assurance  
160 Rio Robles  
San Jose, CA 95134

www.maximintegrated.com

PROCESS CHANGE NOTICE  
 PRODUCT CHANGE NOTICE

MAXIM INTEGRATED HEREBY ISSUES NOTIFICATION OF CHANGE  
THAT MAY AFFECT THE FOLLOWING CATEGORIES:

DESIGN     WAFER FAB     ASSEMBLY     TEST     ELEC/MECH SPECS

AFFECTED PRODUCT:

Ordering P/N: (See PN listing XLS in PCN ZIP file)

CHANGE FROM: - Maxim products in SOIC(N) package manufactured at current subcontractor	CHANGE TO: - Additional Assembler Greatek in Taiwan/R.O.C.
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JUSTIFICATION: -  
 Adding Greatek to expand Maxim's Supply-Chain to meet capacity demands, flexibility and on-time delivery.  
 Greatek is an established assembly subcontractor and is certified under QS 9000, ISO/TS 16949, ISO 14001.  
 Qualification results are reflected in Maxim's Reliability report attached (R29114FQ).  
 There are no regulatory compliance changes to the material content of the devices.  
 There are no changes to the form, fit, function of the devices.

TRACEABILITY: Maxim Integrated maintains full traceability by device marking, packaging labels and shipment documents.

Maxim Integrated's Change Notification System is designed to keep our customer base apprised of major product, manufacturing, or facility improvements.

*Nasser Ali Chaouche*

Nasser AliChaouche / PCN Coordinator

For further information, please contact either of the people listed below.

Contact your local Maxim Integrated Company Representative    or    Nasser AliChaouche, PCN Coordinator  
408-601-5660 / pcn.coordinator@maximintegrated.com

## 1) PURPOSE

To qualify assembler Greatek to build SOIC(N) packages with 0.8/1.0/1.3/2.0 mil Au-wire

## QUALIFICATION REQUIREMENTS AND RESULTS

Rel#	R29114A	R29114C	R29114D		
Lot#	JQ3AHA023DC	JQ3AHA023DE	JQ3AHA023DG		
Device:	MAX1709ESE+	MAX1709ESE+	MAX1709ESE+		
Die Type:	PX35Y	PX35Y	PX35Y		
Die Size (mils)	86X193 mil	86X193 mil	86X193 mil		
Package Type (code):	S16+8	S16+8	S16+8		
Date Code:	1738	1738	1738		
Topmark:	ZQ3A JCF	ZQ3A JCF	ZQ3A JCF		
Stress Test	Duration	Sampling Plan	Result	Result	Result
Convection Reflow <sup>*2,3</sup> 260°C Peak	MSL 1, 3X	0/400	0/399	0/400	0/400
HAST 130°C / 85% R.H. <sup>*1,2,3</sup>	96 hrs.	0/77	0/77	0/77	0/77
Unbiased HAST 130°C / 85% R.H. <sup>*1,2</sup>	96 hrs.	0/77	0/77	0/77	0/77
Temperature Cycle <sup>*1,2,3</sup> -65°C to 150°C (Condition C)	500 cyc	0/77	0/77	0/77	0/77
High Temperature Storage 150°C <sup>*1,2,3</sup>	1000 hrs.	0/77	0/77	0/77	0/77
HTOL <sup>*2,3,4</sup>	1000 hrs	0/77		0/77	
C-SAM*1	T0, Post-Precon	0/25	0/25	0/25	0/25
Wire Bond Pull Minimum 5 grams-force	T (0), Post-TC	0/200 wires	0/200wires	0/200wires	0/200wires
Solderability (Lead-Free,245C)	T (0)	0/15	0/15	0/15	0/15
Physical Dimension (PD)	T (0)	0/20	0/20	0/20	0/20
Bondcrater	Post-Precon	0/20	0/20	0/20	0/20
Solder Shock		0/15	0/15	0/15	0/15

Note:

- \*1. Convection reflow is used as preconditioning for SMD packages.
- \*2. Electrical tests pre- and post-stress were performed at +85°C.
- \*3. Electrical tests pre- and post-stress were performed at +25°C.
- \*4. Electrical tests pre- and post-stress were performed at -40°C.

Rel#	R29114E	R29114F	R29114G		
Lot#	J6Y5GA819FA	J6Y5GA819FB	N380B3108CA		
Device:	MAX5033BASA+	MAX5033BASA+	MAX494CSD+		
Die Type:	NP25U-5Z	NP25U-5Z	OX56Y		
Die Size (mils)	85 X 145	85 X 145	69 X 100		
Package Type (code):	S8+5	S8+5	S14+1		
Date Code:	1744	1744	1744		
Topmark:	Z DM	Z DM	Z380 NCX		
Stress Test	Duration	Sampling Plan	Result	Result	Result
Convection Reflow <sup>*2,3,5</sup> 260°C Peak	MSL 1, 3X	0/400	0/400	0/400	0/400
HAST 130°C / 85% R.H. <sup>*1,2,3,5</sup>	96 hrs.	0/77	0/77	0/77	0/77
Unbiased HAST 130°C / 85% R.H. <sup>*1,2</sup>	96 hrs.	0/77	0/77	0/77	0/77
Temperature Cycle <sup>*1,2,3,5</sup> -65°C to 150°C (Condition C)	500 cyc	0/77	0/77	0/77	0/77
High Temperature Storage 150°C <sup>*1,2,3</sup>	1000 hrs.	0/77	0/77	0/77	0/77
HTOL <sup>*2,3,4</sup>	1000 hrs	0/77	0/77		
C-SAM*1	T0, Post-Precon	0/25	0/25	0/25	0/25
Wire Bond Pull Minimum 5 grams-force	T (0), Post-TC	0/200 wires	0/200wires	0/200wires	0/200wires
Solderability (Lead-Free,245C)	T (0)	0/15	0/15	0/15	0/15
Physical Dimension (PD)	T (0)	0/20	0/20	0/20	0/20
Bondcrater	Post-Precon	0/20	0/20	0/20	0/20
Solder Shock		0/15	0/15	0/15	0/15

Note:

- \*1. Convection reflow is used as preconditioning for SMD packages.
- \*2. Electrical tests pre- and post-stress were performed at +125°C.
- \*3. Electrical tests pre- and post-stress were performed at +25°C.
- \*4. Electrical tests pre- and post-stress were performed at -40°C.
- \*5. Electrical tests pre- and post-stress were performed at +70°C.

<b>Rel#</b>			R29114K	R29114L
<b>Lot#</b>			JQ9CHA128JA	JQ9CHA128JB
<b>Device:</b>			MAX6198BESA+	MAX6198BESA+
<b>Die Type:</b>			RF23Z-2Z	RF23Z-2Z
<b>Die Size (mils)</b>			44X31	44X31
<b>Package Type (code):</b>			S8+2	S8+2
<b>Date Code:</b>			1742	1742
<b>Topmark:</b>			MAX6198BESA+ / 1742	MAX6198BESA+ / 1742
<b>Stress Test</b>	<b>Duration</b>	<b>Sampling Plan</b>	<b>Result</b>	<b>Result</b>
<b>Convection Reflow *2,3 260°C Peak</b>	MSL 1, 3X	0/400	0/400	0/400
<b>HAST 130°C / 85% R.H. *1,2,3</b>	96 hrs.	0/77	0/77	0/77
<b>Unbiased HAST 130°C / 85% R.H. *1,2</b>	96 hrs.	0/77	0/77	0/77
<b>Temperature Cycle *1,2,3 -65°C to 150°C (Condition C)</b>	500 cyc	0/77	0/77	0/77
<b>High Temperature Storage 150°C *1,2,3</b>	1000 hrs.	0/77	0/77	0/77
<b>HTOL *2,3,4</b>	1000 hrs	0/77	0/77	
<b>Solderability (Lead-Free,245C)</b>	-	0/15	0/15	0/15
<b>C-SAM*1</b>	-	0/25	0/25	0/25
<b>Wire Bond Pull Minimum 5 grams-force</b>	T (0), Post-TC	0/200 wires	0/200 wires	0/200 wires
<b>Physical Dimension (PD)</b>	T (0)	0/20	0/20	0/20
<b>Bondcrater</b>	Post-Precon	0/20	0/20	0/20
<b>Solder Shock</b>		0/15	0/20	0/20

**Note:**

- \*1. Convection reflow is used as preconditioning for SMD packages.
- \*2. Electrical tests pre- and post-stress were performed at +85°C.
- \*3. Electrical tests pre- and post-stress were performed at +25°C.
- \*4. Electrical tests pre- and post-stress were performed at -40°C.

## 2) CONCLUSION

Qualification lots assembled in Greatek have passed reliability qualification (Full Qualification Requirements / Acceptance Criteria). Therefore, assembler Greatek is fully qualified to build SOIC(N) packages with 0.8/1.0/1.3/2.0 mil Au-wire. These packages, as tested MSL1, are not moisture sensitive, therefore, requires no bake-and-bag precautions for shipment and/or storage.

## 3) Package Coverage

The following packages can be covered by this qualification result.

S14+1	S14+6	S16+5	S8+17	S8+20	S8+4
S14+4	S16+1	S16+6	S8+18	S8+21	S8+5
S14+5	S16+3	S16+8	S8+2	S8+22	

Affected product numbers	Customer   PCN Proposed Ship Date
ICL7662EBA+	1-May-20
ICM7555ESA+	1-May-20
ICM7555ESA+T	1-May-20
MAX1232CSA+	1-May-20
MAX1232CSA+T	1-May-20
MAX13089EASD+	1-May-20
MAX1483ESA+T	1-May-20
MAX1771CSA+	1-May-20
MAX1771CSA+T	1-May-20
MAX232ECSE+	1-May-20
MAX232ECSE+T	1-May-20
MAX250CSD+	1-May-20
MAX250CSD+T	1-May-20
MAX3070EESD+	1-May-20
MAX3070EESD+T	1-May-20
MAX3072EASA+	1-May-20
MAX3079EESD+	1-May-20
MAX3079EESD+T	1-May-20
MAX3082ESA+	1-May-20
MAX3082ESA+T	1-May-20
MAX3430CSA+	1-May-20
MAX3430CSA+T	1-May-20
MAX3441EASA+	1-May-20
MAX3442EASA+	1-May-20
MAX3442EESA+	1-May-20
MAX3442EESA+T	1-May-20
MAX3443EESA+T	1-May-20
MAX3462CSA+	1-May-20
MAX3485CSA+	1-May-20
MAX3490ECSA+	1-May-20
MAX483ECSA+	1-May-20
MAX483ECSA+T	1-May-20
MAX483ESA+	1-May-20
MAX485EESA+	1-May-20
MAX485EESA+T	1-May-20
MAX531ACSD+	1-May-20
MAX539BCSA+	1-May-20
MAX5541CSA+	1-May-20
MAX705CSA+	1-May-20
MAX705CSA+T	1-May-20
MAX706CSA+	1-May-20
MAX706SCSA+T	1-May-20
MAX708CSA+T	1-May-20
MAX708TCSA+	1-May-20
MAX7400ESA+	1-May-20
MAX764ESA+	1-May-20

MAX795SESA+	1-May-20
MAX907ESA+	1-May-20
MAX907ESA+T	1-May-20
MAX932ESA+T	1-May-20
MAX942CSA+T	1-May-20